

High Mobility Ge-Channel Formation By Localized>Selective Liquid Phase Epitaxy (LPE) Using Ge+B Plasma Ion Implantation And Laser Melt Annealing

IWJT-2013, June 6-7, 2013

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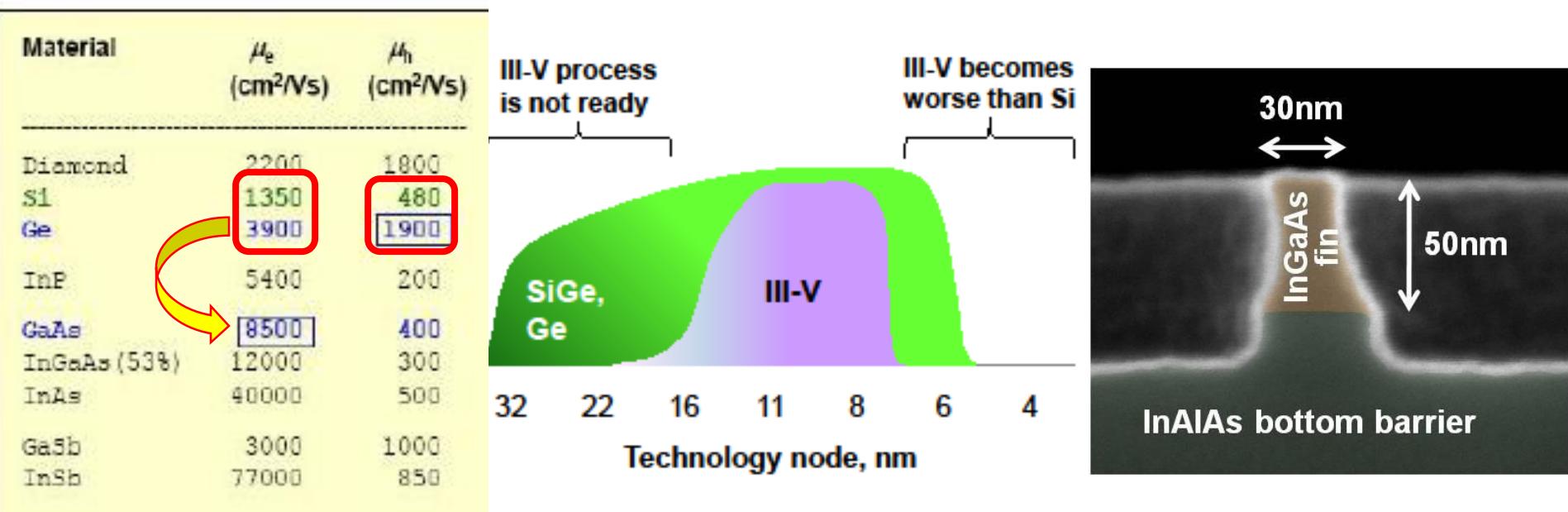
Outline

- Introduction
- Experimentation
 - **Micron** for Ge+B Plasma Implantation/Deposition Doping
 - **Innovavent**/Germany for 515nm laser annealing
 - **Excico**/France for 308nm laser annealing
- Results
 - **KT**/Shanghai for Hx-4PP and TW analysis
 - **EAG**/NJ for SIMS analysis
 - **CNSE**/Albany for SIMS, XRD & X-TEM analysis
 - **UCLA** differential Hall analysis for mobility and carrier density depth profiles
- Summary

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 - SiGe & Ge High Mobility Channel Formation Methods
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Opportunity Window for Non-Si Channel



- Any new technology has to last at least 2 nodes
- SiGe channel is easier to manufacture
- High Ge content SiGe or even pure Ge to follow
- III-V materials have a narrow opportunity window

Paper 13.5: A. Khakifirooz of IBM on “Strain Engineered Extremely Thin SOI (ETSOI) for High Performance CMOS”. Said ET-SOI needed for 20nm and beyond but ET-SOI is not compatible with eSiGe and eSiGe is limited with scaling they need something different. Finally shows a better way to integrate sSOI for nMOS with SiGe for pMOS by using an sSOI wafer with localized Ge condensation or thermal mixing for the pMOS as shown below. For 6nm ET-SOI they reported thermal mixing results in only 11% Ge while condensation method results in 23% Ge. This improves drive current by 35%.

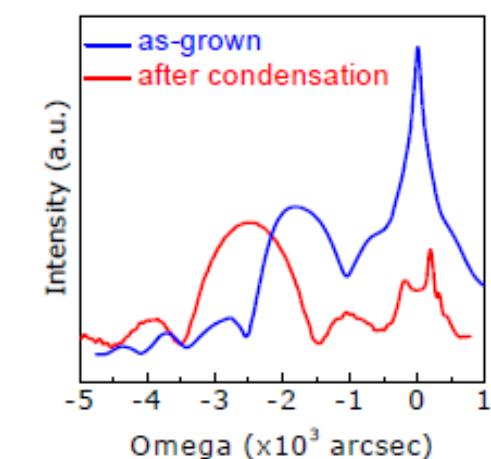
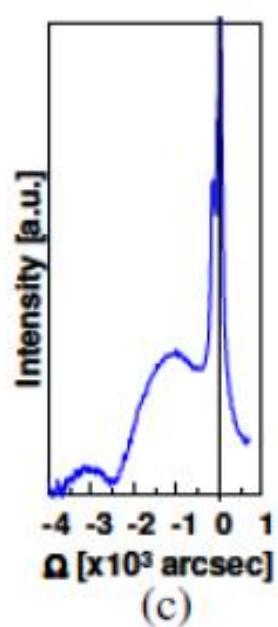
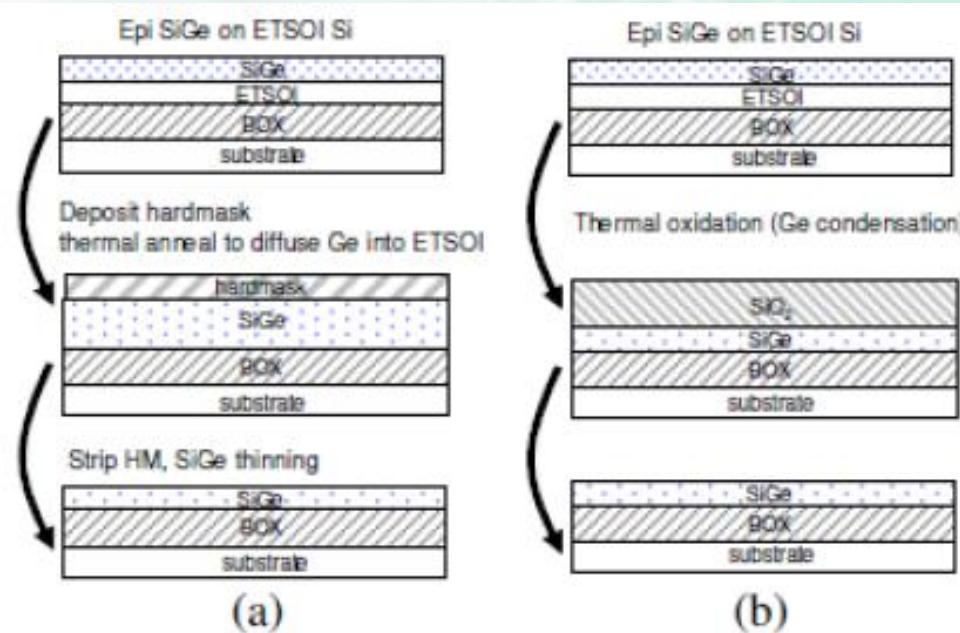


Fig. 6 Schematic process flow to form SiGe-channel ETSOI (a) thermal mixing (b) condensation. (c) HRXRD data showing compressively strained SGOI with 23% of Ge.

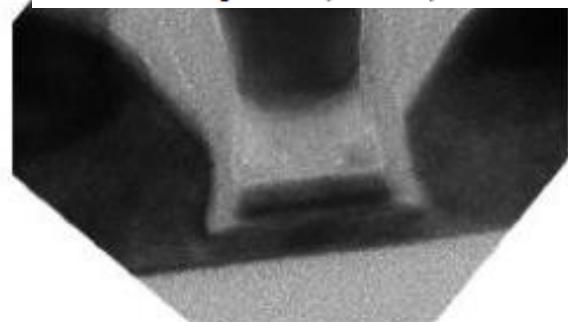


Fig. 6 Schematic process flow to form SiGe-channel ETSOI (a) thermal mixing (b) condensation. (c) HRXRD data showing compressively strained SGOI with 23% of Ge.

Fig. 7 TEM cross-section of SiGe channel ETSOI with a channel thickness of 6 nm.

SSDM-2011: Apple iPhone 4

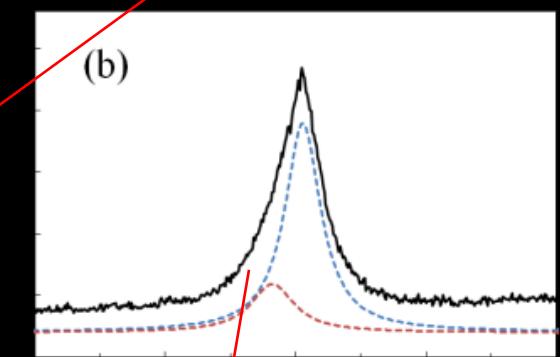
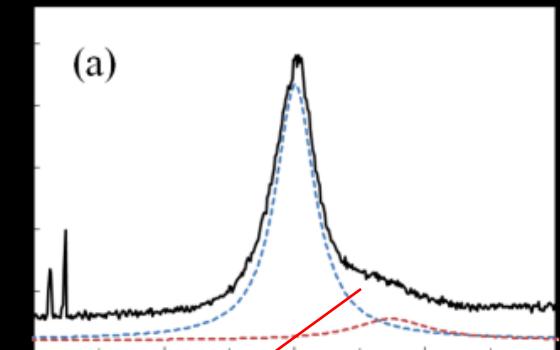
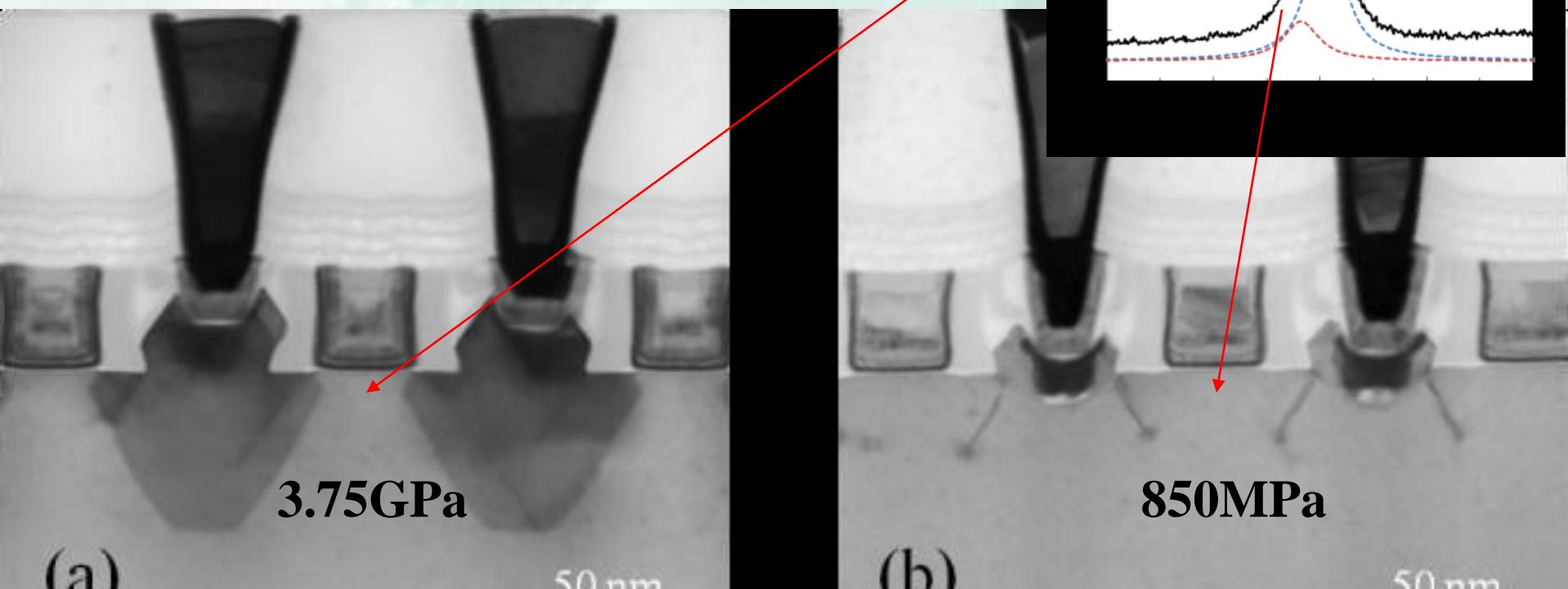
Channel strain measurements in 32nm-node CMOSFETs

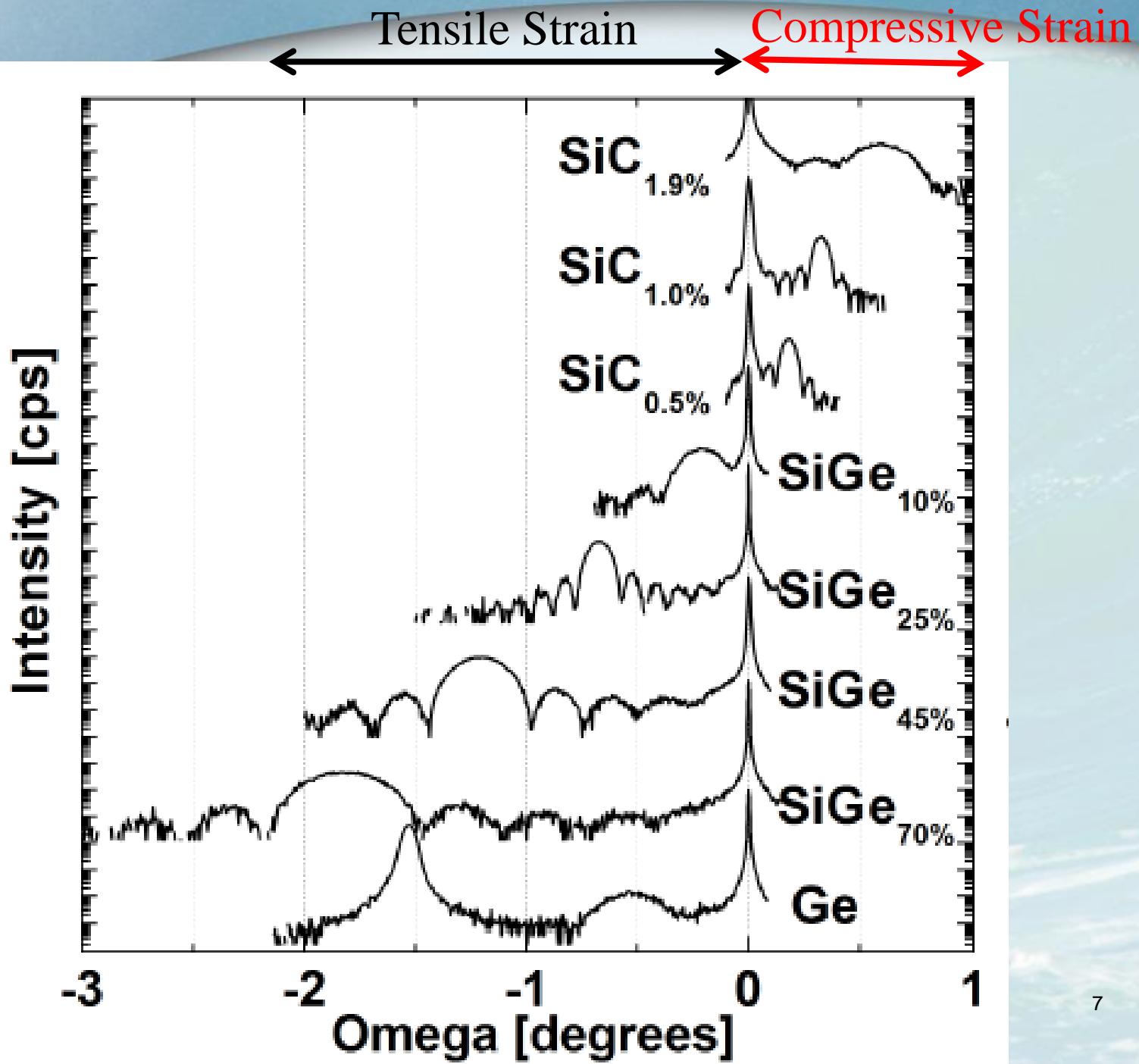
Munehisa Takei¹, Hiroki Hashiguchi¹, Takuya Yamaguchi¹, Daisuke Kosemura¹,

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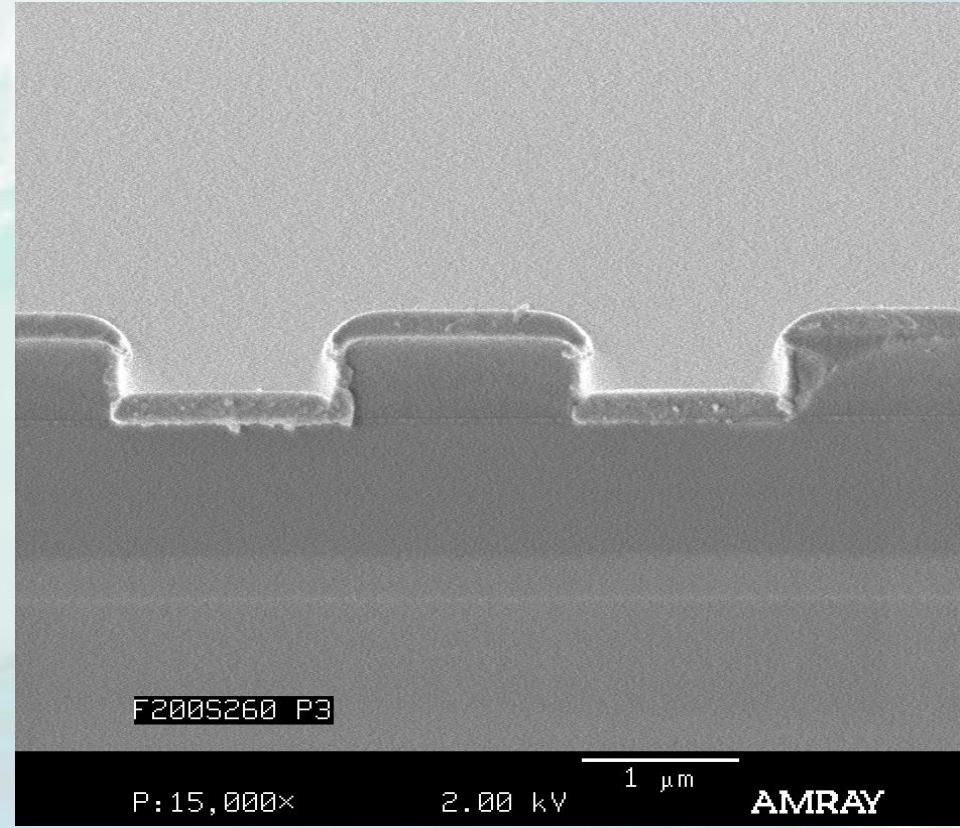
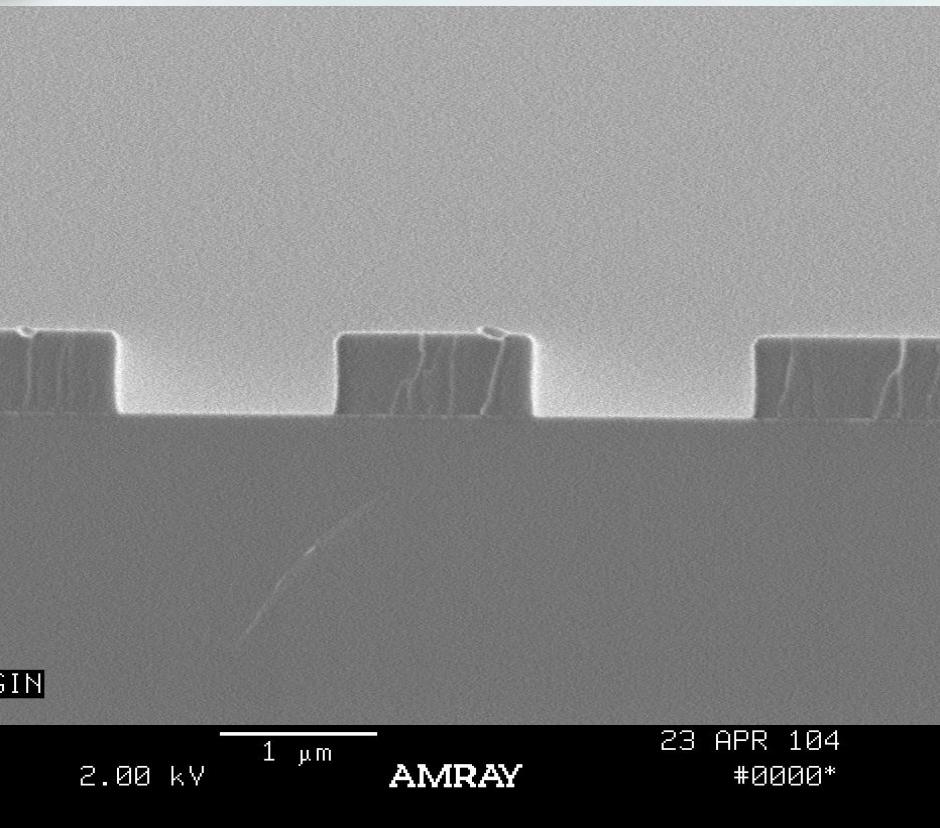
Phone/Fax: +81-44-934-7324, E-mail: m_takei@ meiji.ac.jp





200nm Ge Infusion Deposition On Photo Resist

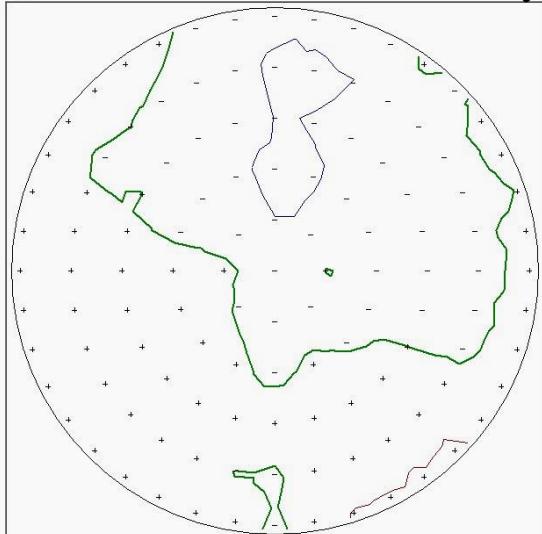
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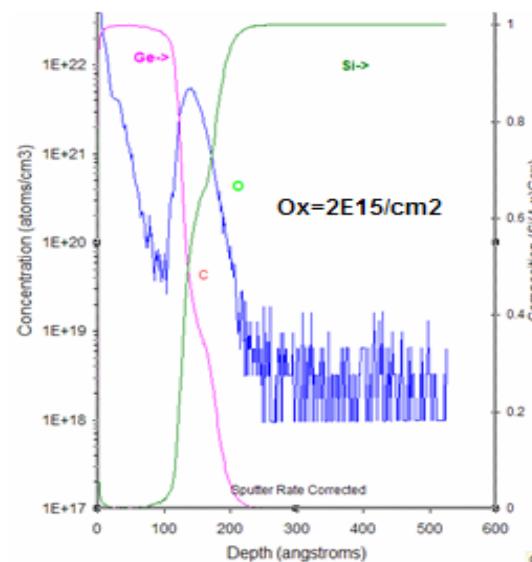
Infusion Ge-Doping/Deposition (Increasing Dose)

Medium Dose Increase Bss, High Dose Dose Controlled Deposition (DCD)

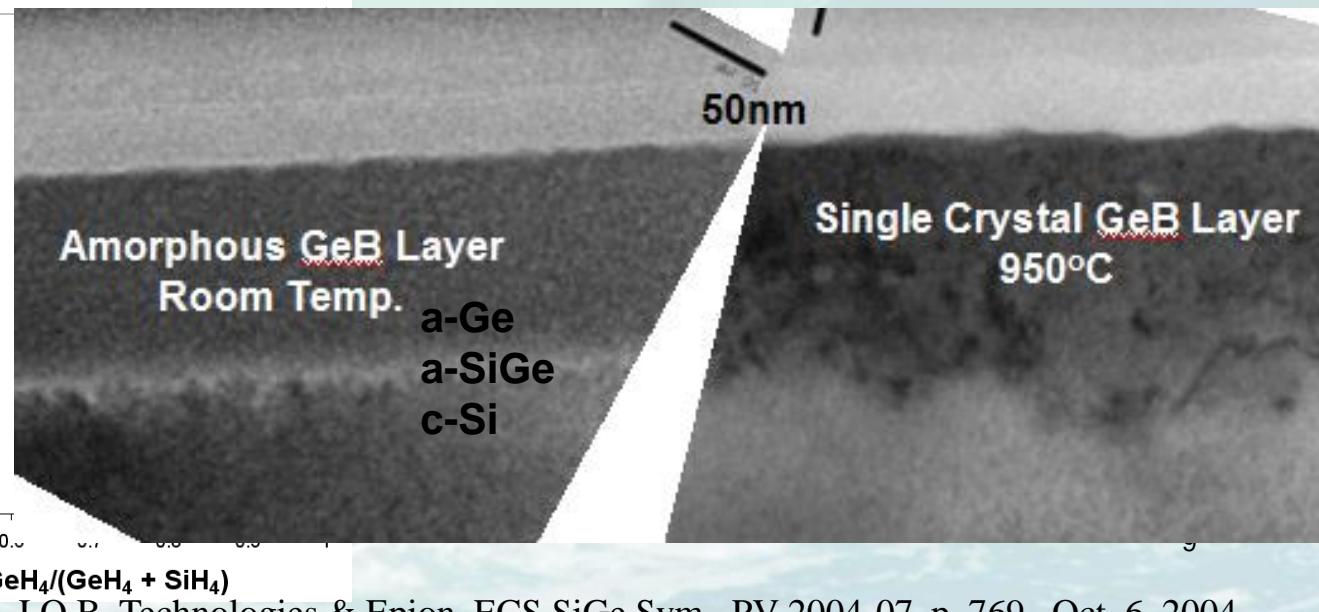
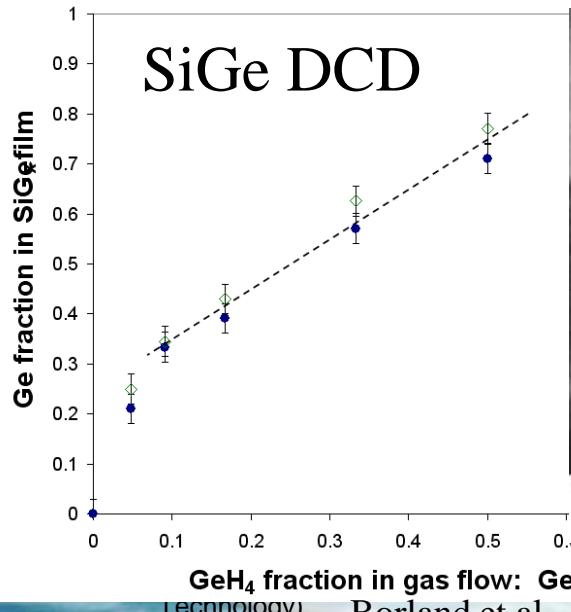
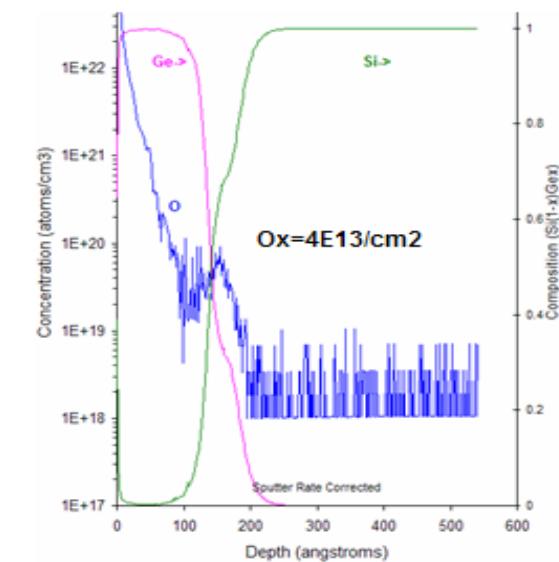
70nm Ge-DCD on 300mm bulk & SOI wafer <0.45% uniformity



No Surface Cleaning



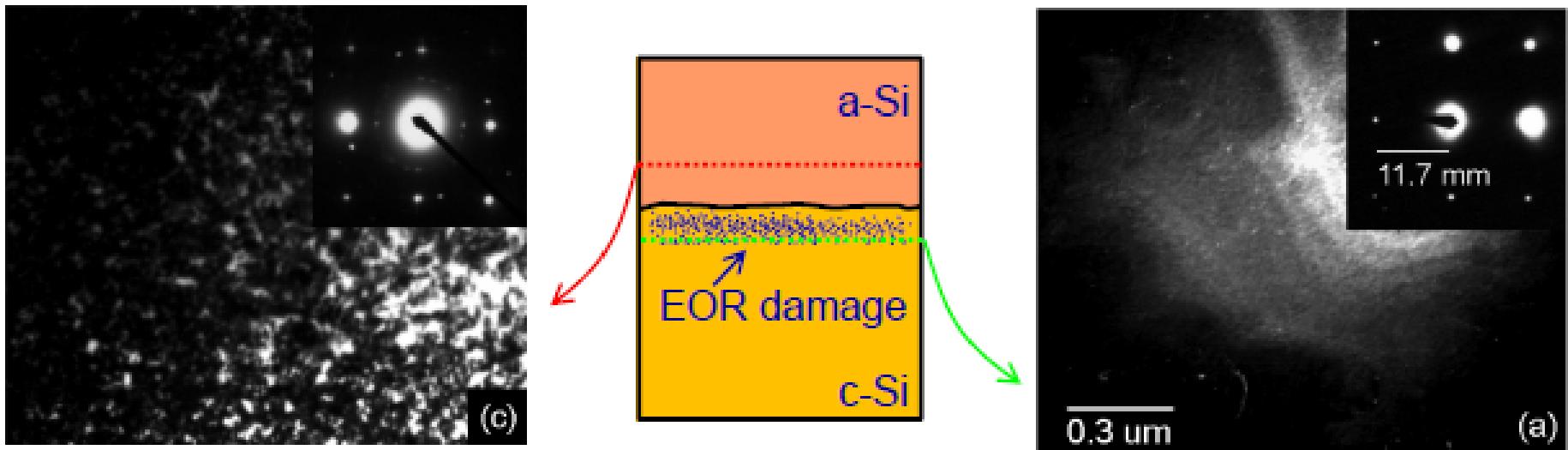
HF-Dip Surface Cleaning



Melt Laser anneals of PAI USJs

Data from PhD Thesis of M.H. Clark, Univ. of Florida, 2004

- PAI: 15 keV Si⁺ (a-thick. ~30 nm)
- XeCl laser (308 nm), 18 ns pulse, 0.68-0.76 J/cm²



Amorphous layer is not fully melted → Poly-Si

Melt front exceeds a-Si and EOR damage
→ Fully recrystallised Si (very few defects left)



0.9um Pixel CMOS Imager Sensor Technology with Backside Illumination

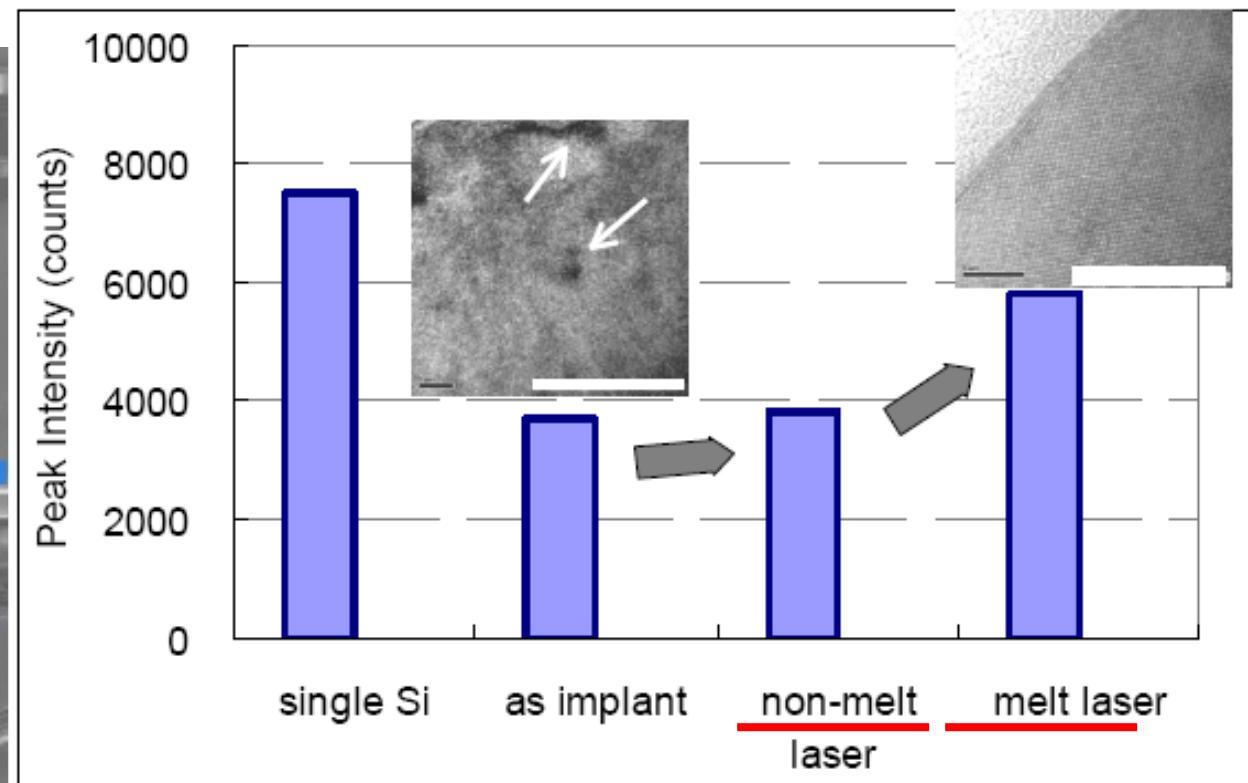
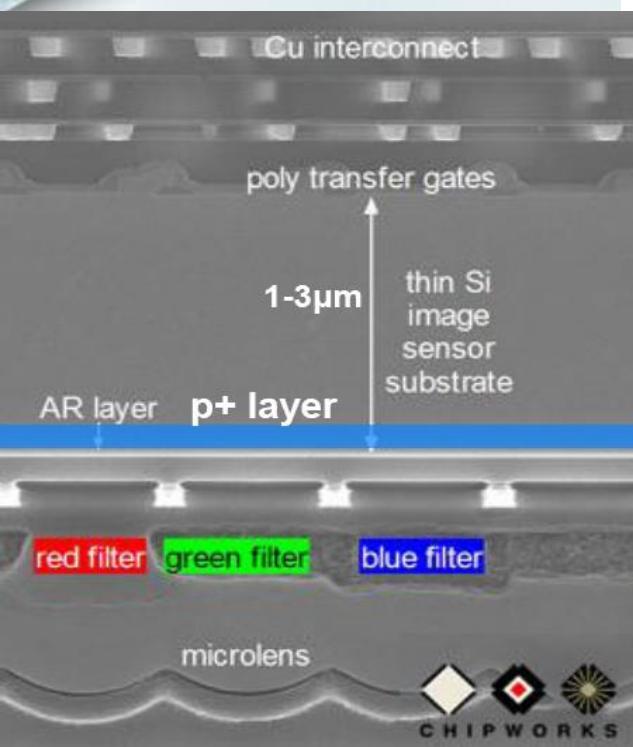
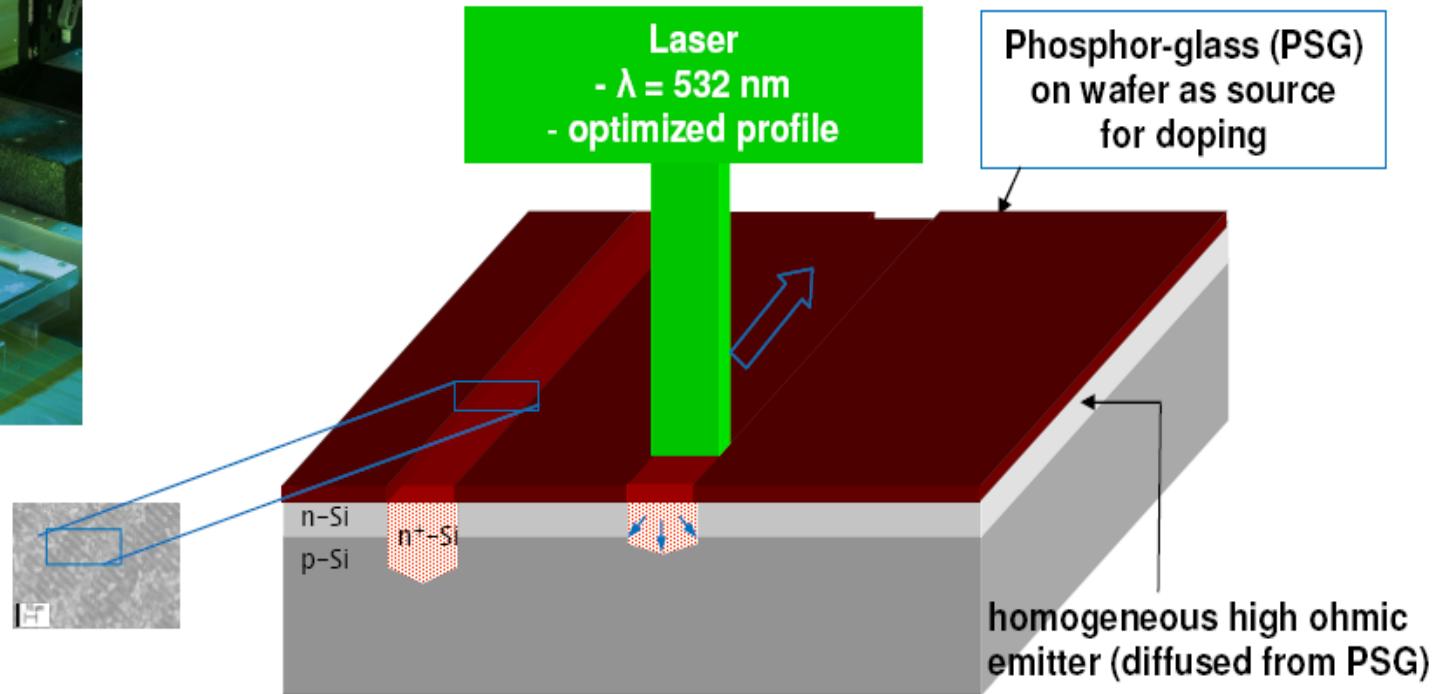


Figure 4. Photoluminescence and TEM study of laser anneal (higher peak intensity means better Si crystal quality)

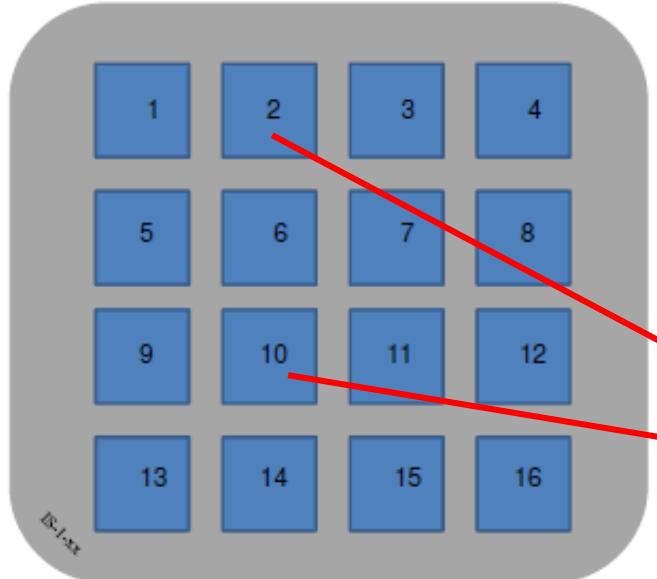
TSMC, IEDM-2010, paper 14.1



Laser process provides additional doping from PSG glass

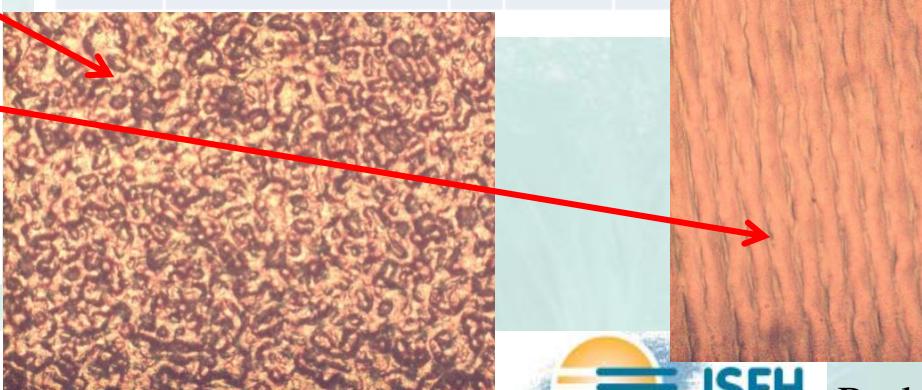


Only one laser process step added to mainstream cell process.
Optimisation of laser process (power/profile/pulse, no defects in silicon...) essential

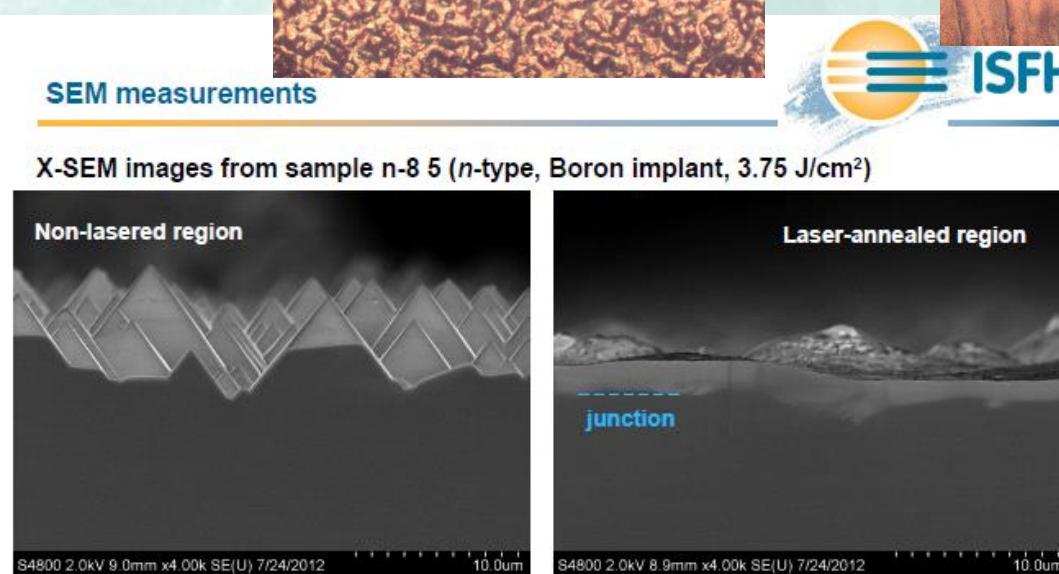
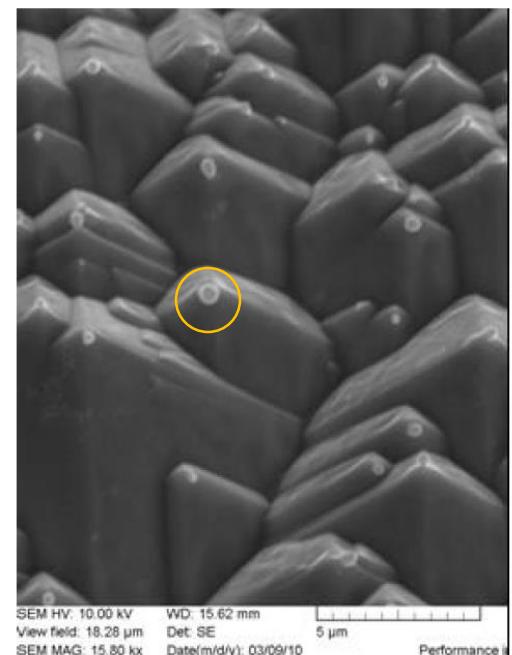


Mark in the lower left corner

| area | energy density (J/cm ²) | area | energy density (J/cm ²) |
|------|-------------------------------------|------|-------------------------------------|
| 1 | 2.75 | 9 | 4.75 |
| 2 | 3.0 | 10 | 5.0 |
| 3 | 3.25 | 11 | 5.25 |
| 4 | 3.5 | 12 | 5.5 |
| 5 | 3.75 | 13 | 5.75 |
| 6 | 4.0 | 14 | 6.0 |
| 7 | 4.25 | 15 | 6.25 |
| 8 | 4.5 | 16 | |

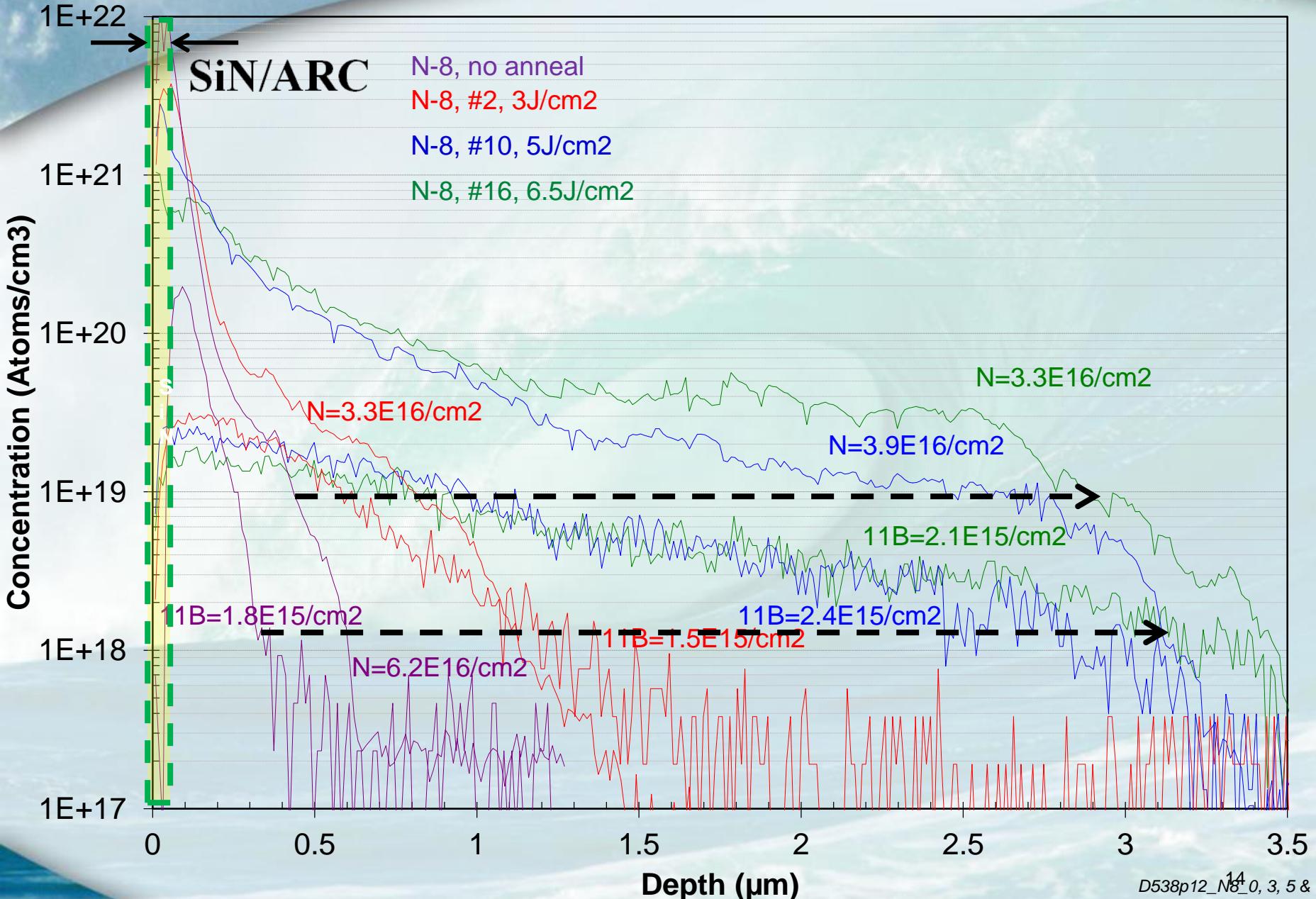


Borland et al,
EU-PVSEC
2012



- *p*-doped regions visible by contrast (appear brighter)
- As-implanted junction much more shallow than after anneal (not surprising)
- Junction depth after anneal ~2μm → much deeper than reported in paper (~0.4μm)

80nm SiN/ARC+B-implant: Note Nitrogen Melt Profile!



IWJT-2011 S9-4 paper by Keio Univ/SEN/SHI on p+ USJ by non-melt KrF(248nm) and Green (527nm) laser annealing.

Table 2. Peak temperature for each laser energy density

| KrF / Green laser (mJ/cm^2) | Peak Temperature (K) | |
|--|----------------------|--------|
| 300 / 560 | 1078 K | =805C |
| 350 / 655 | 1205 K | =932C |
| 400 / 750 | 1323 K | =1050C |
| 450 / 850 | a-Si melting (1420K) | =1147C |

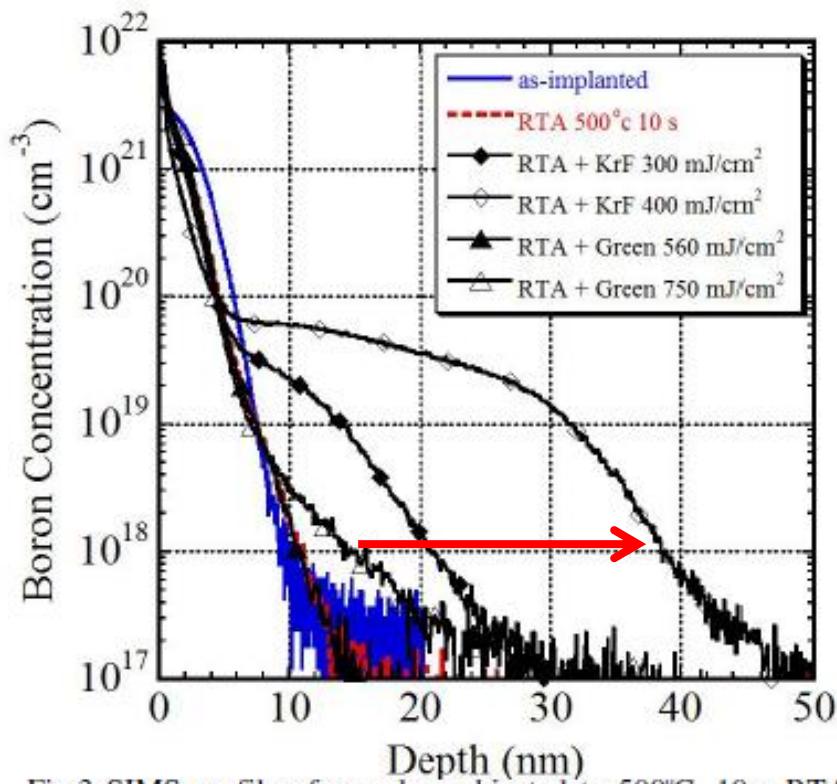


Fig.3 SIMS profile of samples subjected to 500°C, 10 s RTA + NLA by KrF and green laser

Marketing, Sales
Technology

With KrF sees selective/localized Si-melt of amorphous region only!

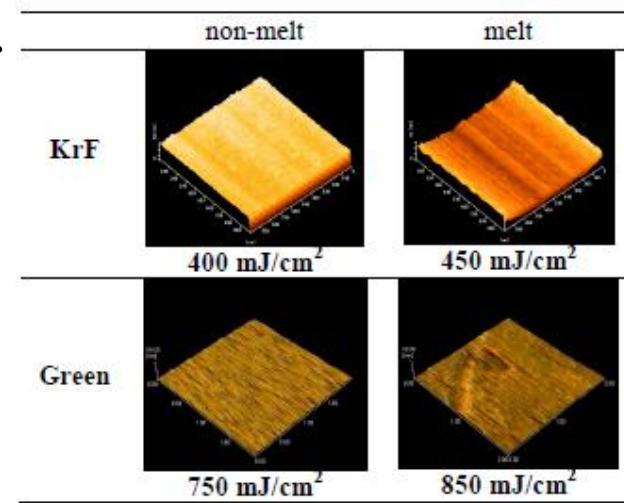
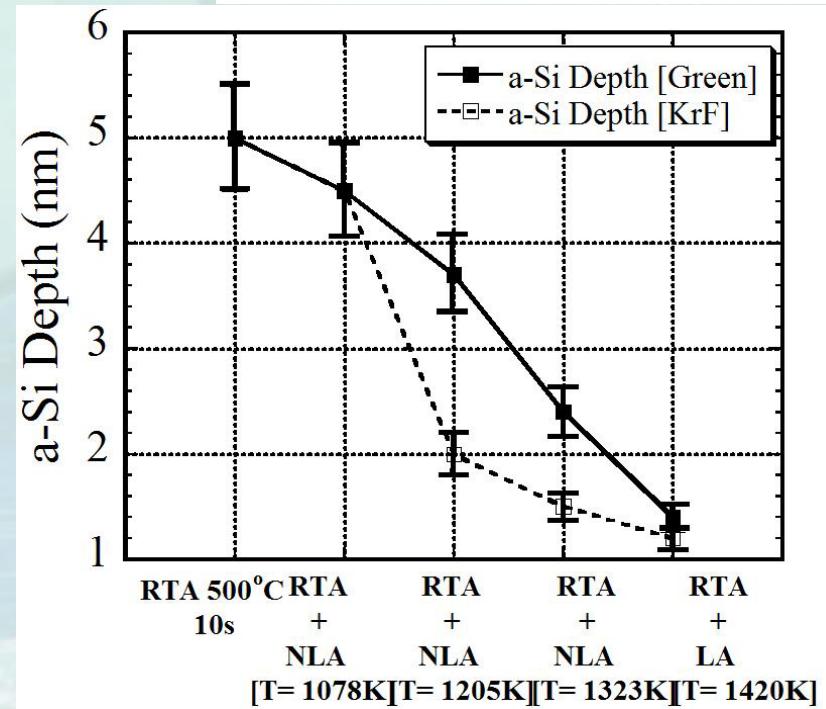
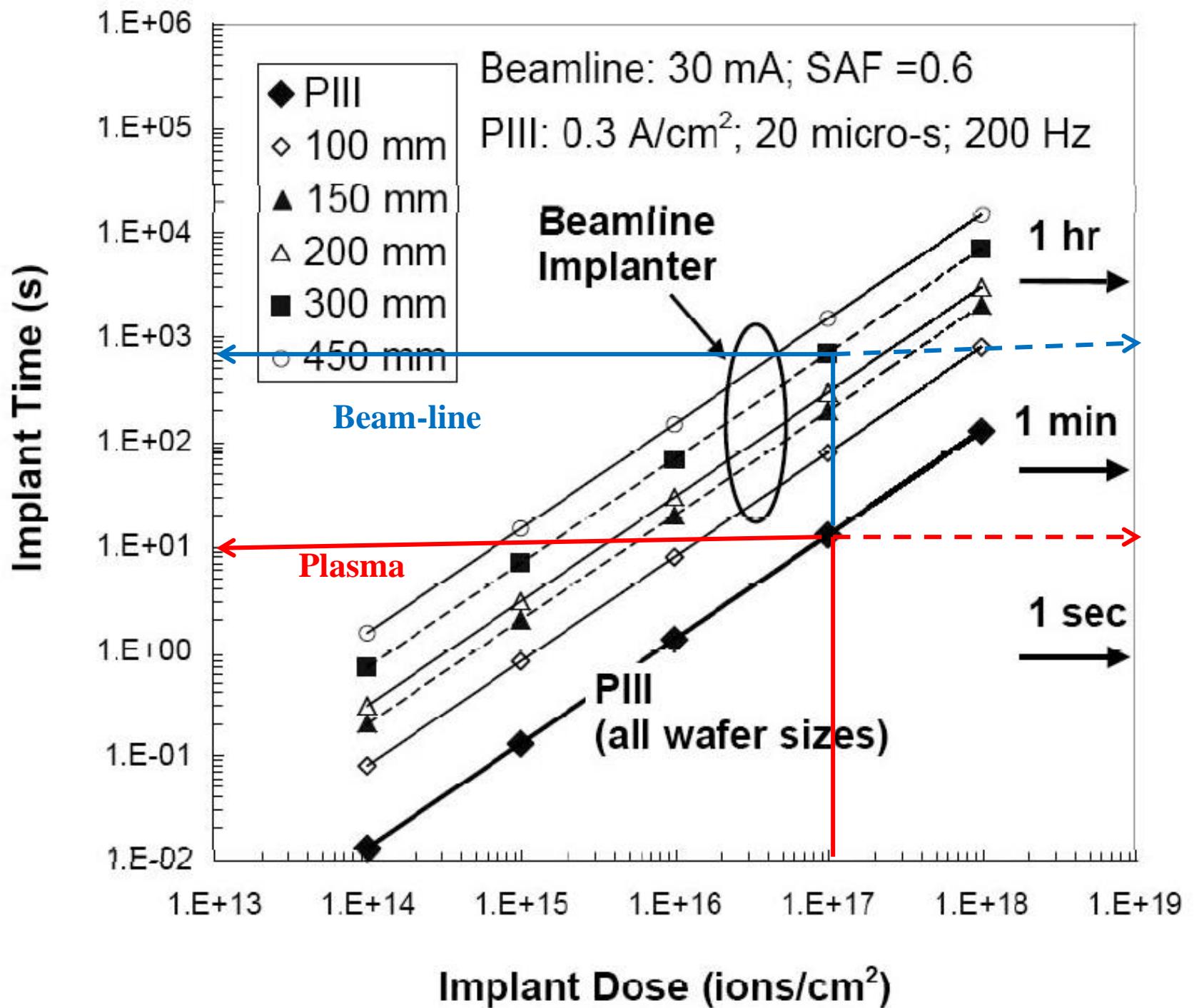


Fig. 2 AFM images for annealed samples



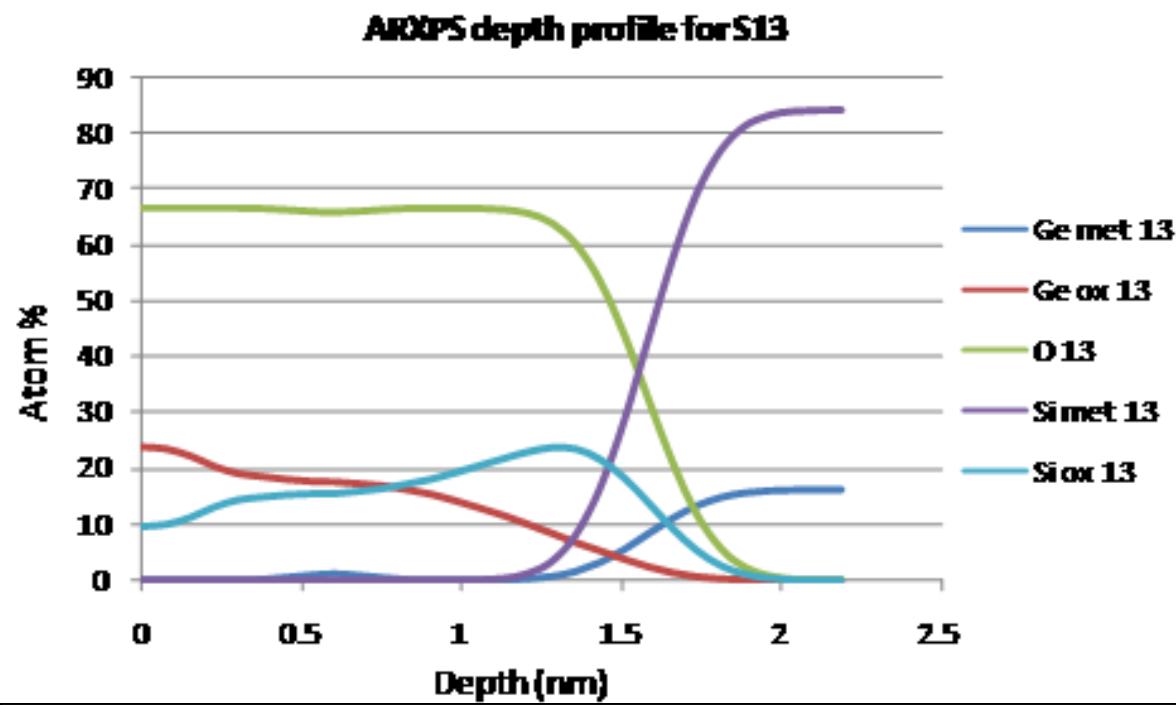
Outline

- Introduction
- Experimentation
 - Ge+B Plasma Implantation/Deposition Doping
 - Ge=3kV, 1E16/cm² and 1e17/cm² dose
 - B=500V, 4E15/cm² and 4E16/cm² dose
 - Laser Melt Annealing
 - Innovavent 515nm laser
 - Excico 308nm laser
- Results
- Summary



Micron Provided Wafers

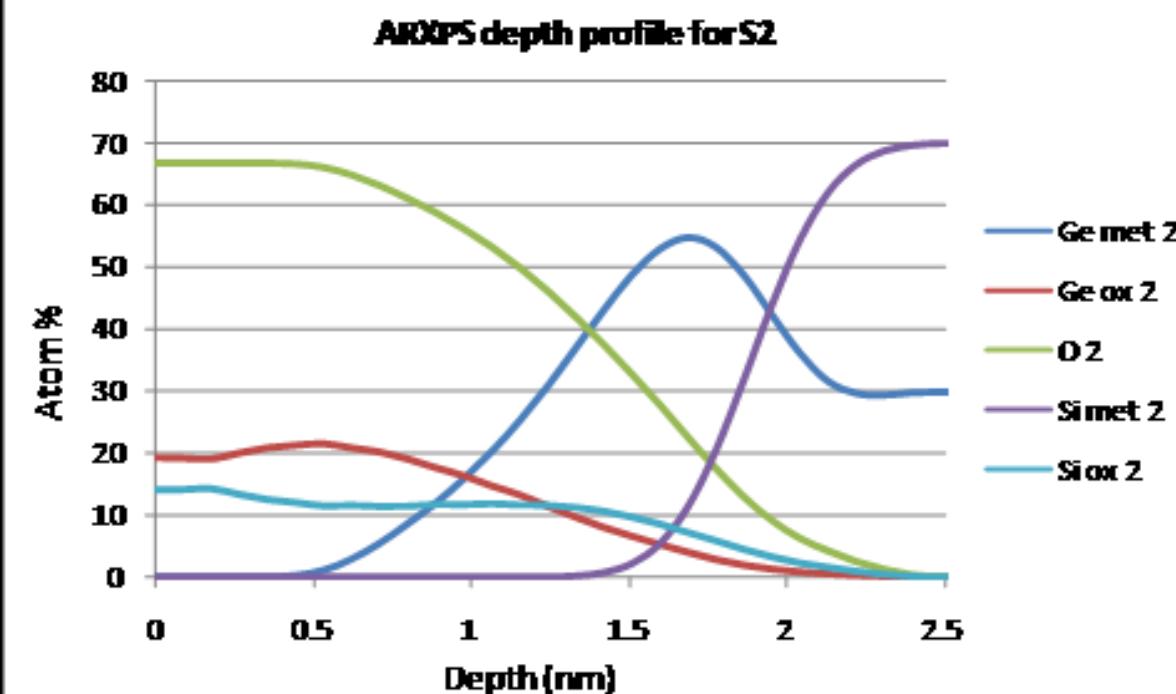
#13, GEH3K1E16
(8.6sec) shows a ~20%
Ge peak near Si surface,
also shows a ~ 1.5nm
native SiO₂/GeO_x layer.



#15 (#2), GEH3K1E17
(66 sec) shows a ~55%
Ge peak near Si surface,
also shows a ~ 1.8nm
native SiO₂/GeO_x layer.

Why not 200%?

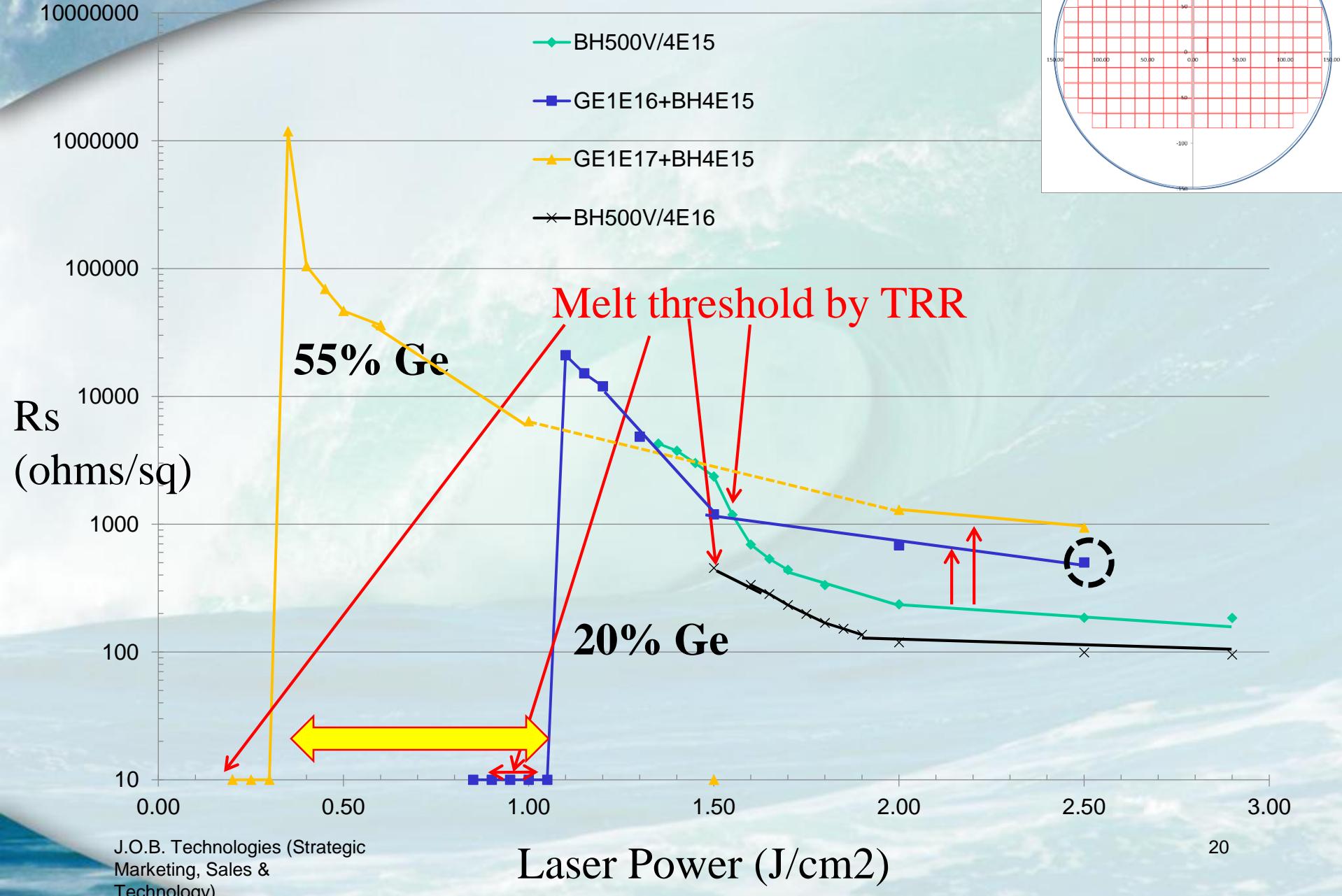
J.O.B. Technologies (Strategic
Marketing, Sales &
Technology)



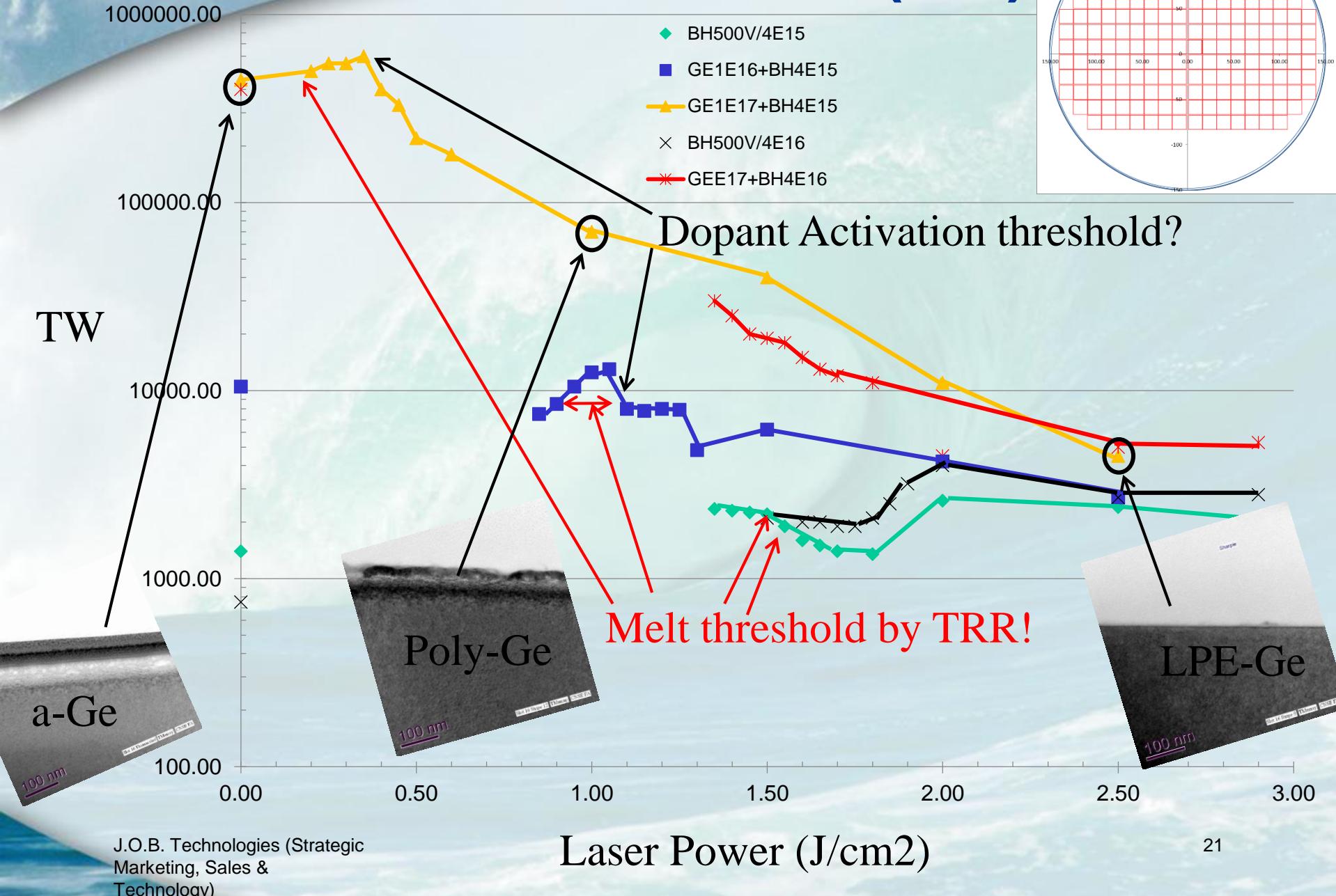
Outline

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- Experimentation
- Results
 - 308nm Laser Anneal
 - 515nm Laser Anneal
- Summary

Excico-Rs



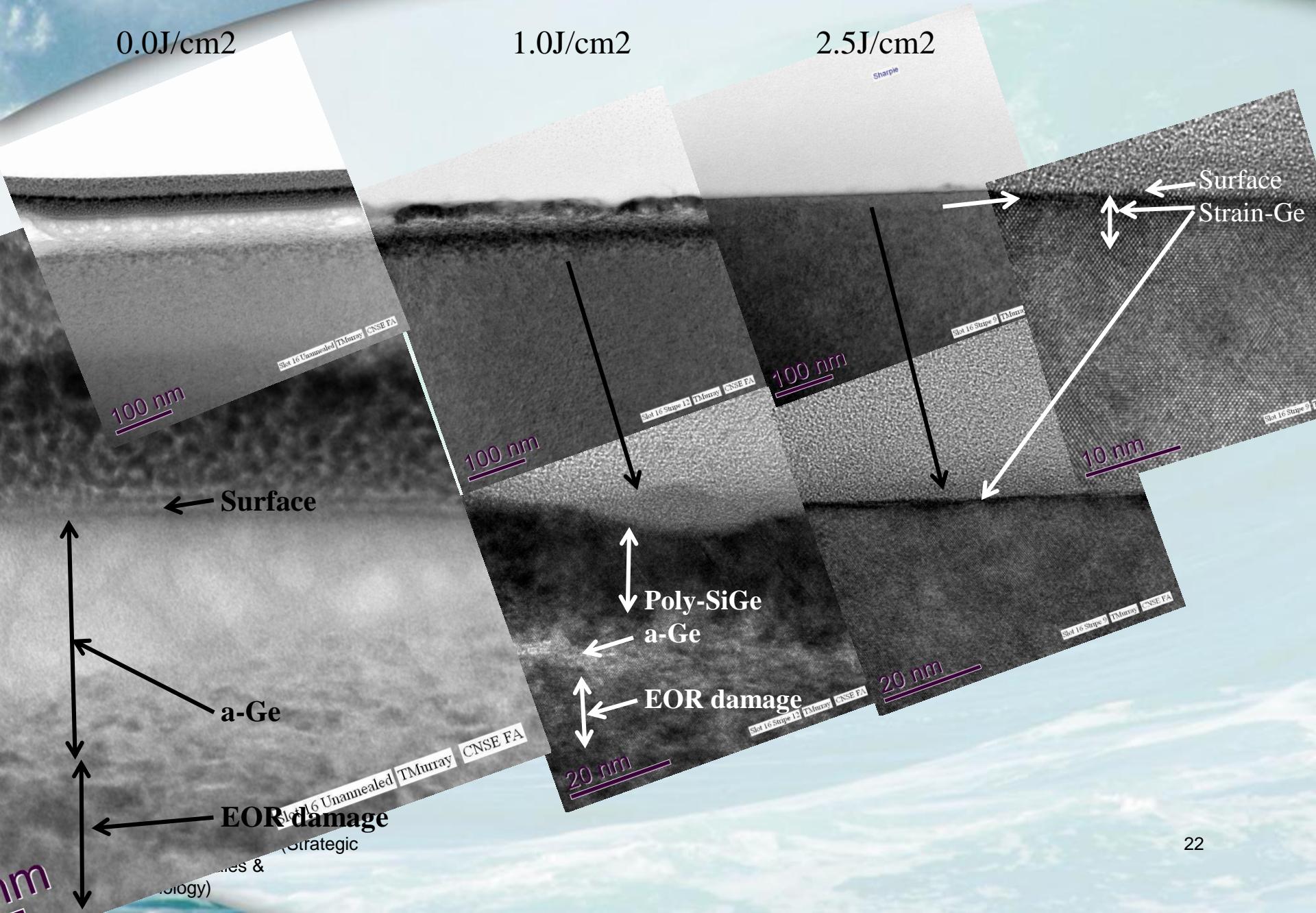
Excico-Therma-Wave (TW)

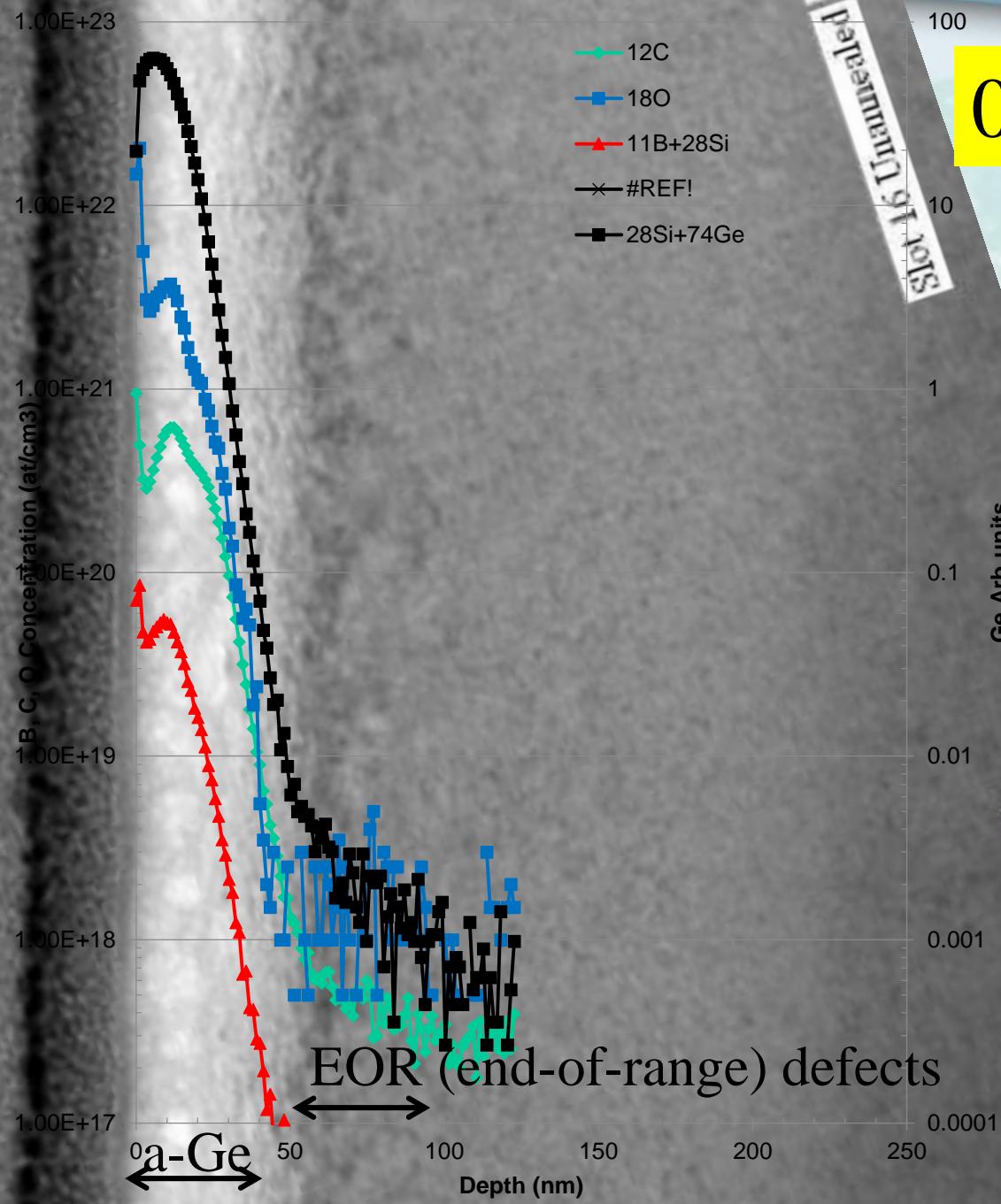


0.0J/cm²

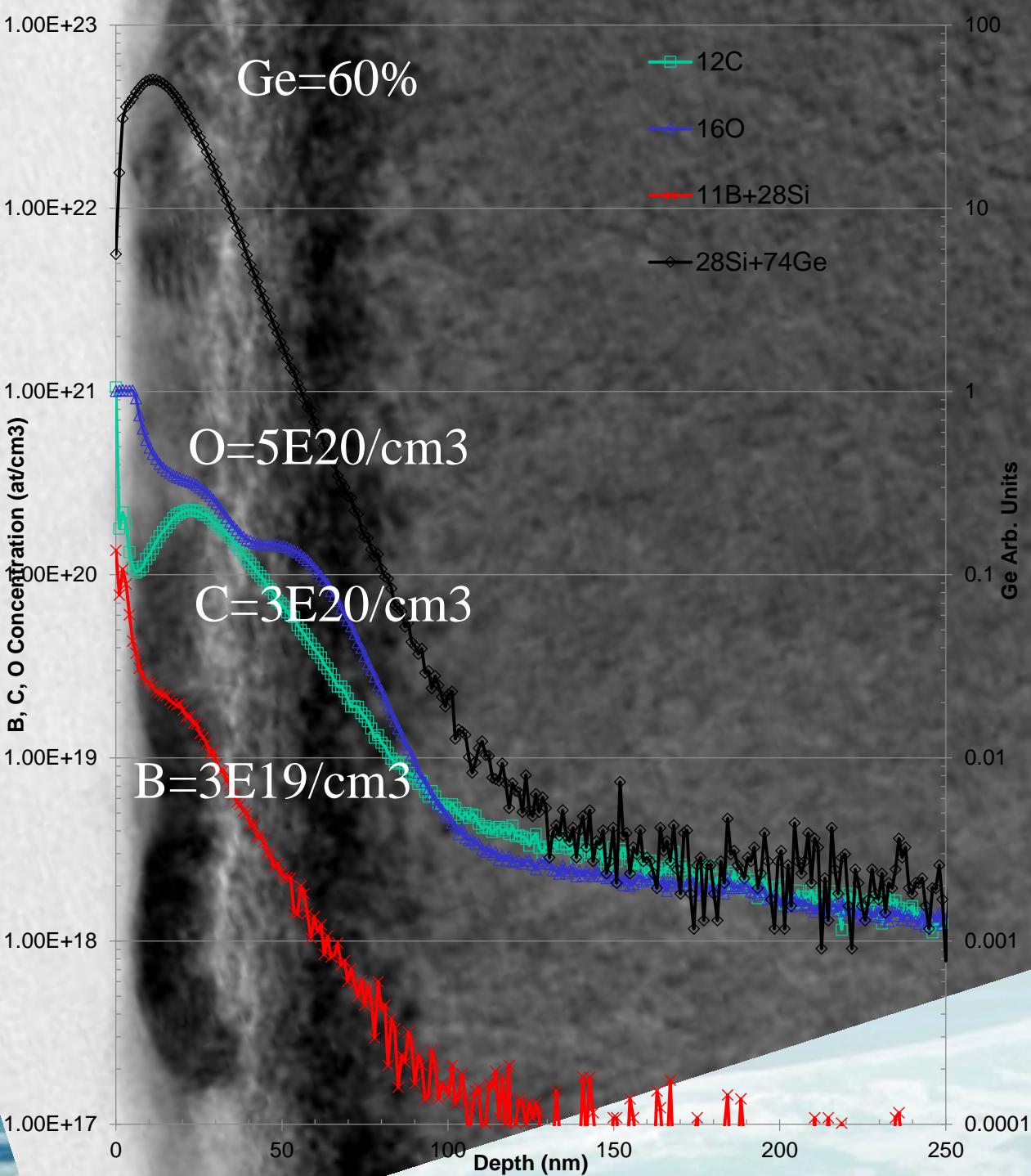
1.0J/cm²

2.5J/cm²

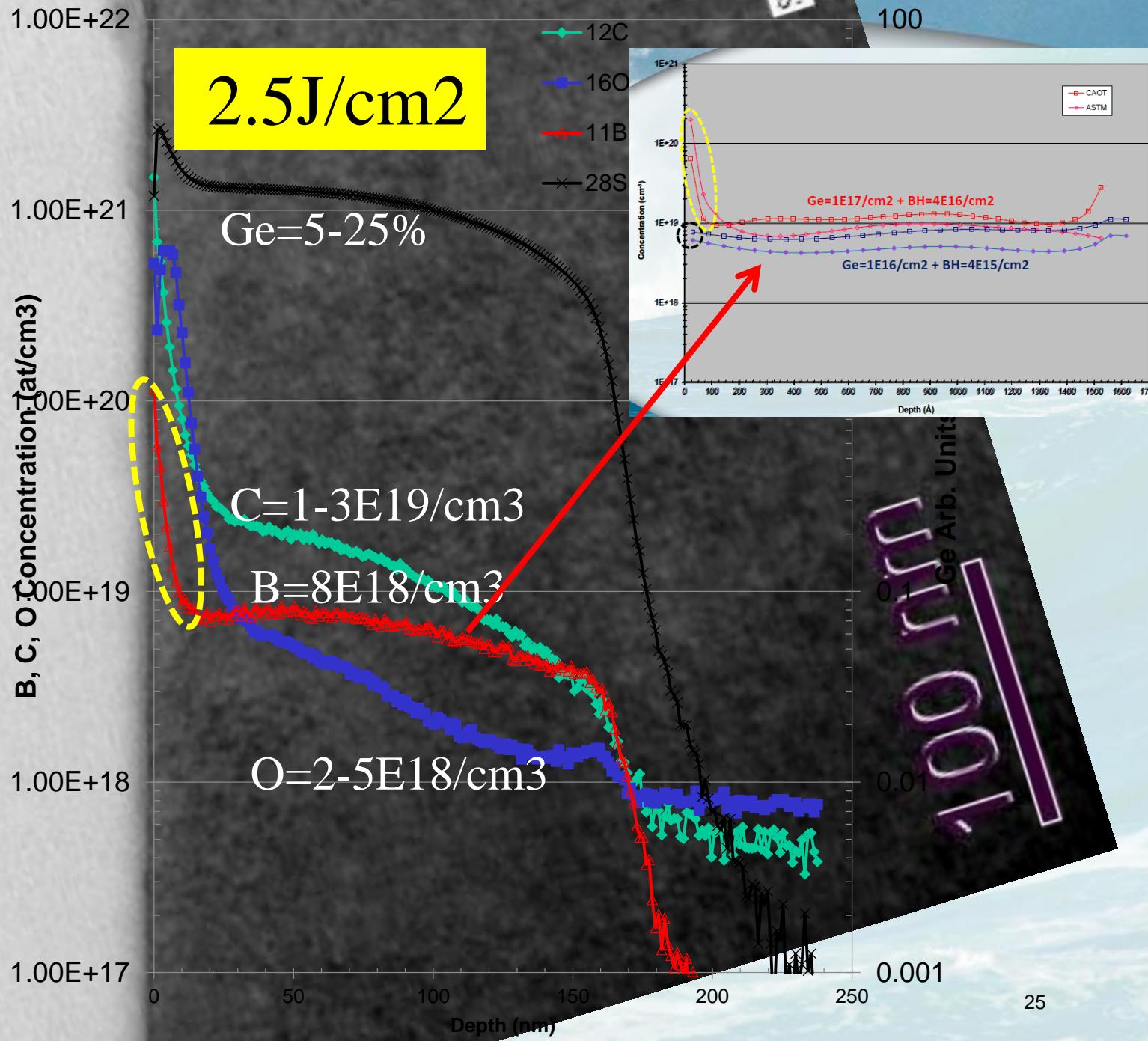




0.0J/cm²



1.0J/cm²

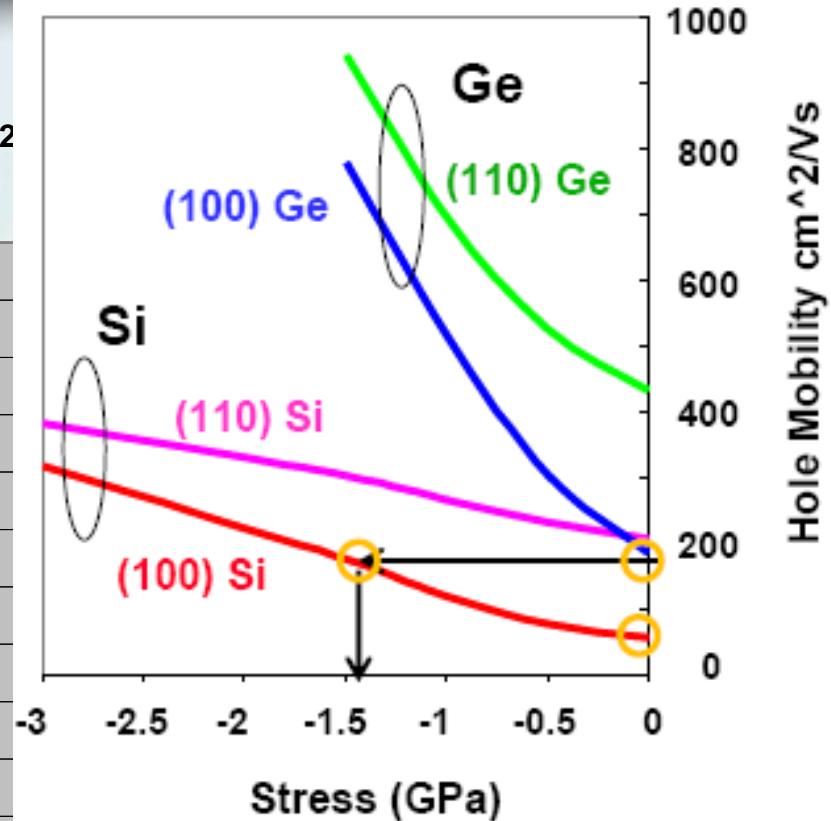
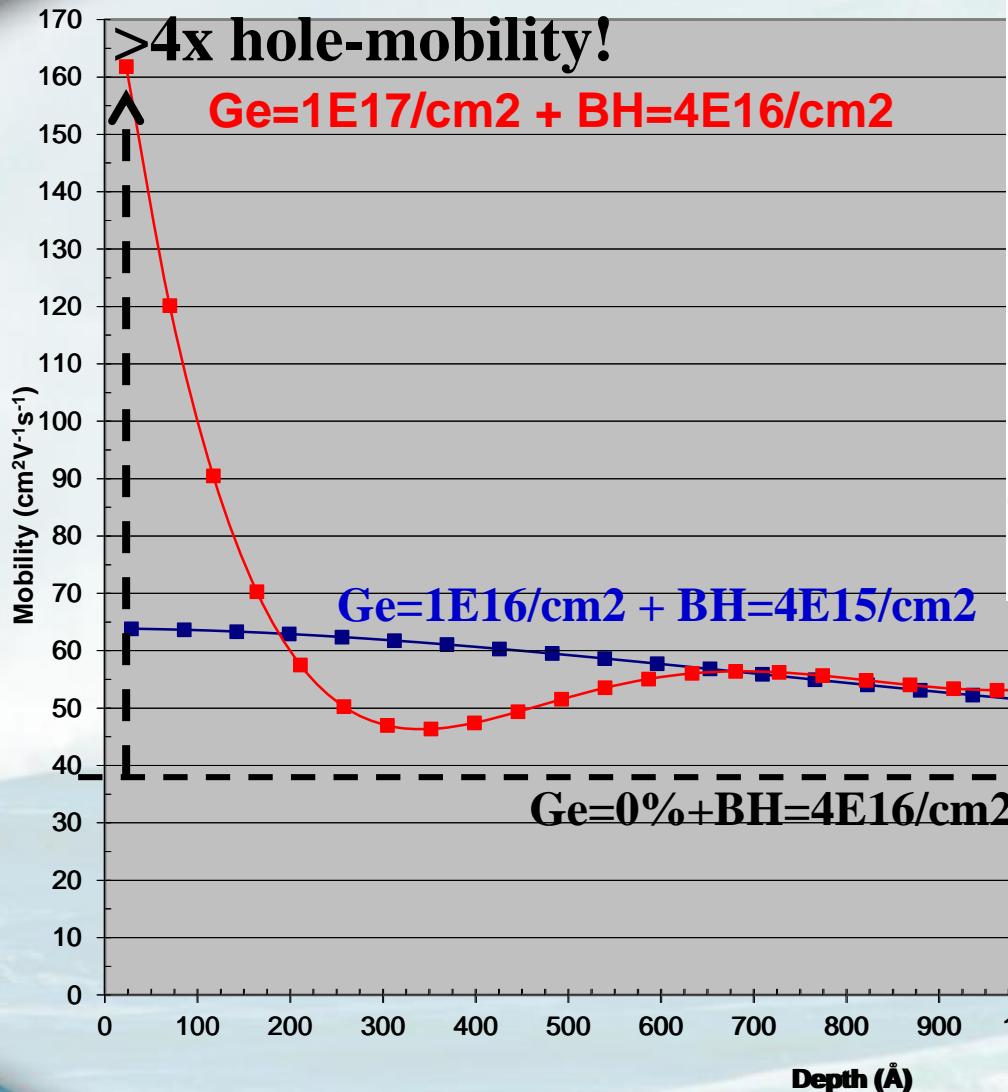


UCLA Hall Analysis of 308nm

Slot#14: Ge=1E16+B=4E15

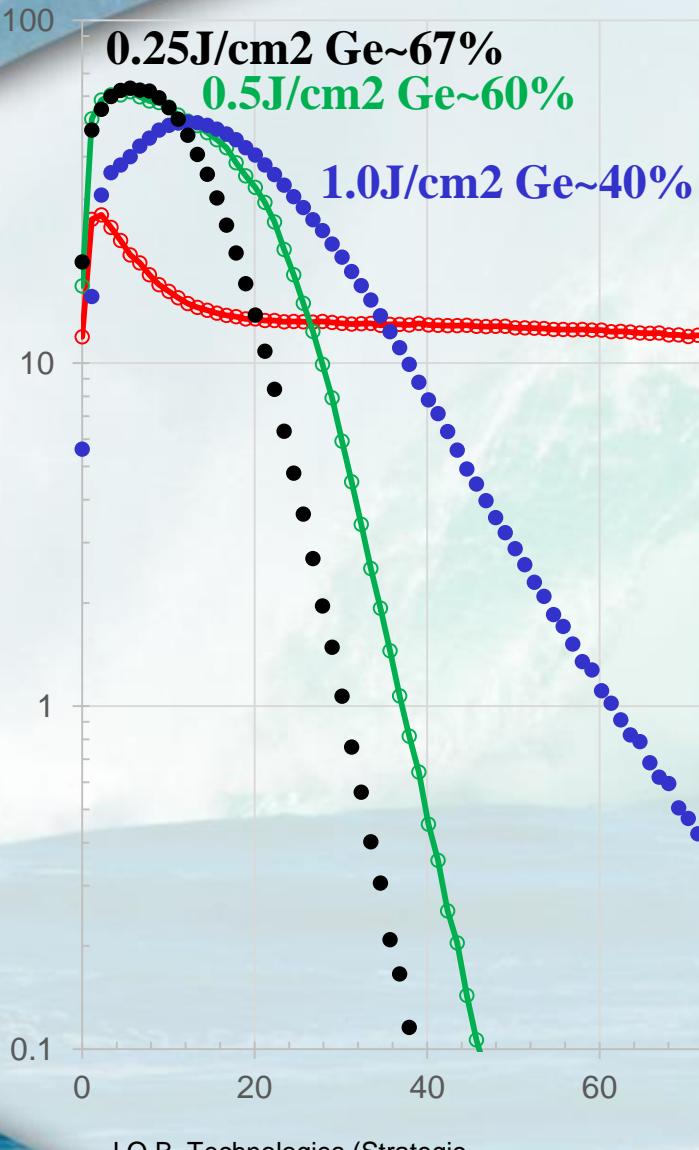
Slot#18: **Ge=1E17**+B=4E16

Mobility JA14ED12

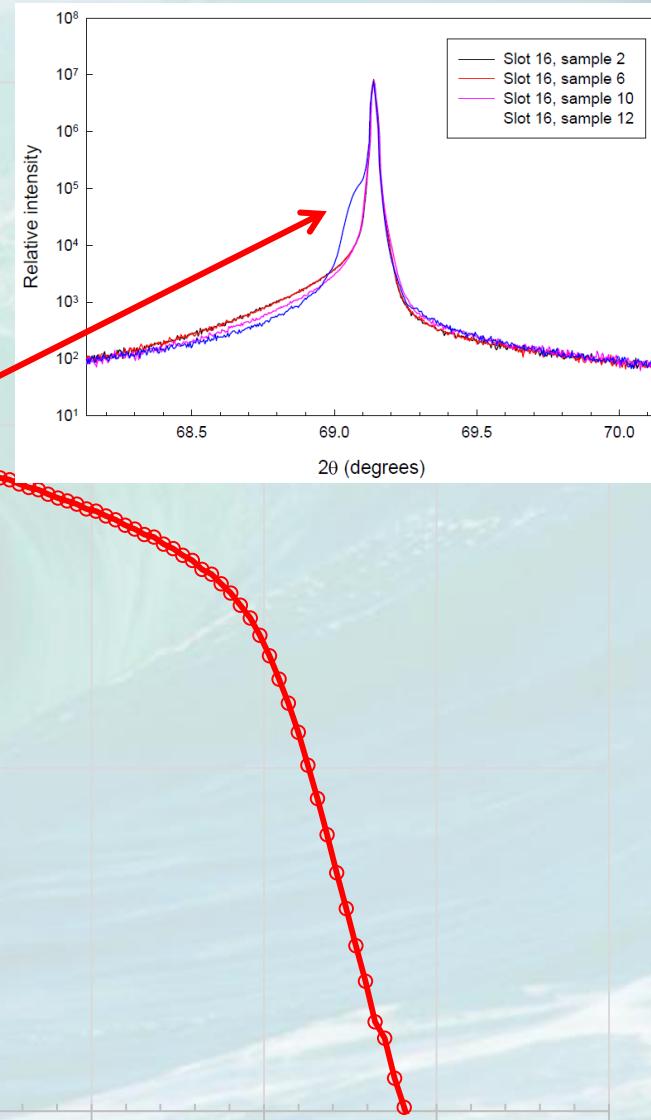


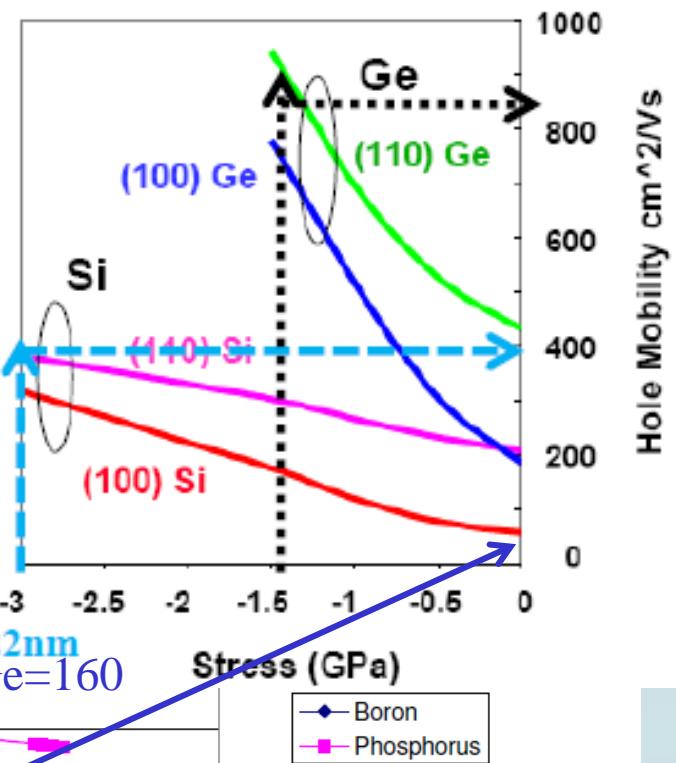
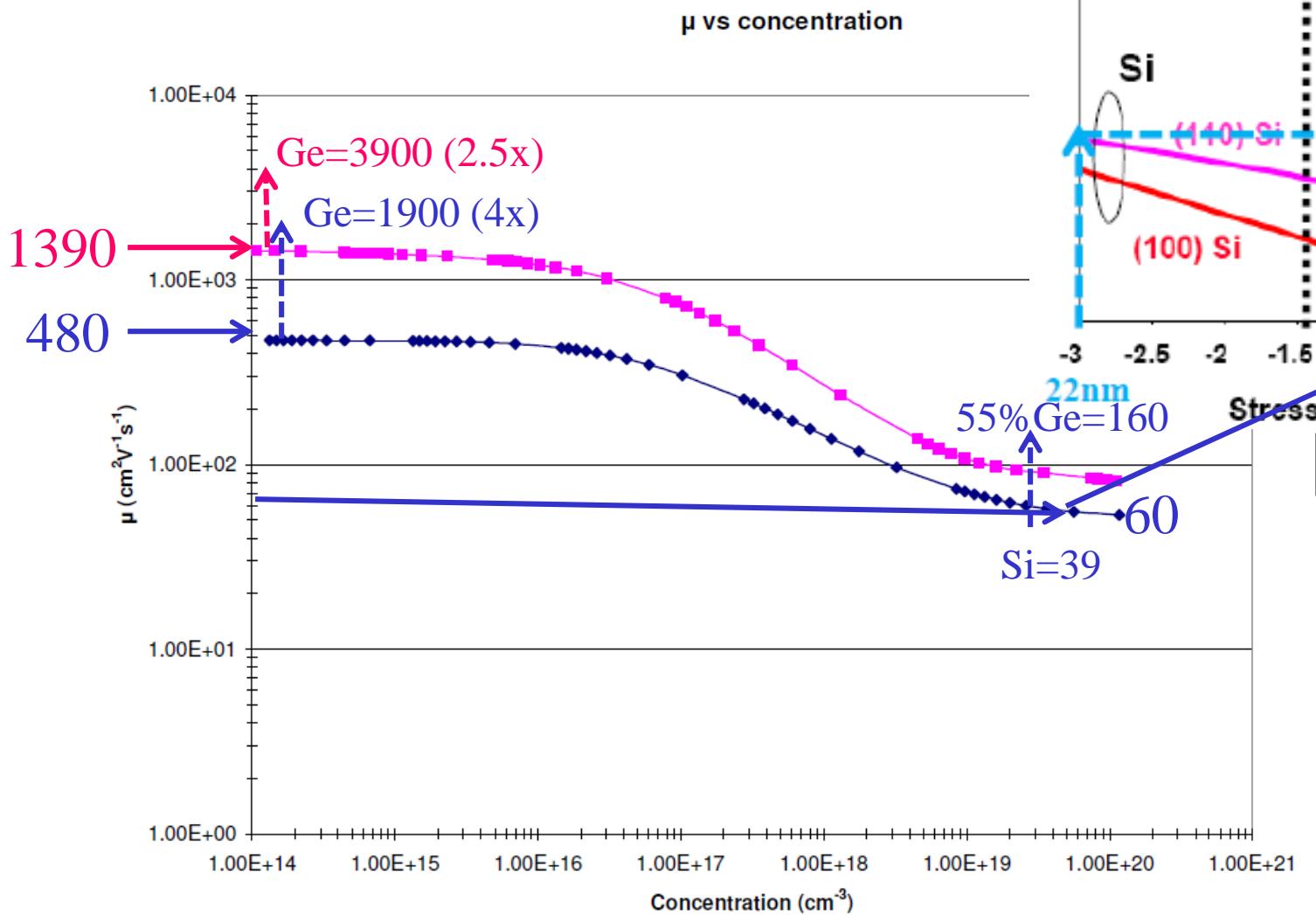
Excico (308nm) Ge-SIMS by CNSE

Ge Concentration %



Ge 2.5J
Ge 1.0J
Ge 0.5J
Ge 0.25J

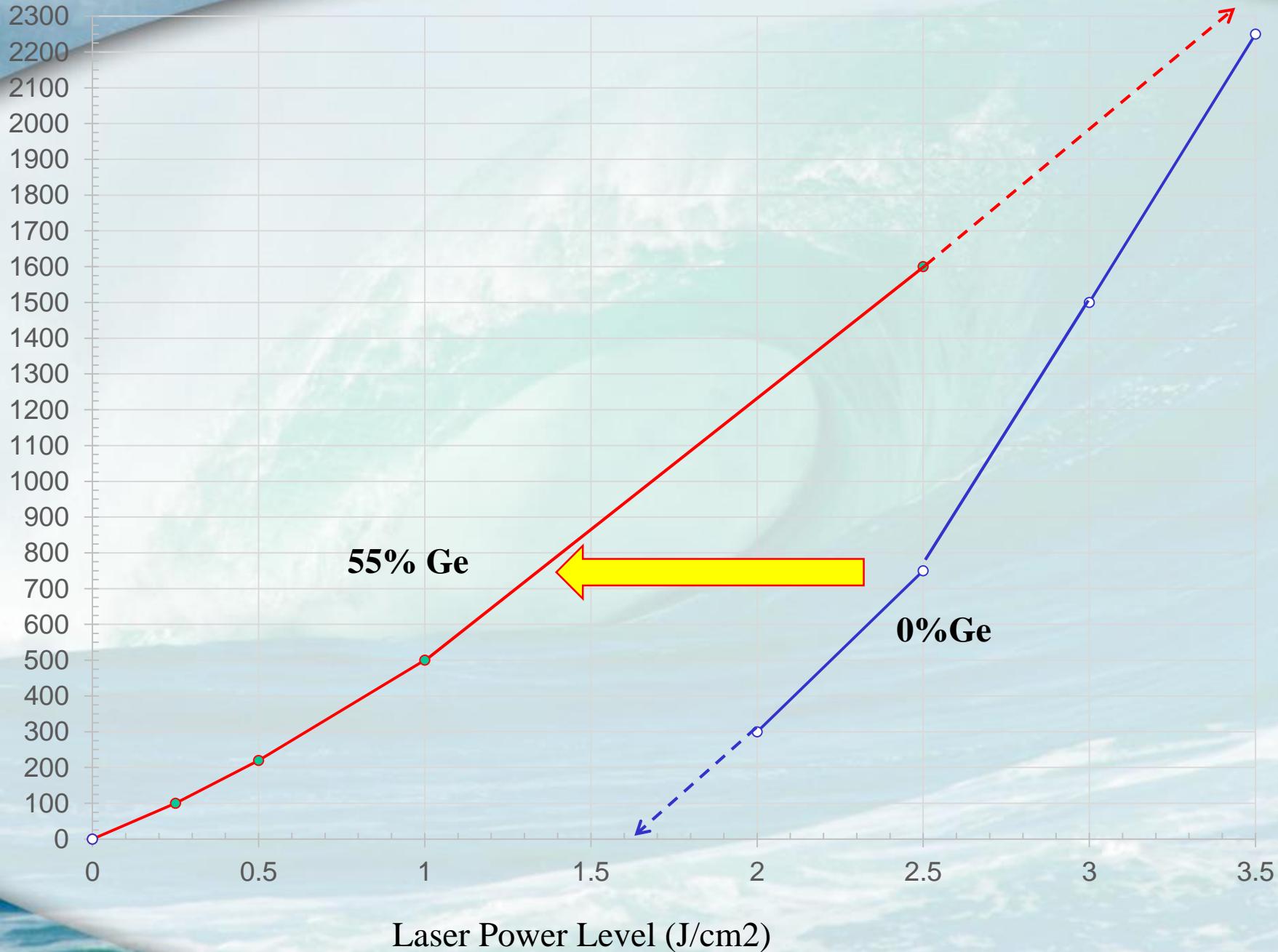




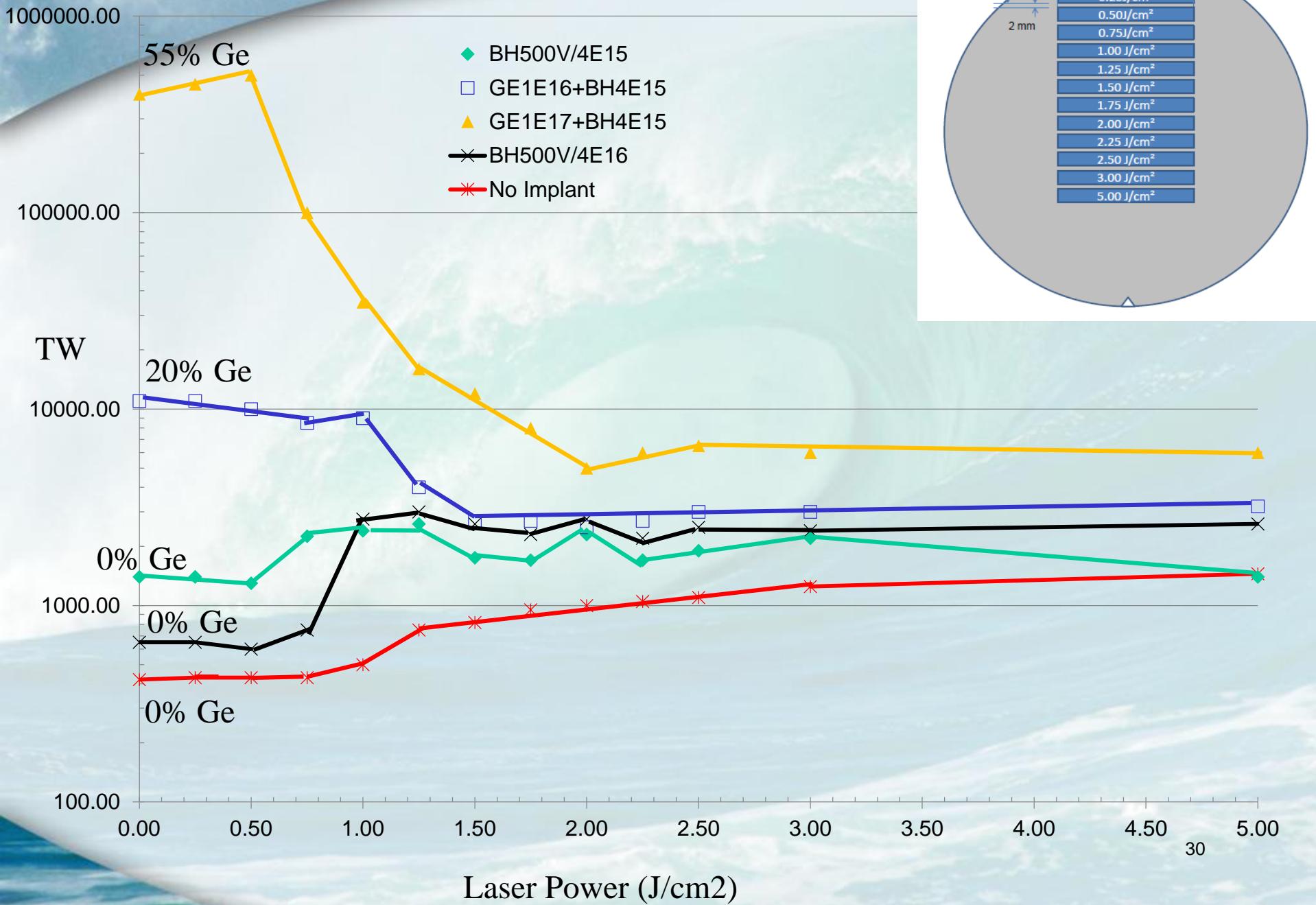
Melt Depth (A)

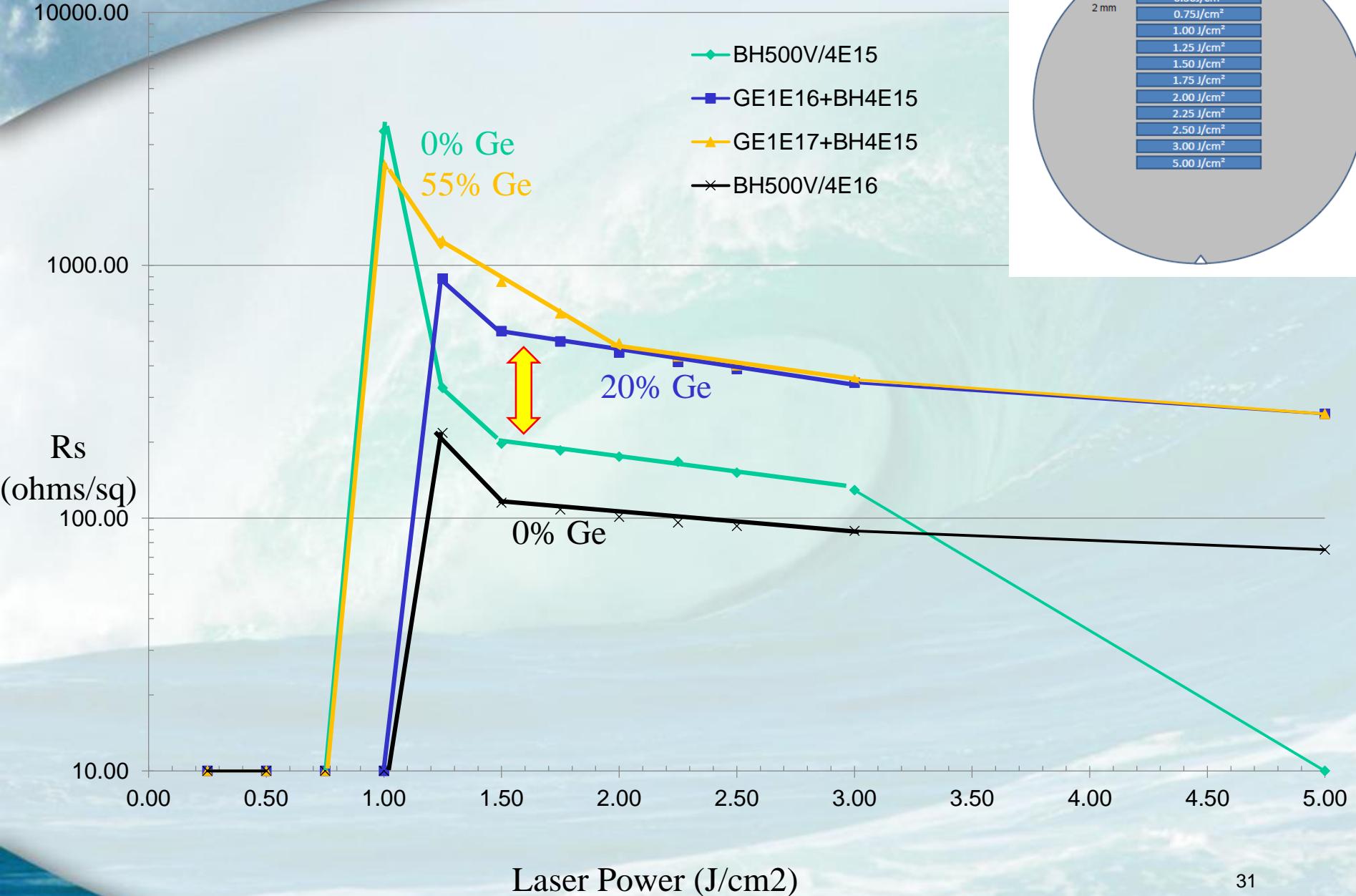
308nm

Excico



Innovavent-Therma-Wave (TW)





0.0J/cm²: B=2E21/cm³=4.1E14/cm², Xj~11nm

0.5J/cm²: B=8E20/cm³=2.2E14/cm², Xj~12nm

Ge=55% <3nm

Ge=10-30% <22nm

1.0J/cm²: B=7E19/cm³=2.4E14/cm², Xj~35nm

2.0J/cm²: B=2-3E19/cm³=3.3E14/cm² Xj~105nm

Ge=3-6% <95nm

Ge->

B

Ge=45% <10nm

B

B

**Ge-Plasma channeling
B-channeling**

Amorphous Ge-Plasma Implant
X-TEM

B CONCENTRATION (atoms/cc)

1E+22

1E+21

1E+20

1E+19

1E+18

1E+17

1E+16

1E+15

100

10

1

0.1

0.01

0.001

0.0001

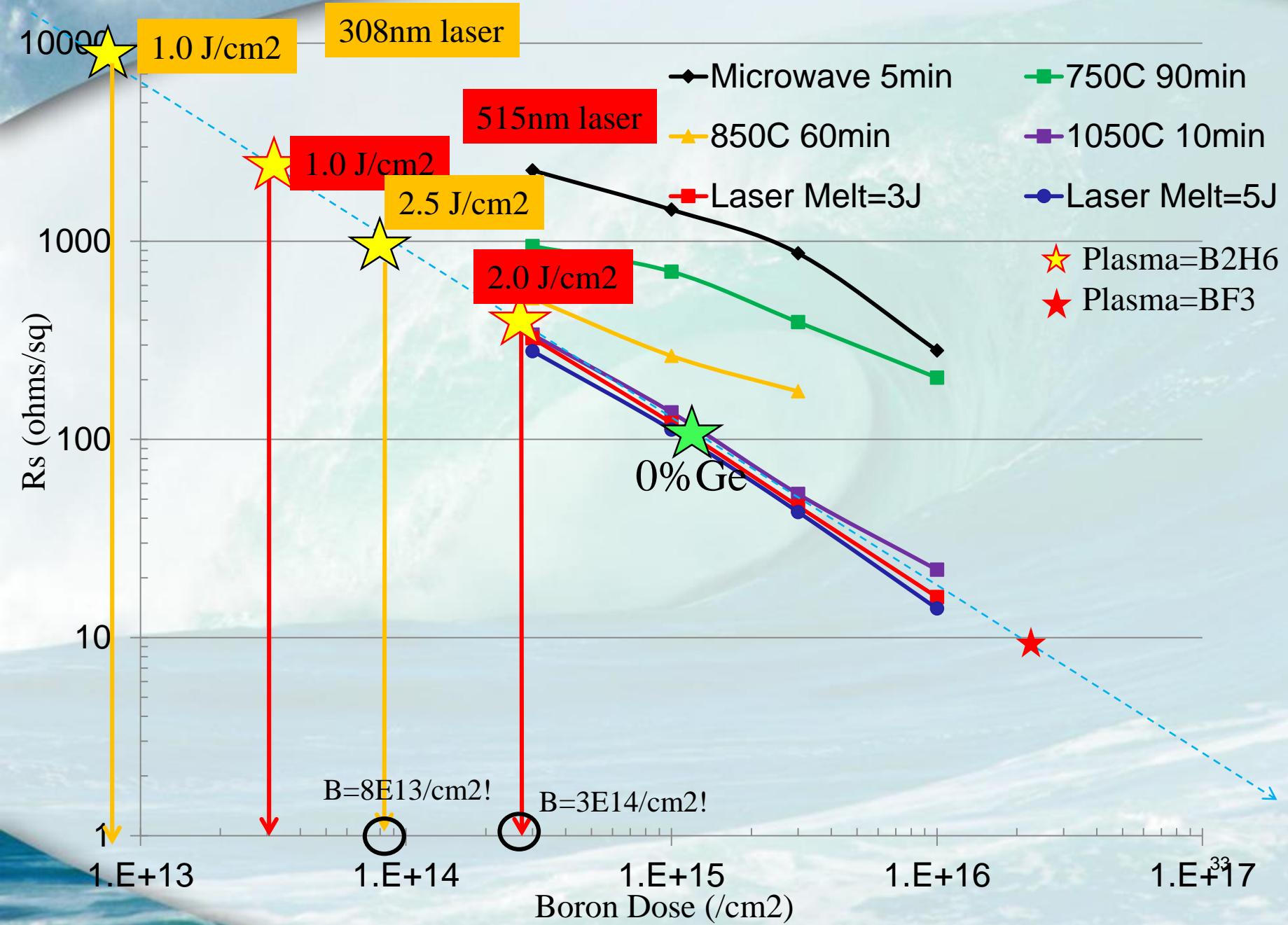
0.00001

Ge INTENSITY (arbitrary units)

DEPTH (Å)

Y0DHV503_yL_11Micronslot9DPAGM038MX03Unannealed(B)

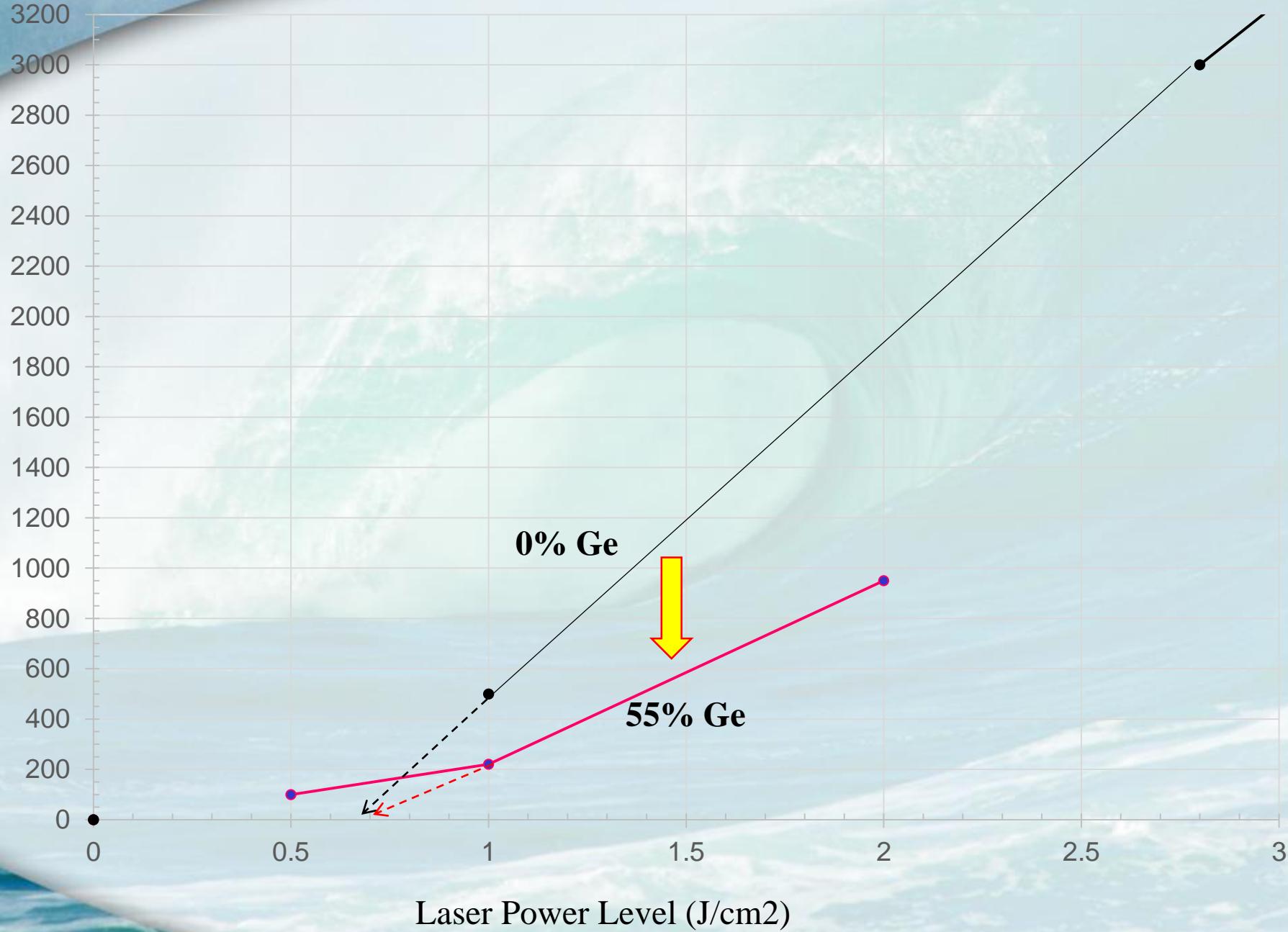
Boron Implant

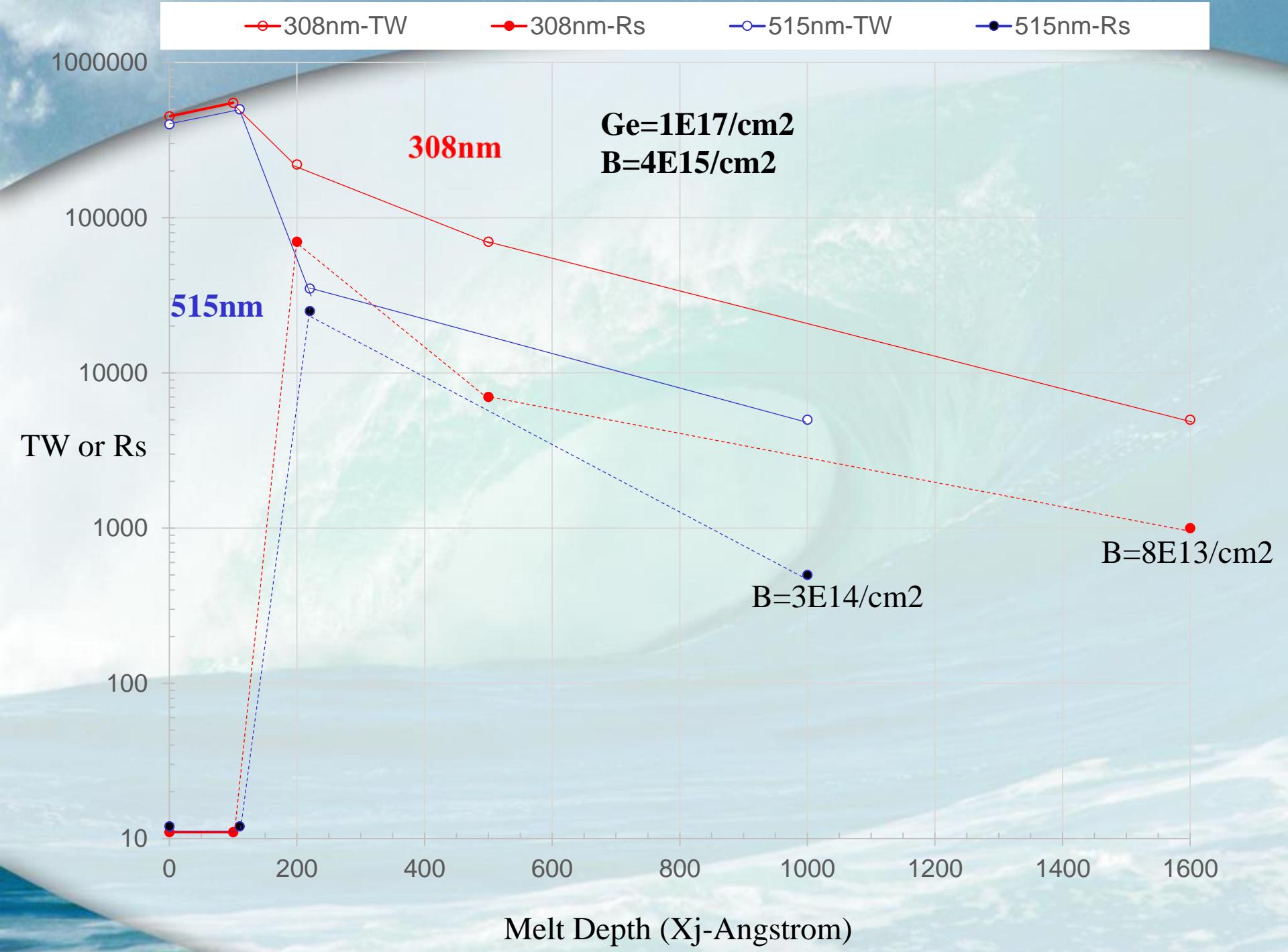


Melt Depth (A)

515nm

Innovavent





Summary & What Next?:

- High quality Ge-Epitaxy was achieved from $1\text{e}17/\text{cm}^2$ amorphous Ge-plasma implantation when using LPE (liquid phase epitaxy) by both 308nm and 515nm laser melt annealing.
- This resulted in a **4x increase in surface hole-mobility**.
 - Try lower B dose $<5\text{E}12/\text{cm}^2$ and look at electron mobility next.
- The 308nm laser melt annealing process achieved **shallow $<10\text{nm}$ selective localized melting of amorphous-Ge and amorphous-Si regions** but showed higher Rs dopant activation.
 - Why >3 x lower B electrical activation or B retained dose? Loss of surface Ge doping reported with Ge-RMG without surface cap!
- The 515nm laser melt annealing process showed lower Rs dopant activation (higher B retained dose) but was not selective to amorphous-Ge or amorphous-Si regions.
 - Try longer pulse duration next.
- Next: Ge, SiGe & Si hole (B) and electron (P, As & Sb) mobility
 - Ge beam-line implantation at $5\text{E}16/\text{cm}^2$ dose
 - Ge-Epilayer on Si-bulk, SiGe-buffer and SOI wafers