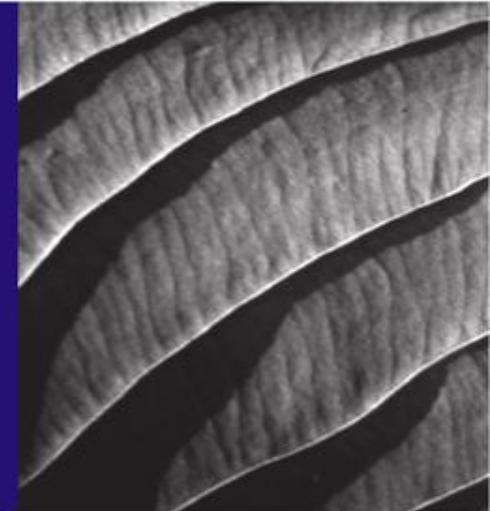




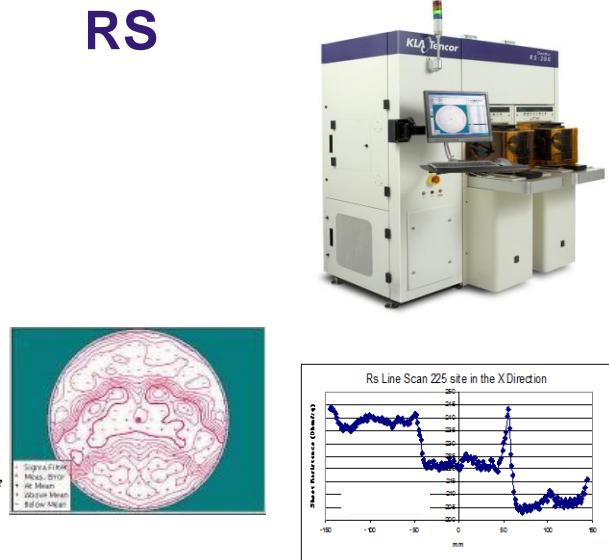
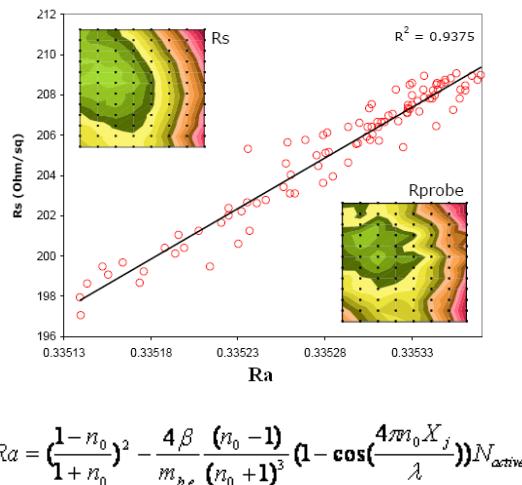
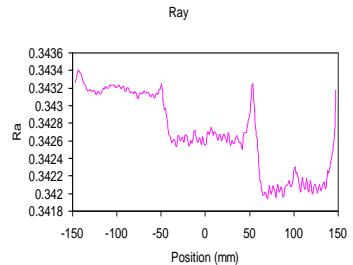
Optimizing the Synergy Between Thermal Wave and Sheet Resistance Measurements for the 15nm Node

Walt Johnson



Implant Monitor Solution with RS and TP

Therma-Probe

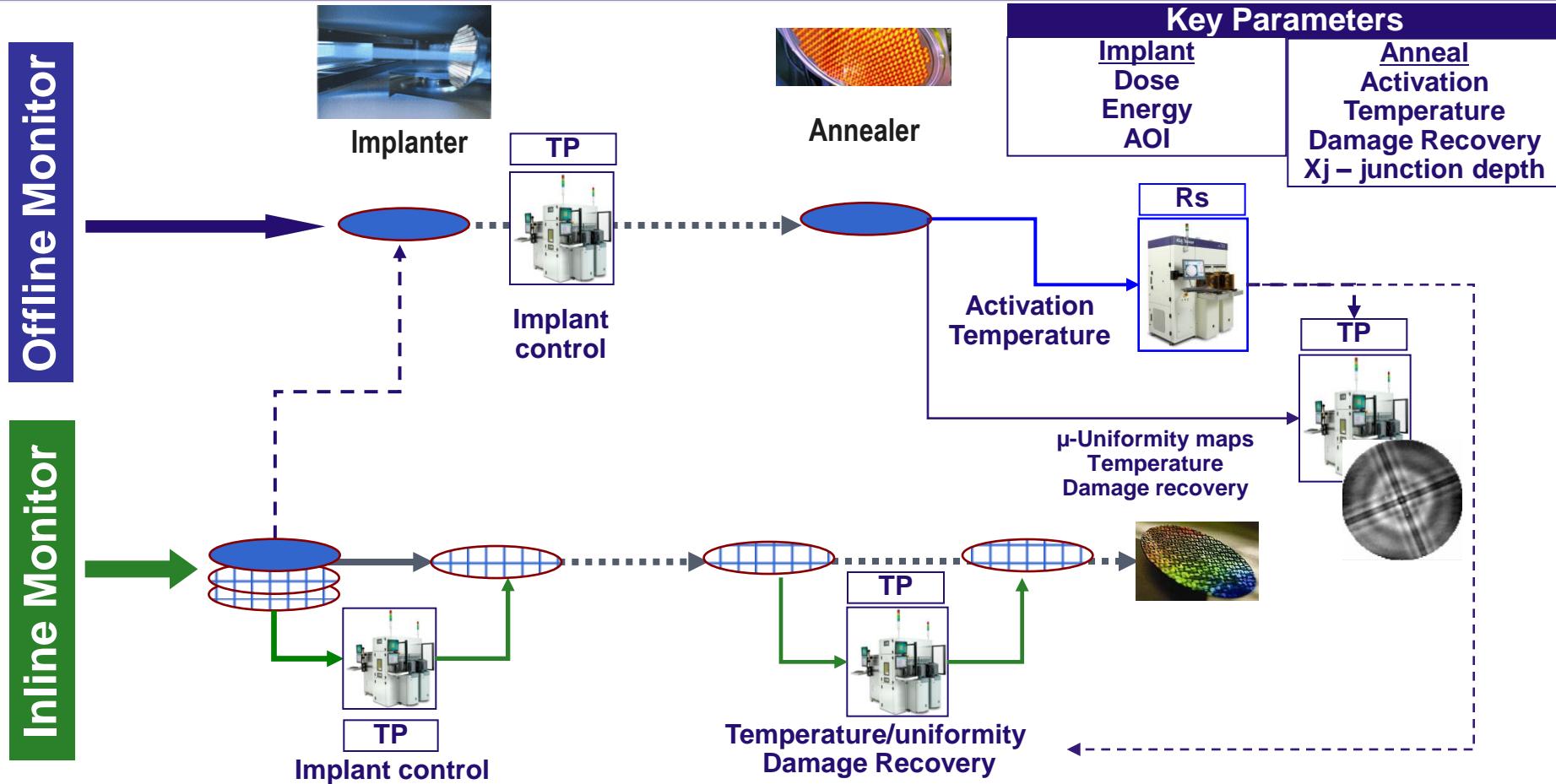


- Relative measurement, In-line on PW process control, optical micro-4PP
- High resolution, sensitive & stable
- Pre- and post- anneal
 - Decouples implant & anneal

- Absolute measurement, providing calibration for TP
- Industry standard NIST-traceable sheet resistances
- Post-anneal
 - activation

Implant & Anneal Control with Therma-Probe (TP) + Rs

TP + Rs provide full implant & anneal critical process parameters



TP + Rs provide complete metrology parameters for implant & anneal process

Rs for “reference” level Activation & junction electrical quality

TP for implant dose and post-anneal parameters (damage recovery & Xj)

TP680 Summary of Improvements

Signal and repeatability define practical tool performance

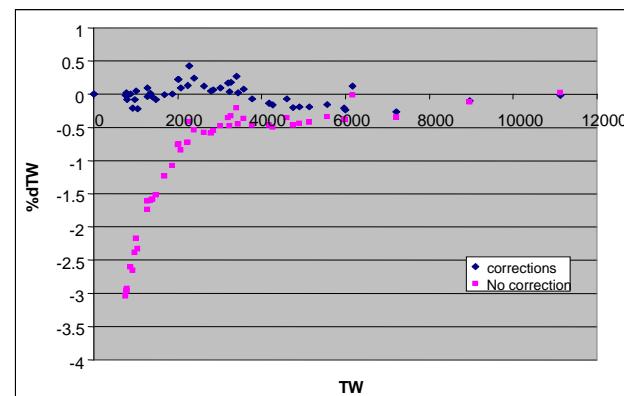
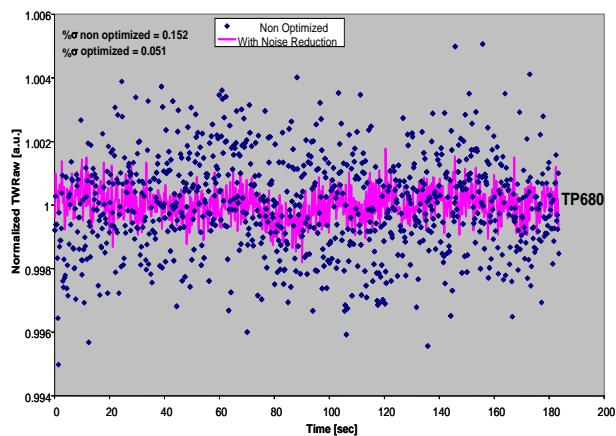
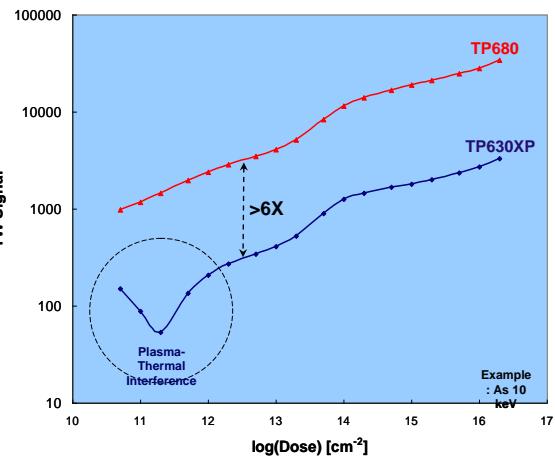
Dose Detectability (DD) = % Repeatability / S

$$= \frac{\% 3\sigma TW}{S}$$

Signal Strength

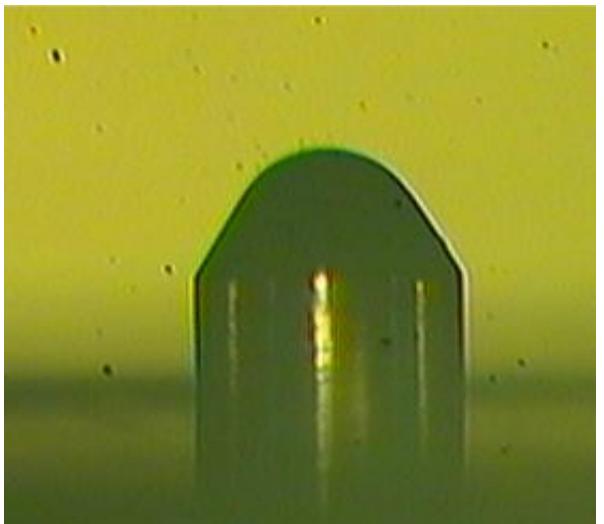
Low Noise

Stability



TP680 improves DD by $>2x$ compared to TP630XP

Hx probe tip design



Type G probe tip (1 of 4)

Tip radius:	Tip length:	Pitch:	Force:
~100 um	~ 1mm	25 mil	100 g

KT patented Hx tip design

Hx-100

Tip diameter: 0.1 mm

Tip length: 0.2 mm

(12) **United States Patent**
Johnson et al.

(10) Patent No.: **US 6,815,959 B2**
(45) Date of Patent: **Nov. 9, 2004**

WO 81/03396 * 11/1996 324/757
WO 81/03396 * 11/1996 438/17
5,744,971 A * 4/1998 Chan et al. 324/643
6,023,771 A * 2/2000 Boyette, Jr. et al. 324/754
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6,366,103 B1 * 4/2002 Cheng 324/754
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Search Report, application No. PCT/US 02/28954, mailed Dec. 31, 2002.
Nadeau et al., "Effect of Tungsten Probes on Wafer-Probe Yield in a Production Environment," Microelectronic Manufacturing and Testing, Sep. 1989, pp. 35-37.

* cited by examiner

Primary Examiner—Anjan K. Deb
(74) Attorney, Agent, or Firm—Ann Marie Mewherler; Conley Rose P.C.

(54) SYSTEMS AND METHODS FOR MEASURING PROPERTIES OF CONDUCTIVE LAYERS

(75) Inventors: Walter H. Johnson, San Jose, CA (US); Torsten Borchers, Pleasanton, CA (US); Daniel Griffing, Sunnyvale, CA (US); Andrei Danet, Concord, CA (US); George Erskine, San Jose, CA (US)

(73) Assignee: KLA-Tencor Technologies Corp., Milpitas, CA (US)

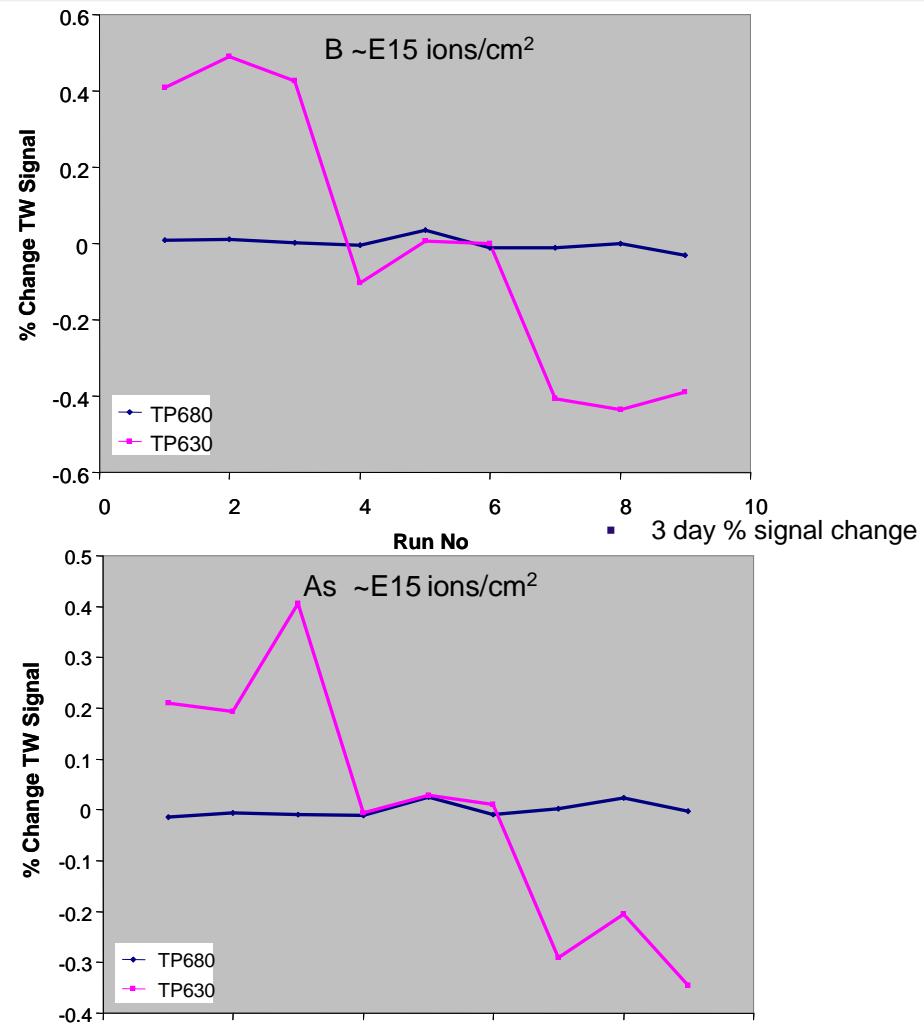
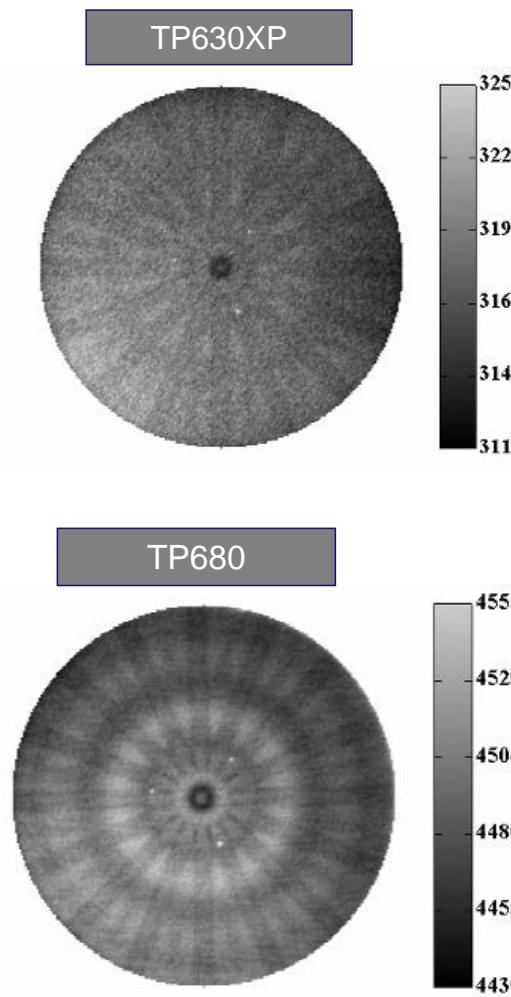
(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: **10/119,377**
(22) Filed: **Apr. 9, 2002**
(65) **Prior Publication Data**
US 2003/0060092 A1 Mar. 27, 2003

Larger contact area results on lower penetration and lower contact resistance.

Contact area does not change through useful life.

TP680 vs. TP630 Comparison



SN and long term stability of TP680 is greatly improved over TP630

TP & Rs Evaluation of Laser Beam Overlap

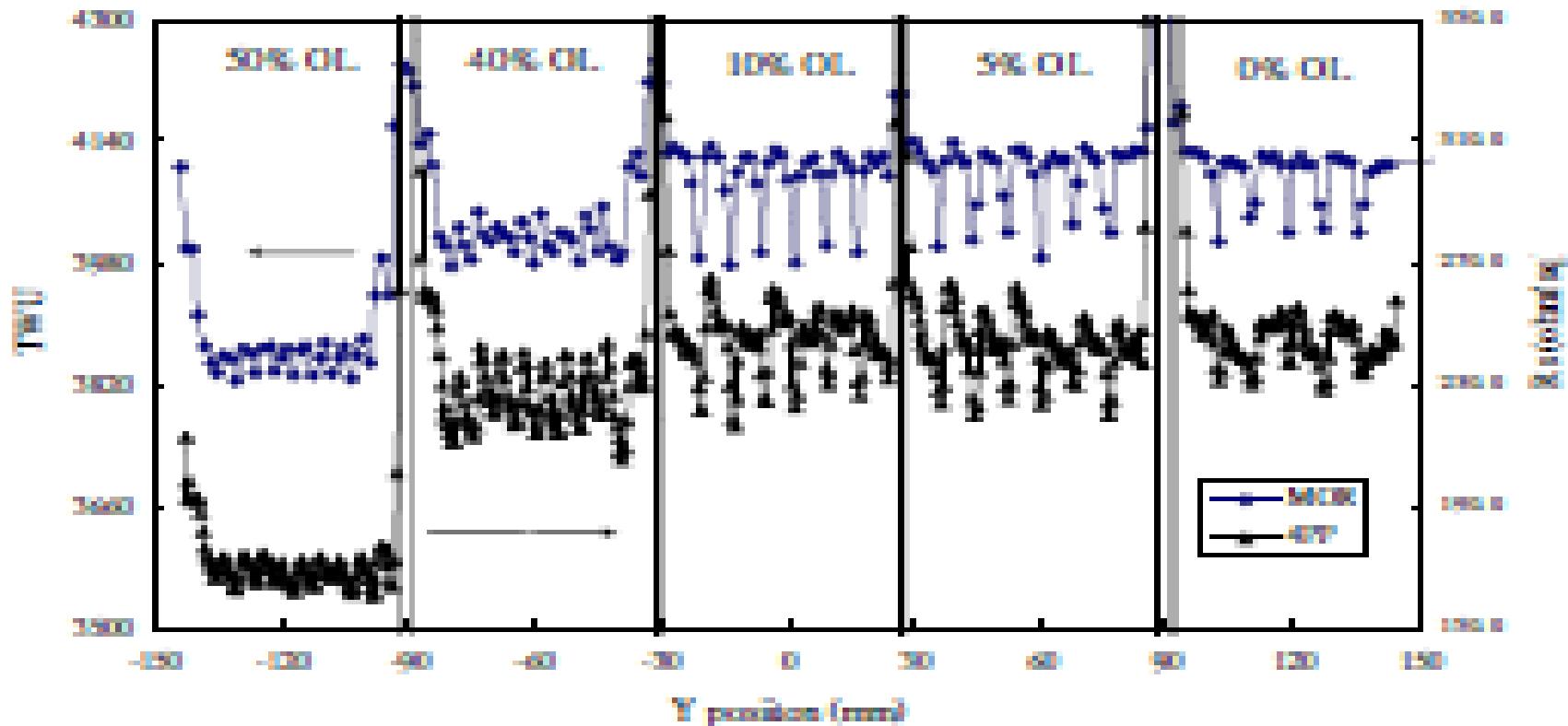
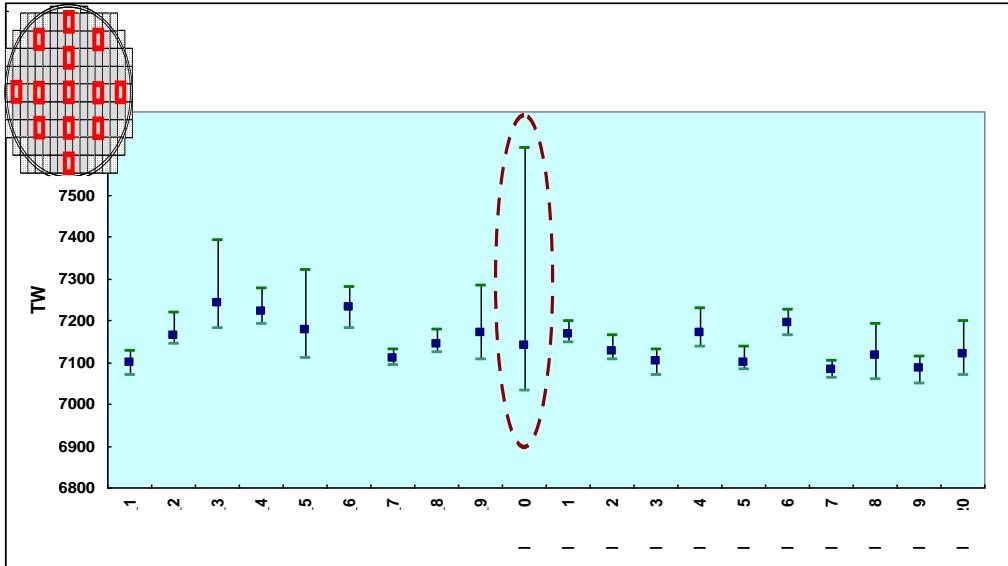


Fig.9: MOR and 4PP diameter scan result of wafer #7

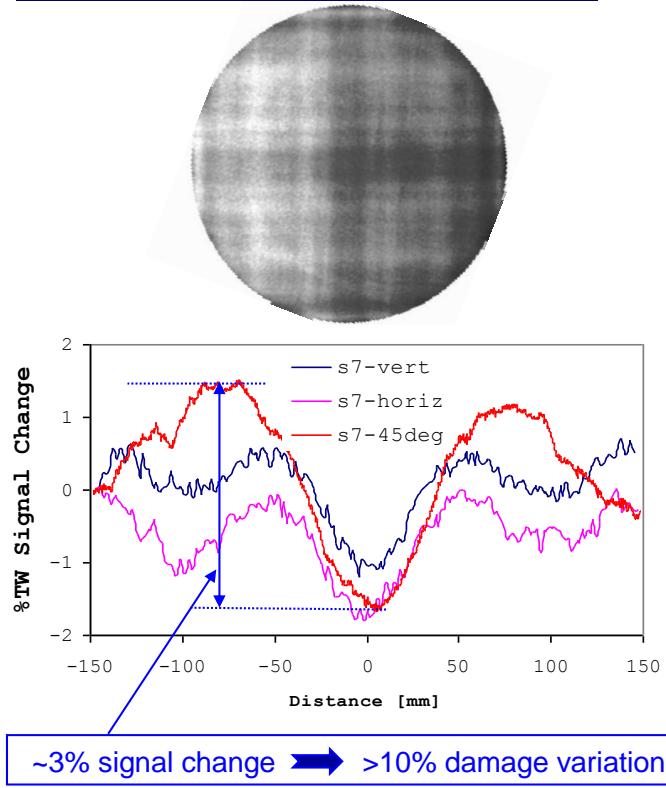
Is there any significance to the asymmetry shown in the Rs data?

TP Product Wafer Monitoring

Implant Excursion Monitoring on Product Wafer



Implant Uniformity Verification

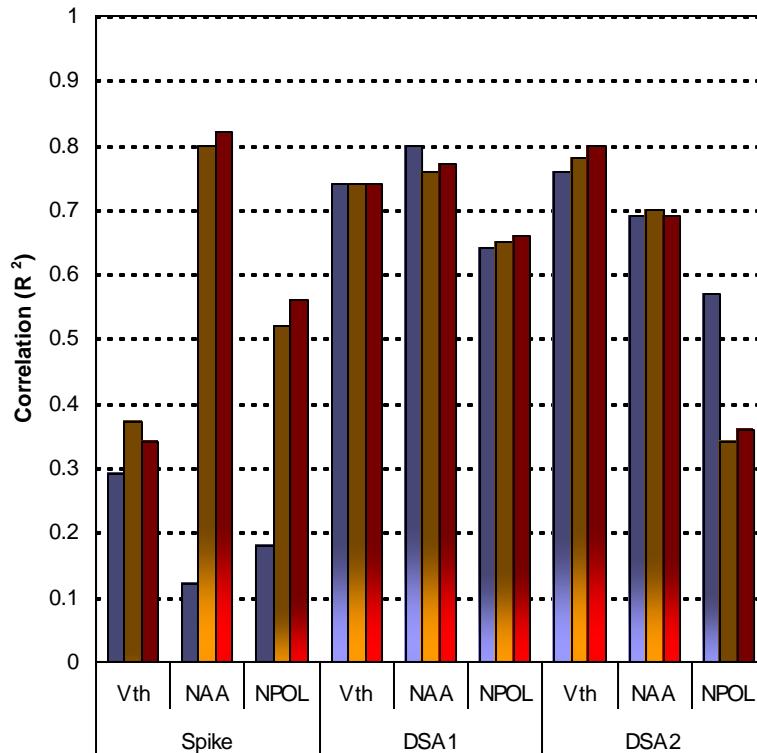


- Integration issues are only detected on product wafers
- Process excursions are identified earlier with product wafer monitoring

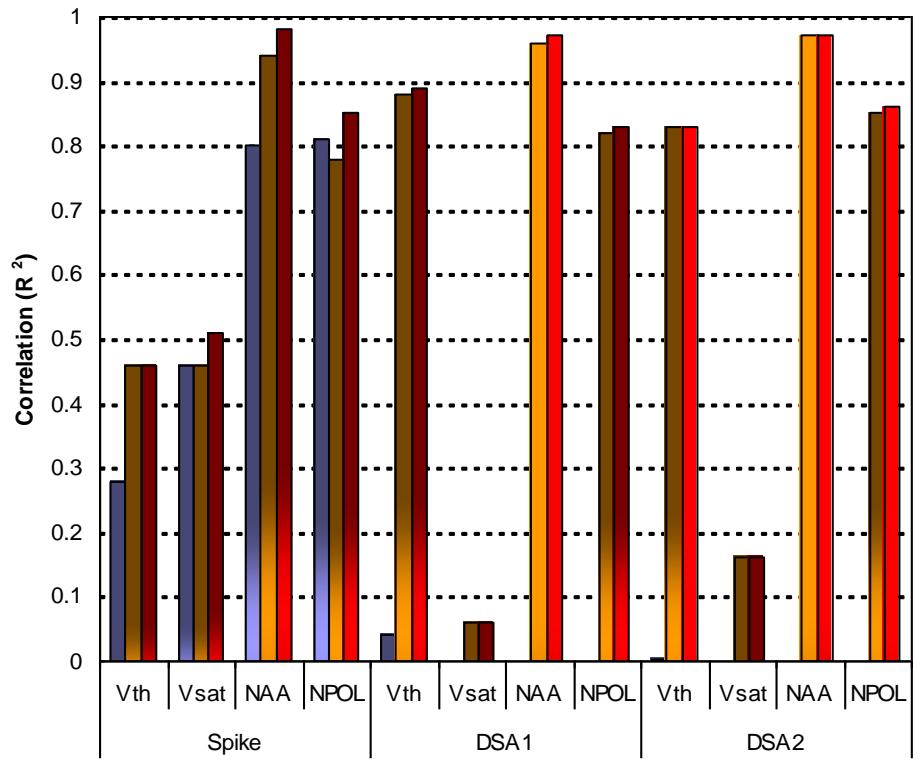
Checker patterns are signatures that result from quad-mode operation of a ribbon beam implanter

Correlation Summary: NMOS & PMOS

NMOS



PMOS



TWinf R_{probe} R_{pump}

Laser Spike Anneal Macro & Micro Non-uniformity Investigation Using Modulated Optical Reflectance

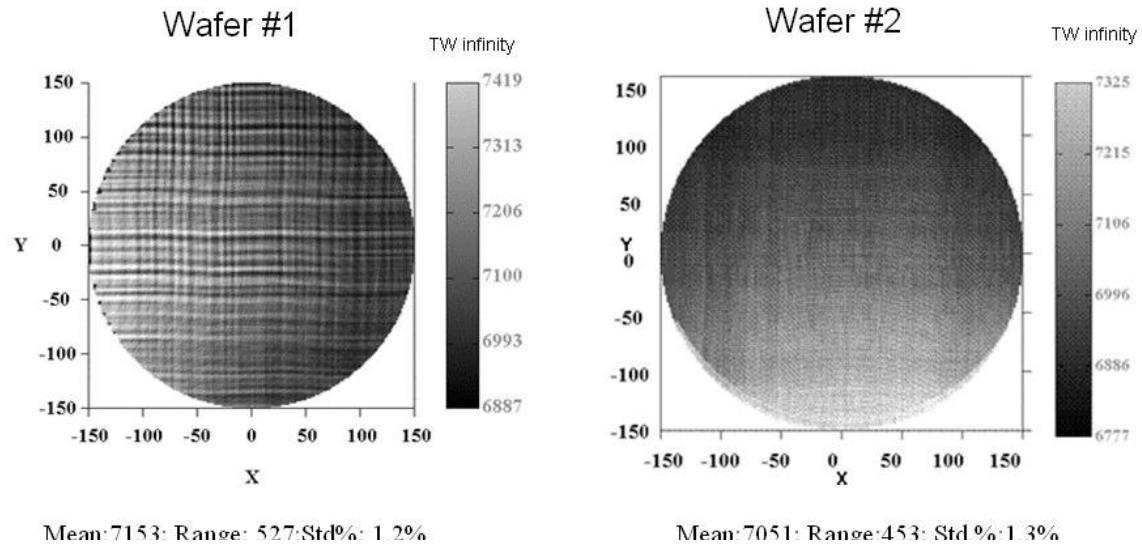


Fig.2: Thermawave full-mapping for wafer#1 (left) and wafer#2 (right).

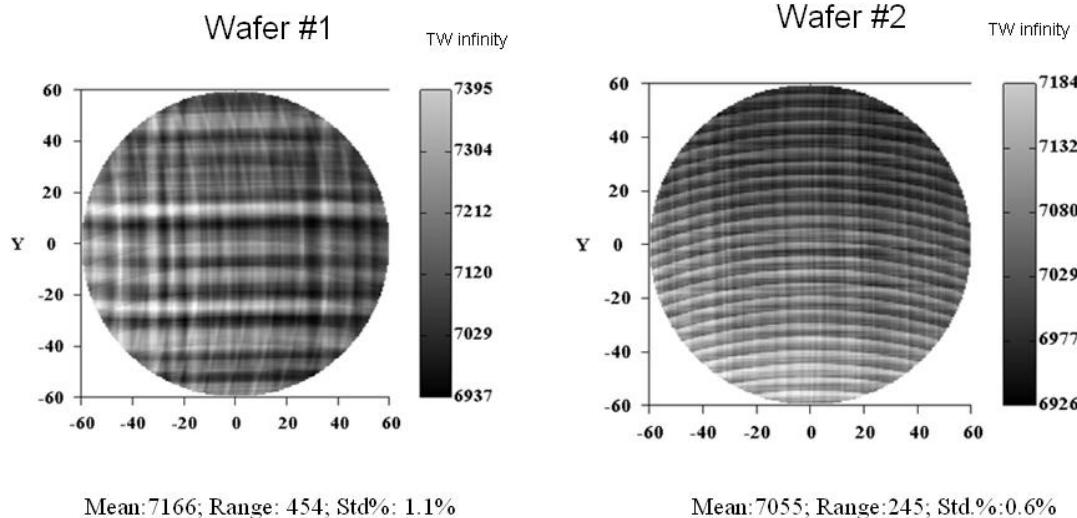
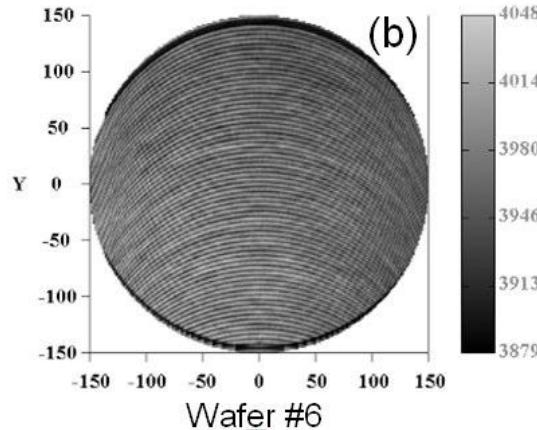
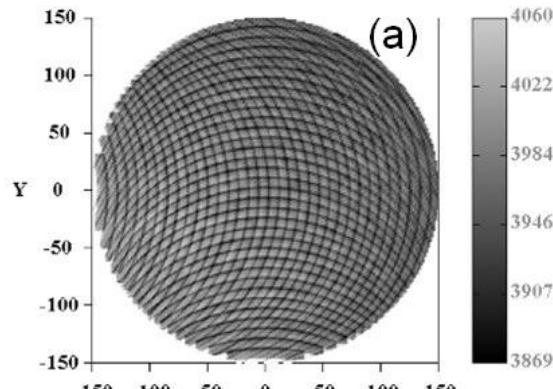
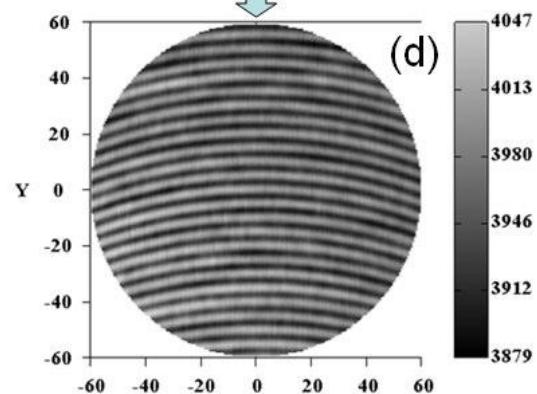
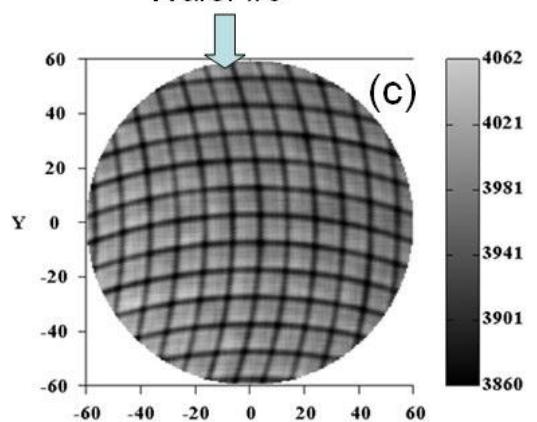


Fig.3: Localized MOR mapping with high resolution for wafer#1 (left) and wafer#2 (right).

High Resolution Zoomed Images



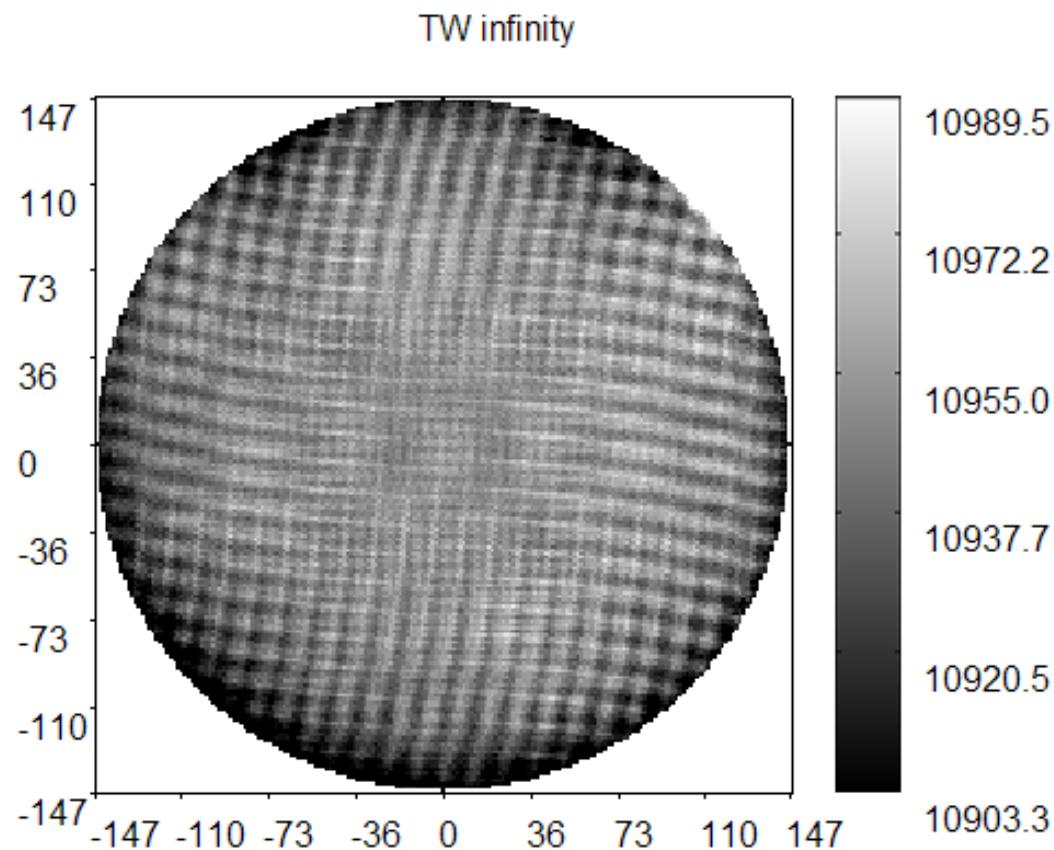
Due to the two periodic patterns (implanter scan and TP scan) a Moiré effect occasionally can be seen.



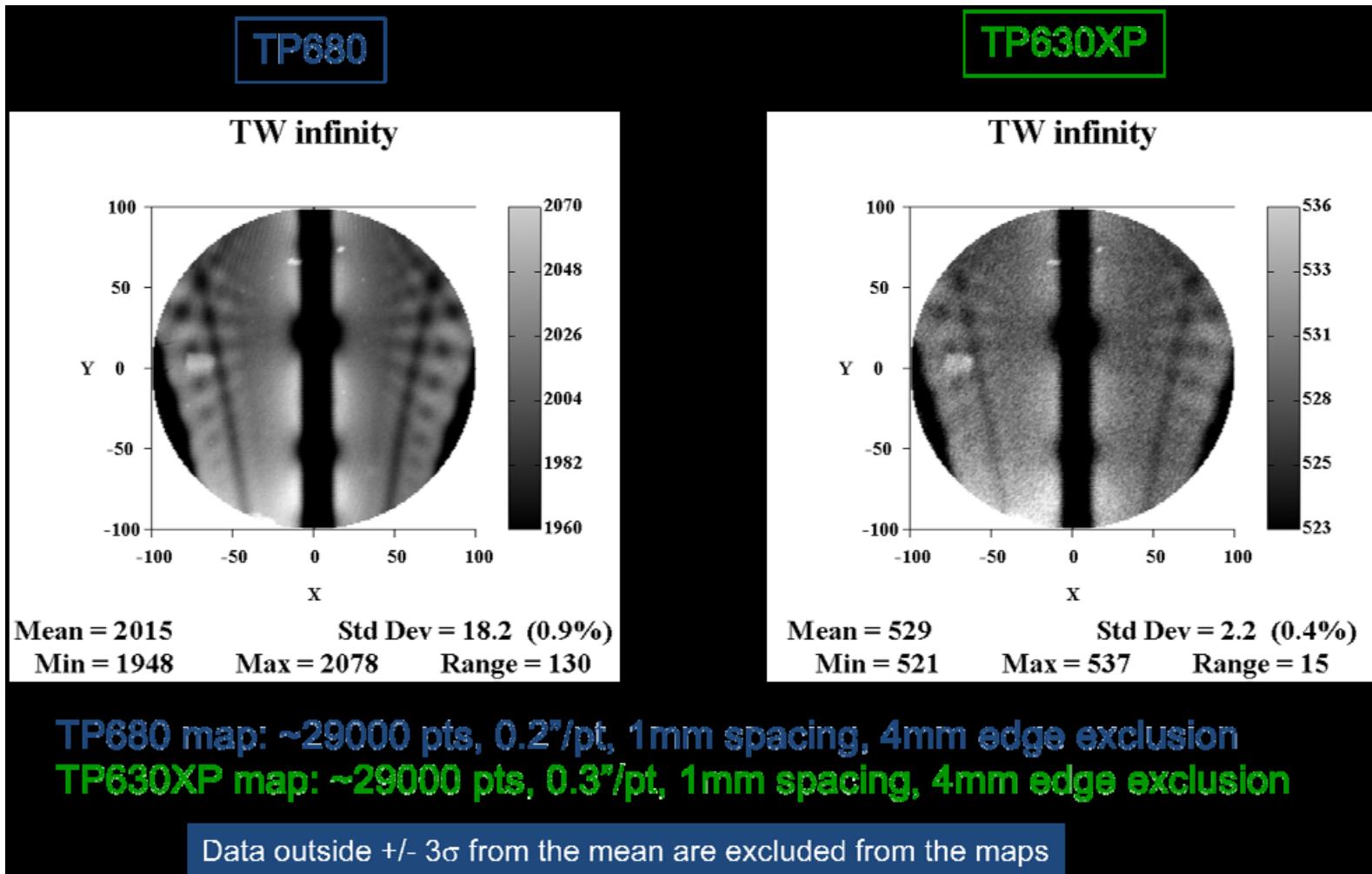
A higher resolution image not only gives more information about the wafer pattern but will typically eliminate the Moiré pattern.

Fig.7: Global (low resolution) and localized (high resolution) MOR map: (a), wafer#5 global, (b), wafer #6 global, (c) wafer #5 local, (d),wafer#6 local.

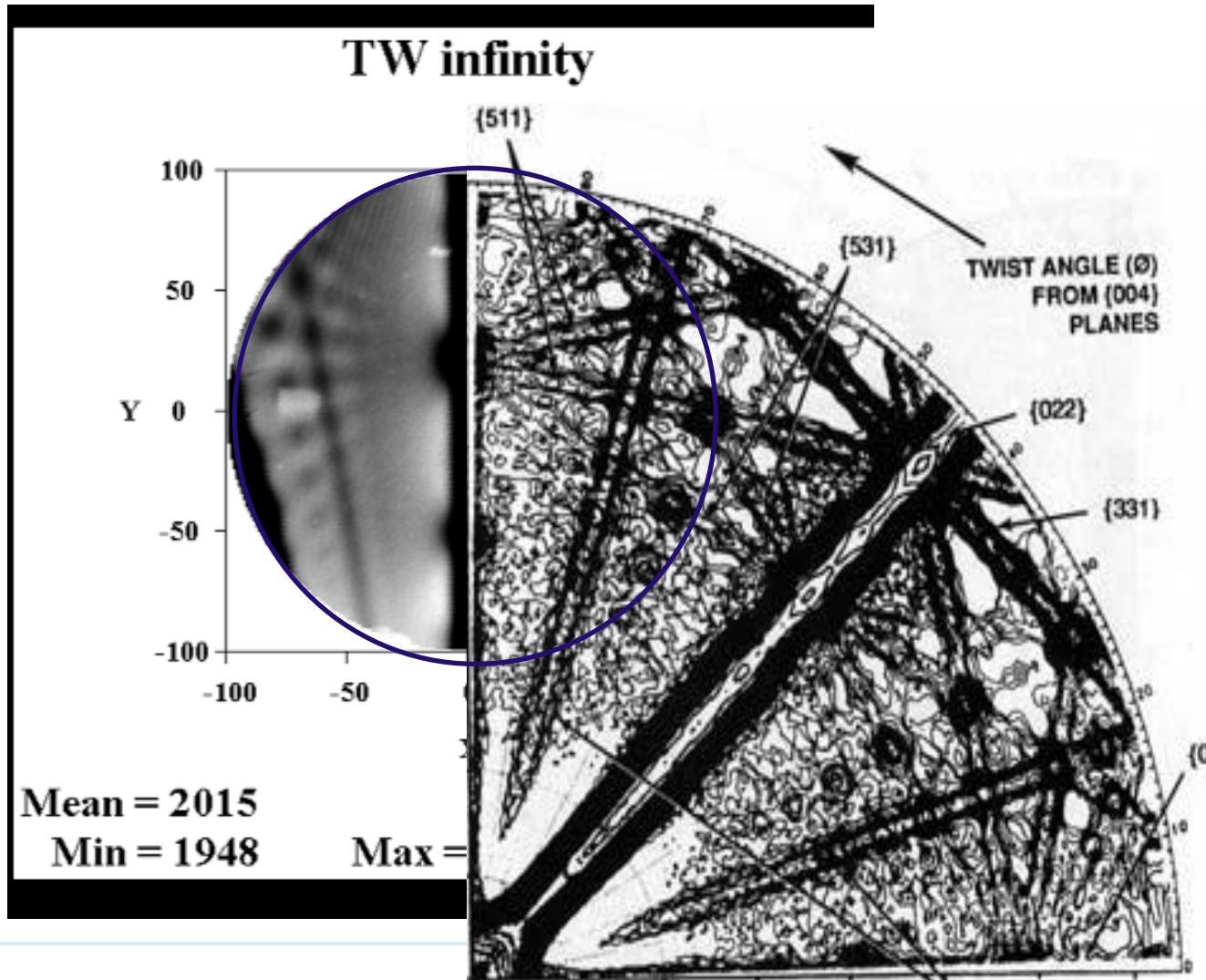
One of the More Interesting TP680 μ Maps



TP680 Twice the Resolution in Half the Time

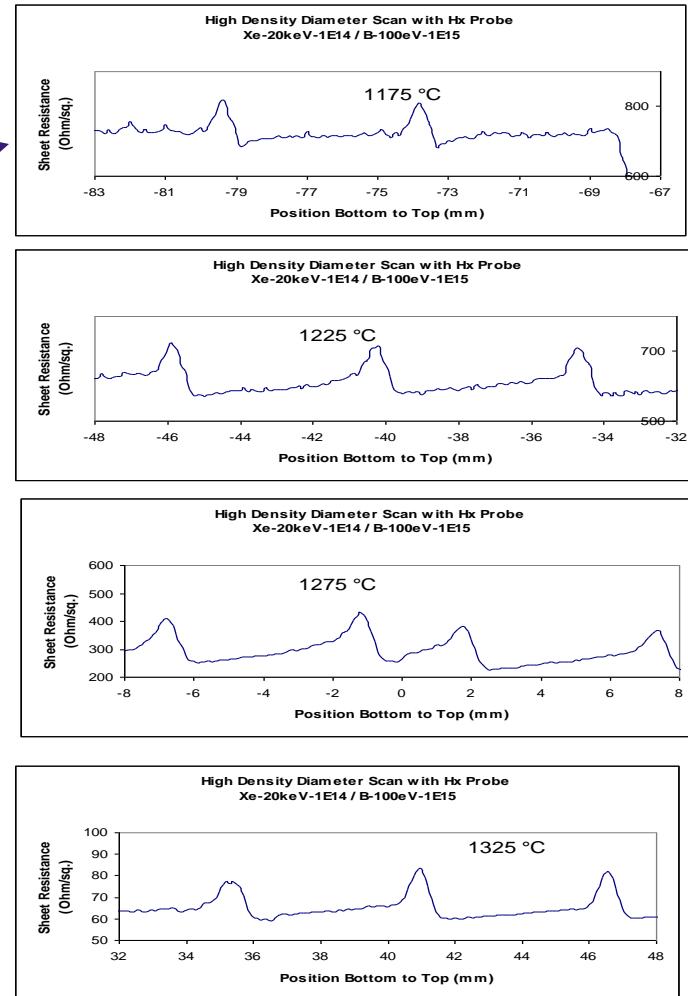
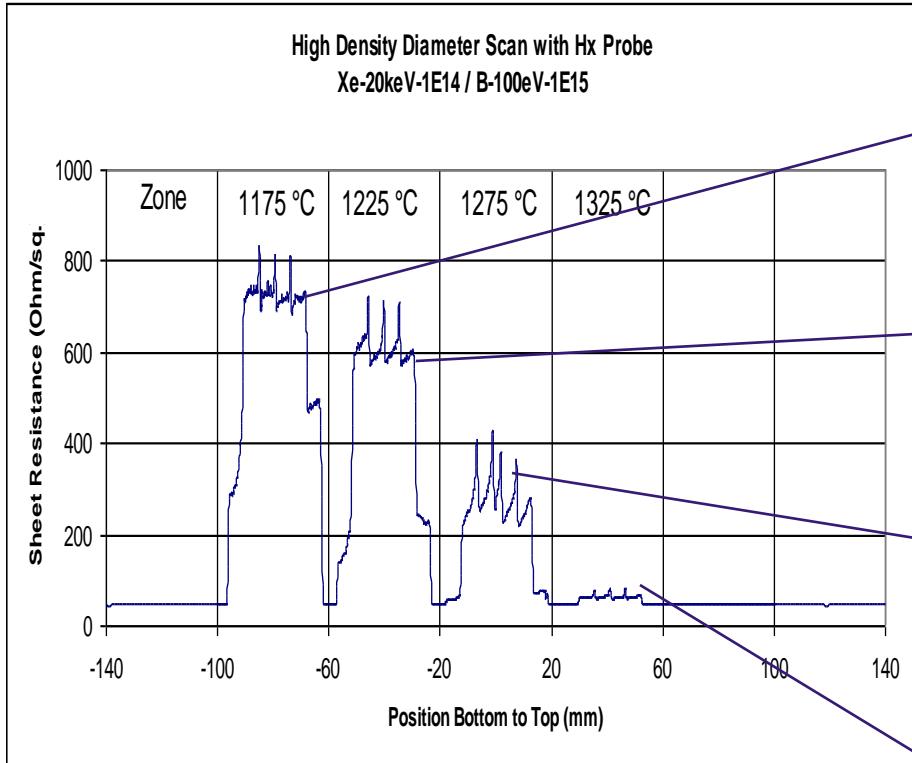


TP680 µMap and Crystal Channel Map



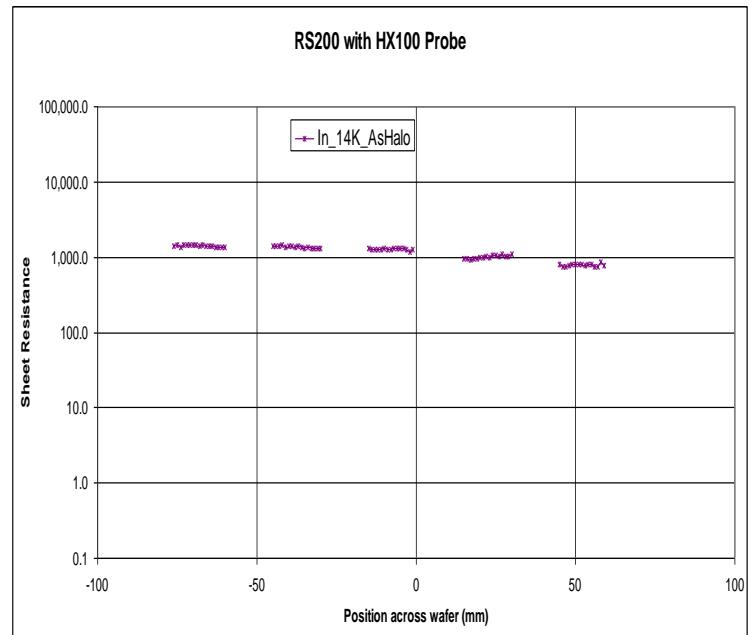
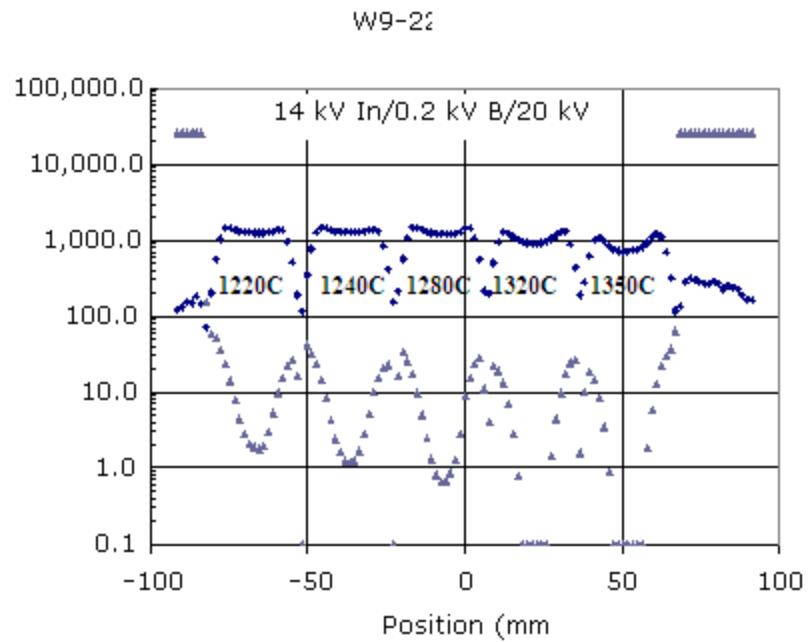
Hx Probe Better s/n for USJ uniformity

--- Intel 22nm Node USJ Implant (~5nm X_j)



Each temperature Zone shows the stitching pattern of the laser annealer.

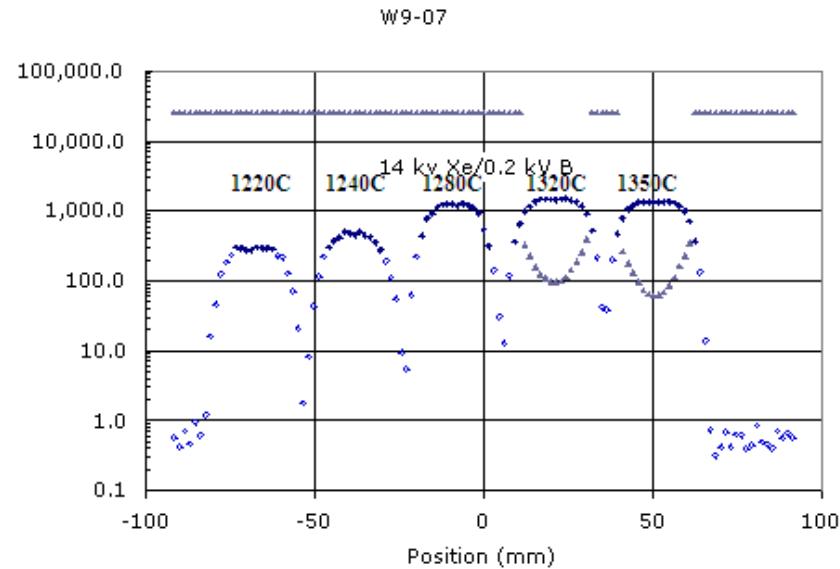
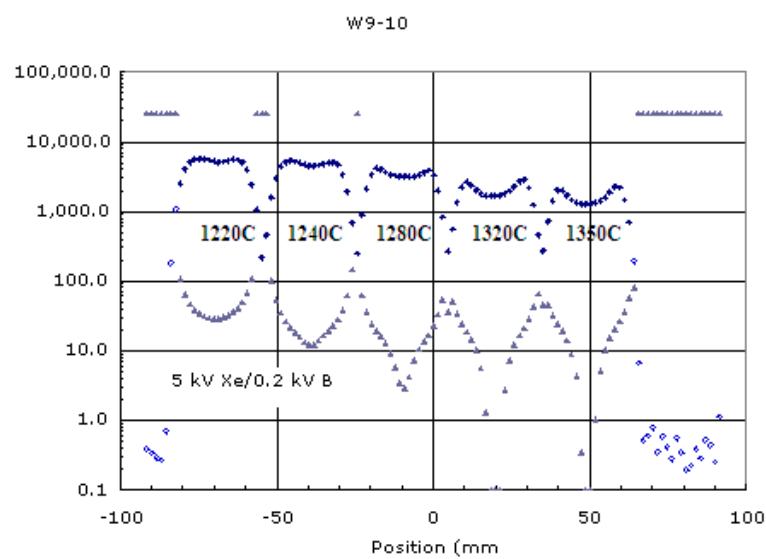
As-HALO 14keV In-PAI RsL & Hx results



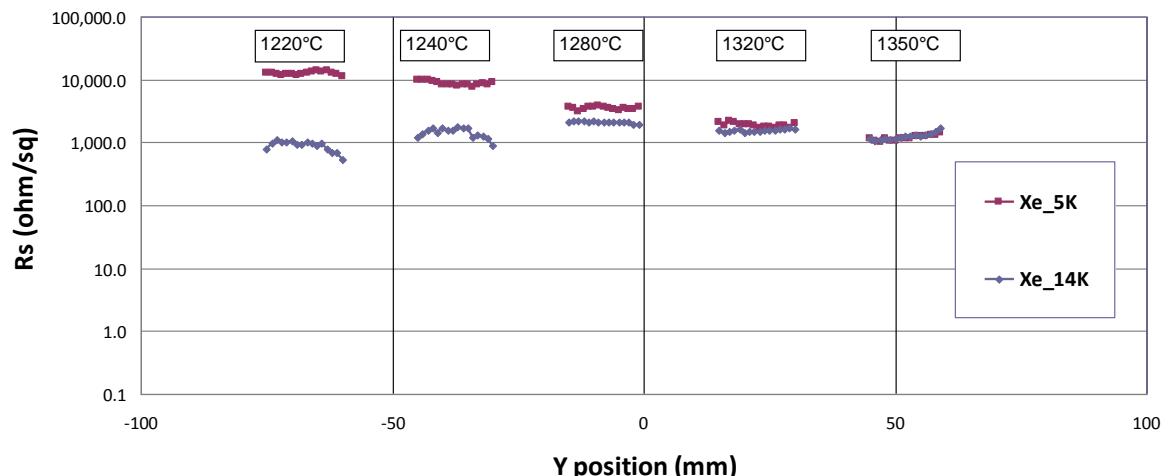
**22nm Node p+ USJ Formation Using PAI & HALO Implantation
With Laser Annealing**

John O. Borland¹, John Marino², Michael Current³ and Blake Darby⁴

No HALO 5 & 14keV Xe-PAI

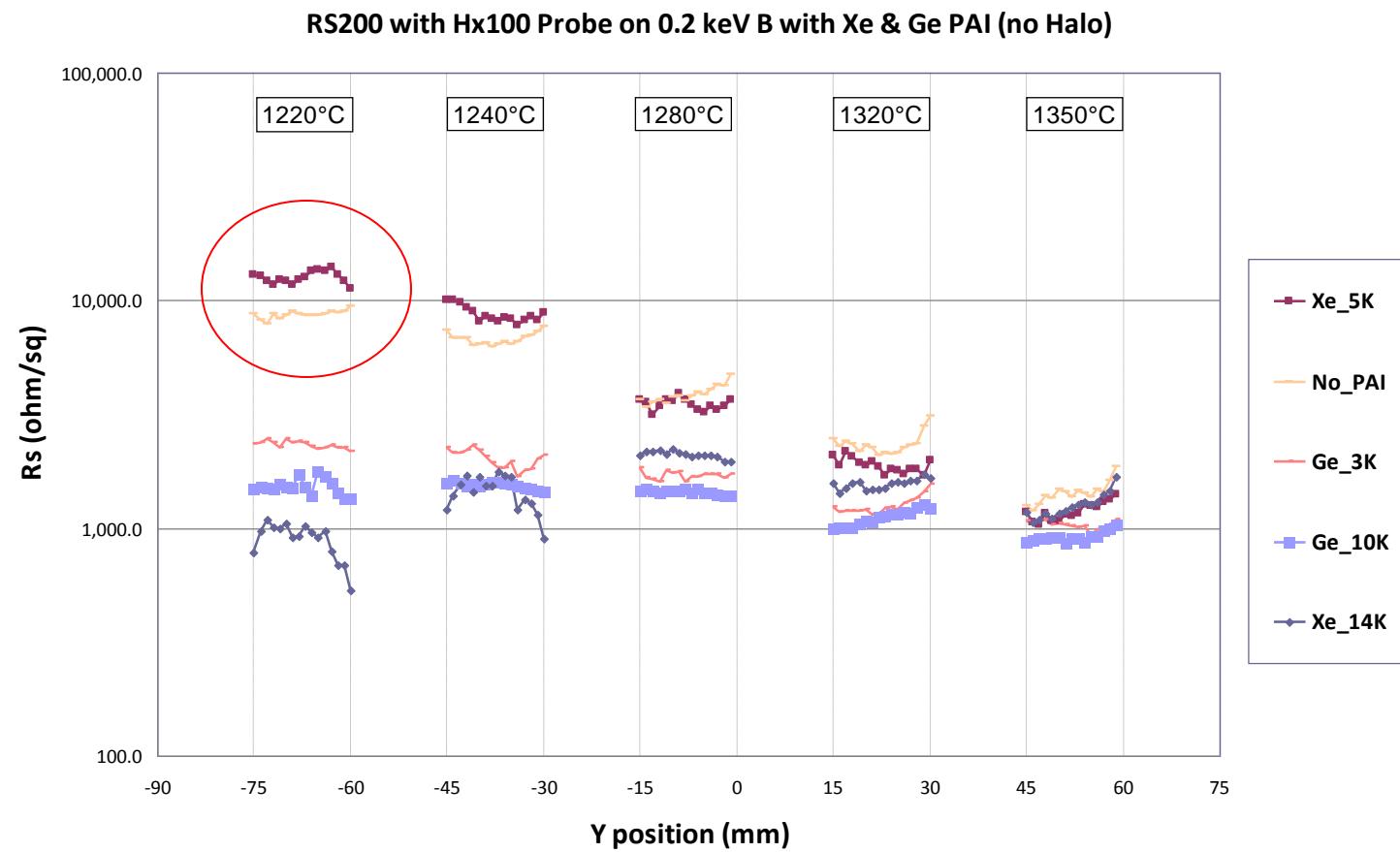


RS200 with Hx100 Probe on 0.2 keV B with Xe PAI (no Halo)



Hx probe shows higher Rs (less leakage?) for 1220 ° C and 1240 ° C anneals for both 5K and 14K Xe PAI.

Hx Probe on Xe and Ge PAI Implants



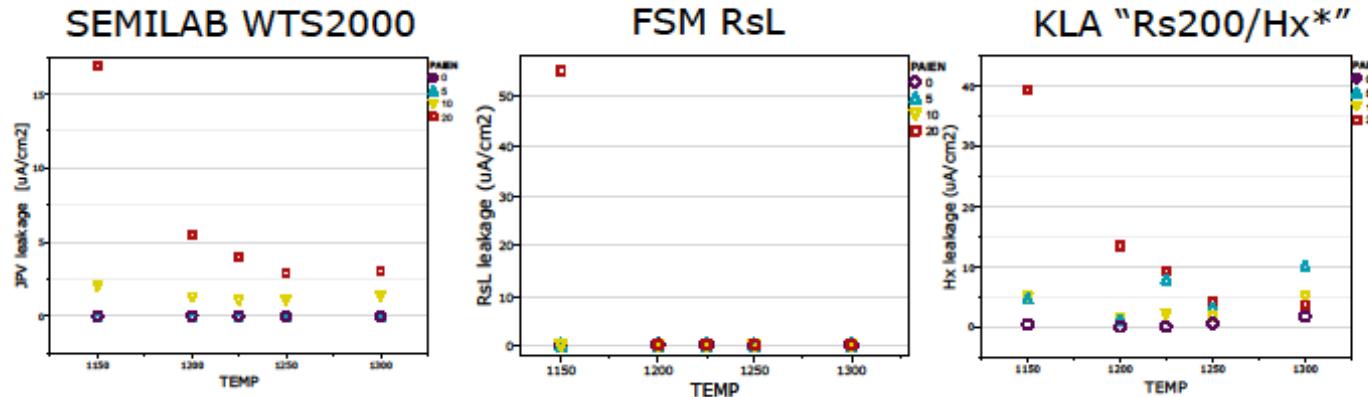
Hx probe results indicated higher R_s for the low temperature 5K Xe implants relative to the no PAI reference. RsL results showed the opposite.

Is this real?

Study of sub-melt laser damage annealing using Therma-Probe

Different leakage measurements

Preliminary data

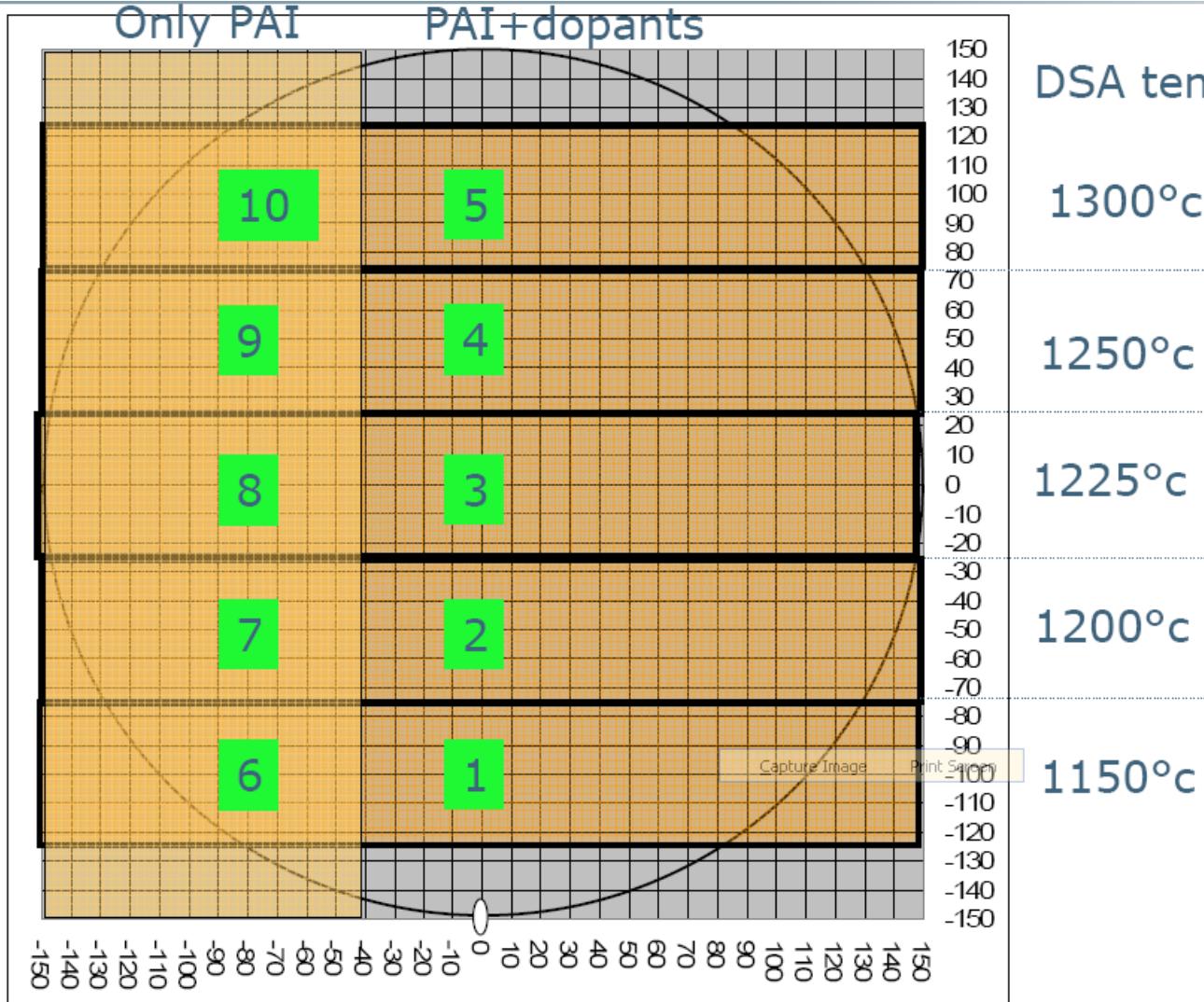


- What leakage values to trust ?? > Need for more leakage understanding
- SEMILAB/KLA seem to follow +/-similar trend
- Not always monotonic with temperature

EOR damage Study

AE090326 KLA JDP		S1 : B reference wafers	S2 : Ge/B mask													S3 : F/B Blank ref for device exp AMPT090				S4 : BF2						
		F02	D03	D04	D05	D06	D07	D08	D09	D10	D11	D12	D13	D14	D15	D16	D17	D18	D19	D20	D21	D22	D23	T25		
	VERSION V5 (28/05/09)																									
7001	NMON F400	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		
	Basic Clean	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		
	PAI																									
3000	Ge 5KeV Q1 tw0-ti0 1.0e14/cm2			x	x																					
3000	Ge 5KeV Q1 tw0-ti0 5.0e14/cm2					x	x																			
3000	Ge 10KeV Q1 tw0-ti0 1.0e14/cm2							x	x												x	x				
3000	Ge 10KeV Q1 tw0-ti0 5.0e14/cm2									x	x											x	x			
3000	Ge 20KeV Q1 tw0-ti0 1.0e14/cm2										x	x														
3000	Ge 20KeV Q1 tw0-ti0 5.0e14/cm2											x	x													
3000	F 10KeV Q1 tw0-ti0 2.0e15/cm2												x	x	x											
	LITHO																									
6400	Half-wafer Litho - Clear right-39mm	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		
	Active implants																									
3000	B 0.5KeV Q1 tw0-ti0 5e14/cm2	x	x	x	x		x	x	Capture Image	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		
3000	B 0.5KeV Q1 tw0-ti0 1e15/cm2		x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		
3000	B 0.5KeV Q1 tw0-ti0 7e14/cm2																	x	x	x						
3000	BF2 2.2 keV (= B 0.5KeV eq.) Q1 tw0-ti0 5e14/cm2																			x	x	x				
3000	BF2 2.2KeV (= B 0.5KeV eq.) Q1 tw0-ti0 1e15/cm2																			x	x	x	x	x	x	
	STRIP																									
7700	junction strip	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		
	TP post implant measurement																									
7480	TP measurement (std & tracker scans)	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		
	Anneal																									
2700	DSA 6 zones (1150-1300)	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		
2600	spike anneal 850C																		x							
2600	spike anneal 950C																		x							
	TP postmeasurement																									
7480	TP measurement (std & tracker scans)	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x		

Wafer Layout



Minimal Leakage with 5 keV Ge PAI

No PAI

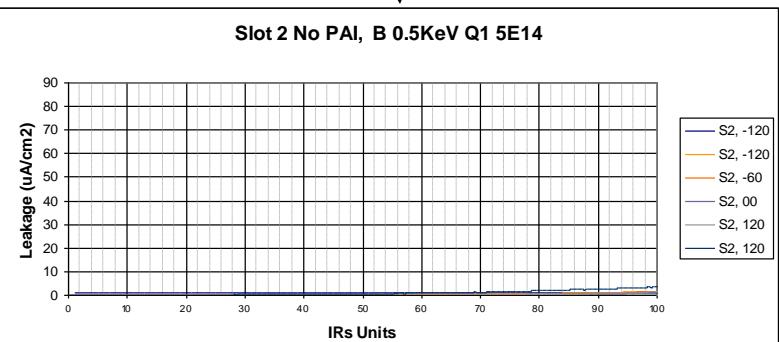


1E14
5k
Ge
PAI

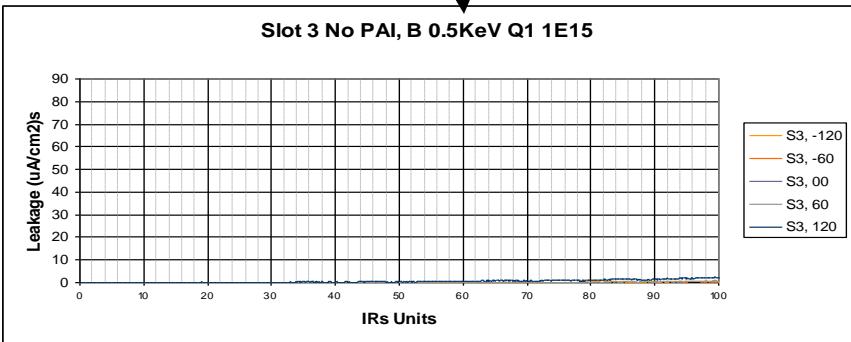


5E14
5k
Ge
PAI

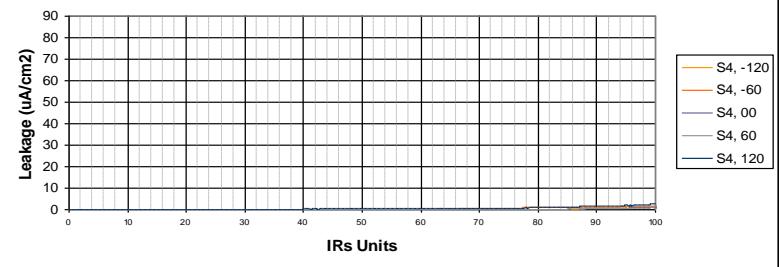
5E14B implants ↓



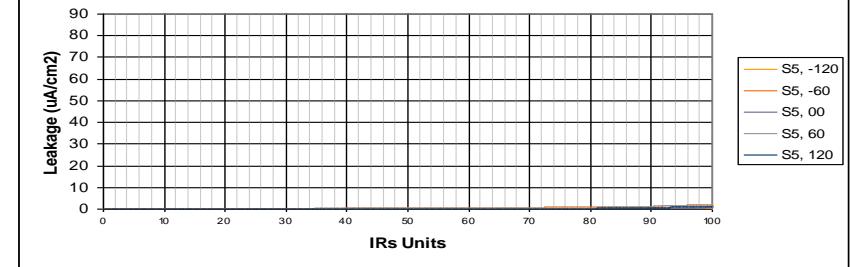
1E15B implants ↓



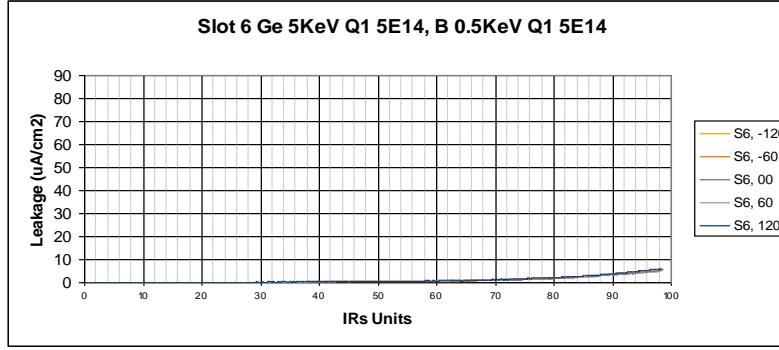
Slot 4 Ge 5KeV Q1 1E14, B 0.5KeV Q1 5E14



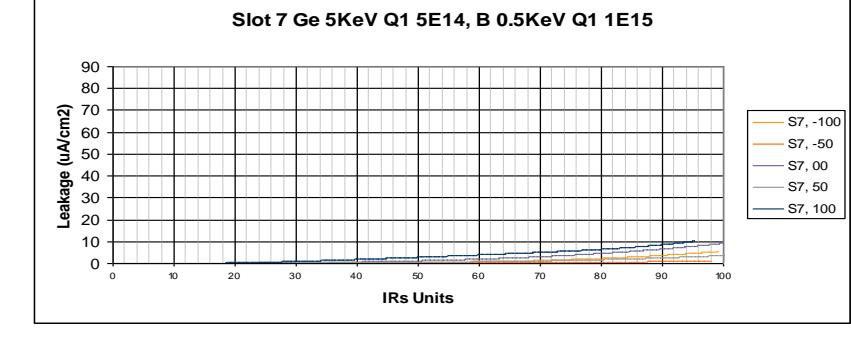
Slot 5 Ge 5KeV Q1 1E14, B 0.5KeV Q1 1E15



Slot 6 Ge 5KeV Q1 5E14, B 0.5KeV Q1 5E14



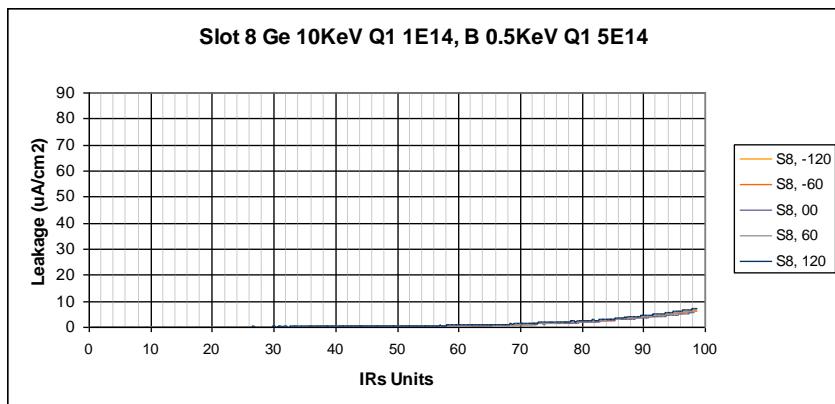
Slot 7 Ge 5KeV Q1 5E14, B 0.5KeV Q1 1E15



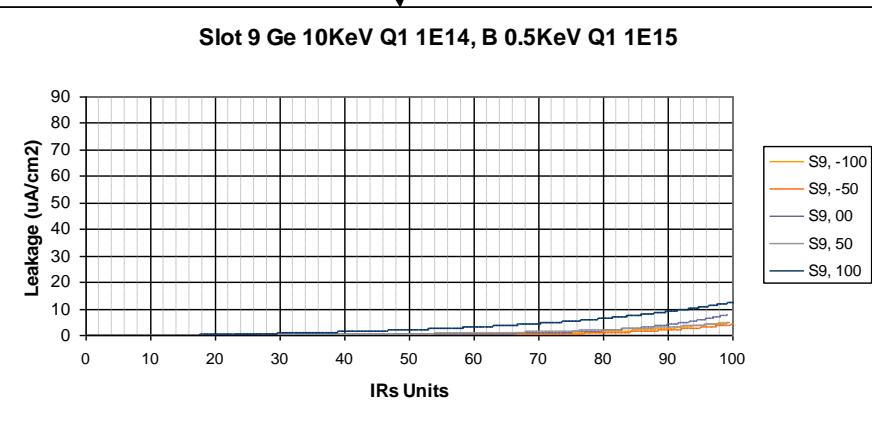
Higher Dose 10 keV Ge PAI Shows Leakage

1E14
10k
Ge
→

5E14B implants ↓

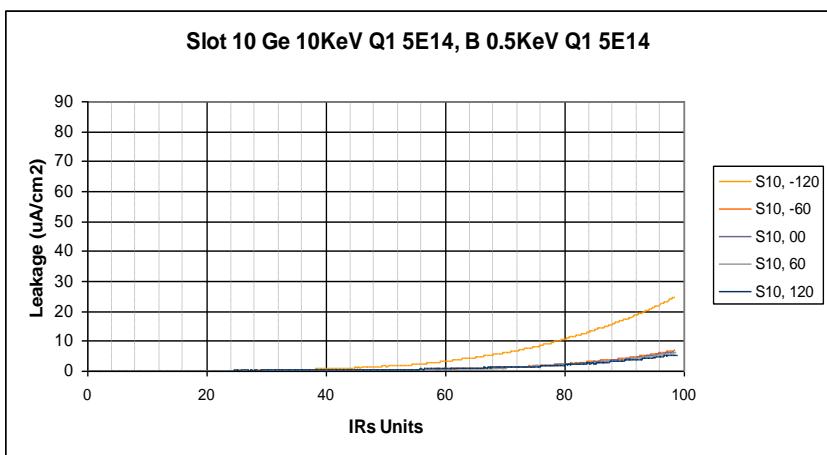


1E15B implants ↓

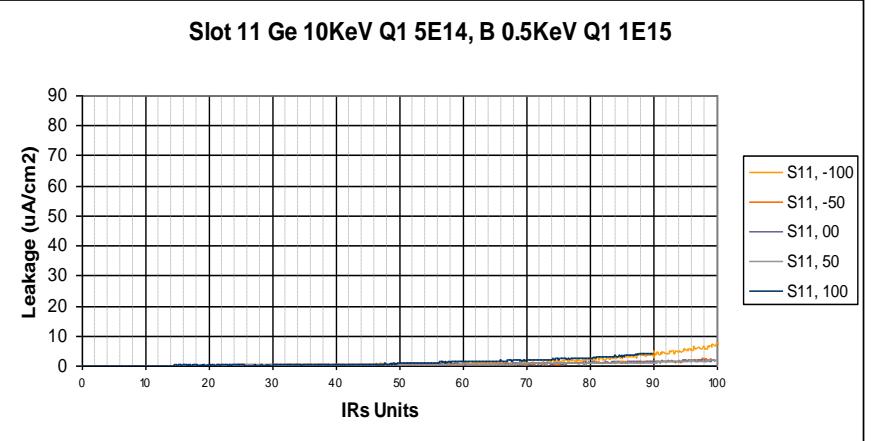


5E14
10k
Ge
PAI
→

Slot 10 Ge 10KeV Q1 5E14, B 0.5KeV Q1 5E14



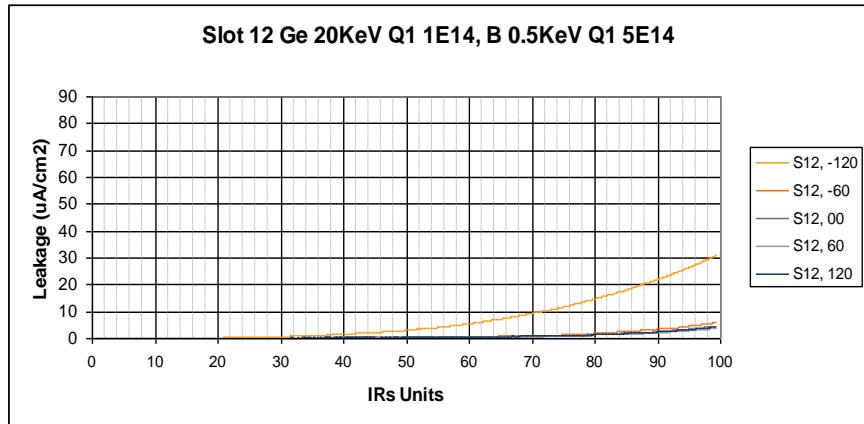
Slot 11 Ge 10KeV Q1 5E14, B 0.5KeV Q1 1E15



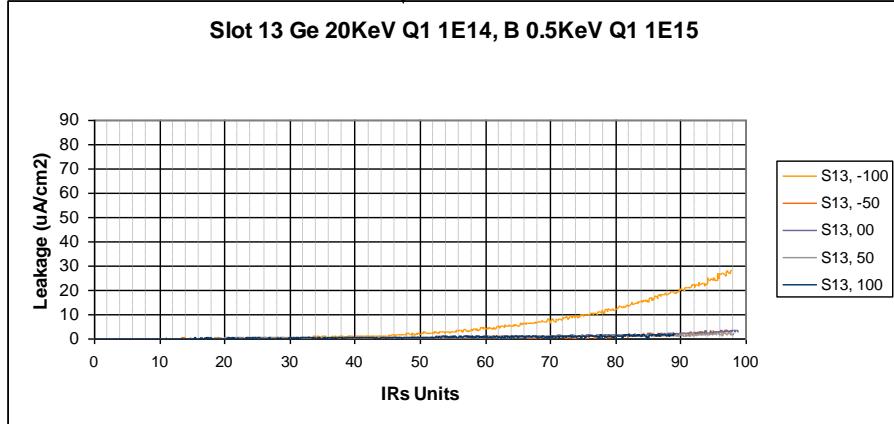
Substantial Leakage with 20keV Ge PAI

1E14
20k
Ge
PAI

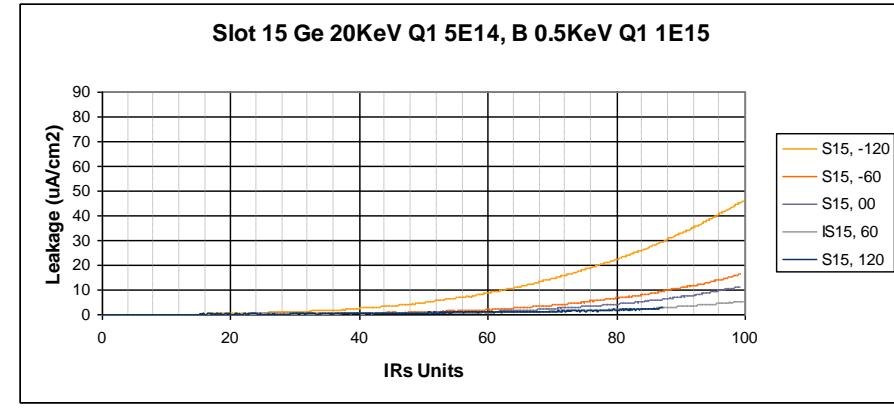
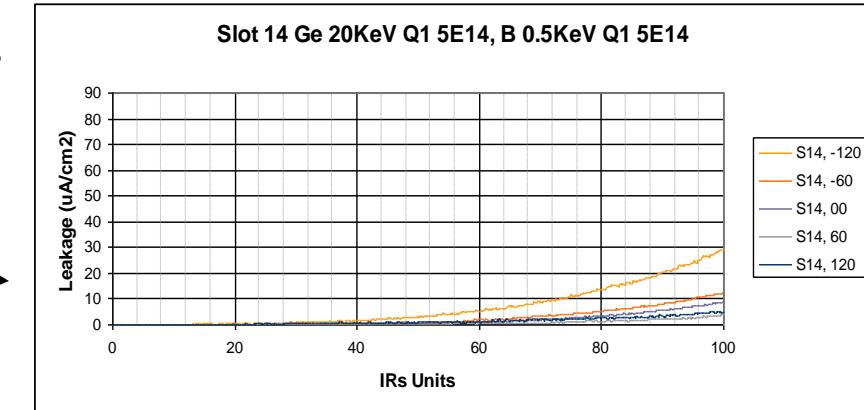
5E14B implants ↓



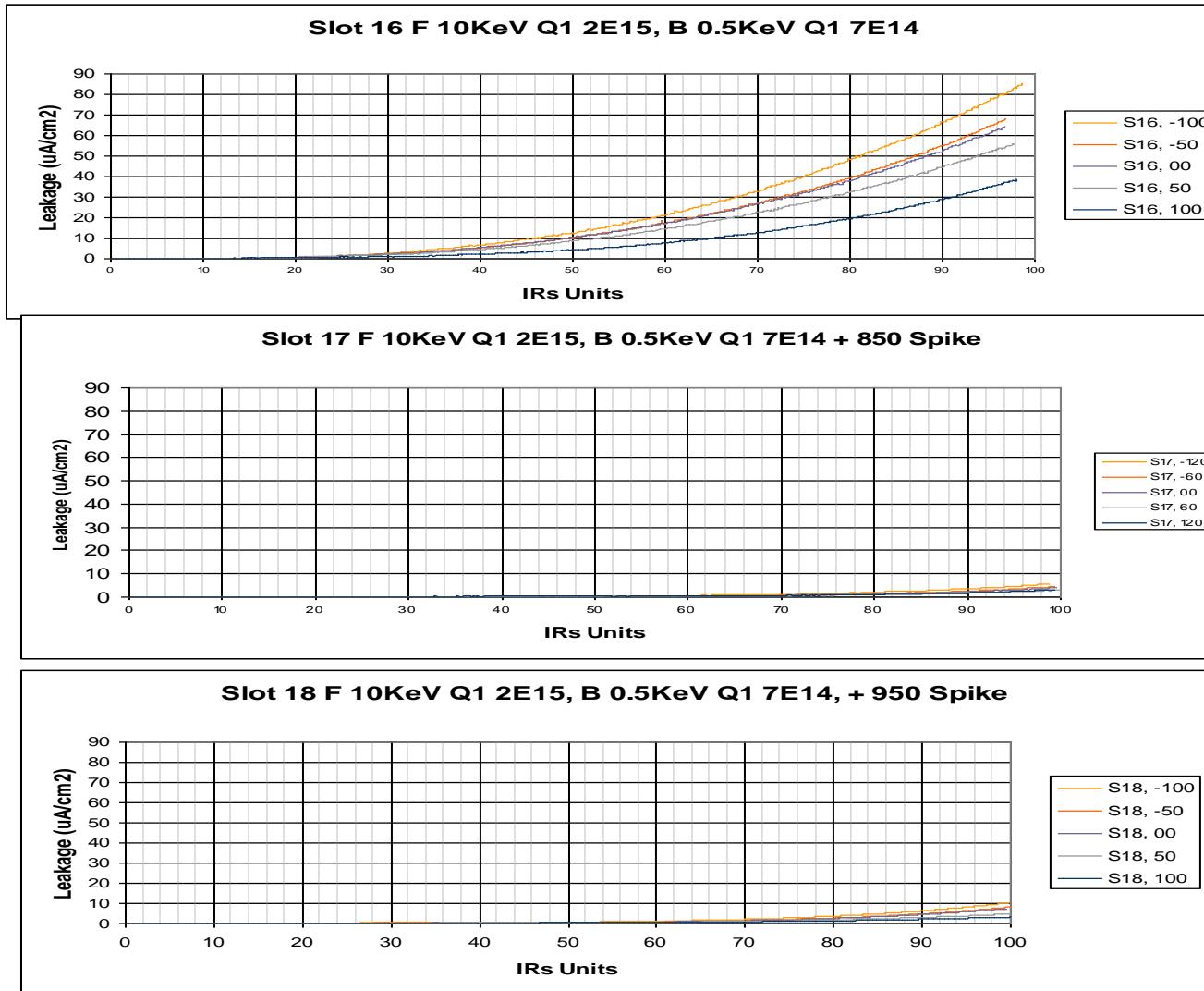
1E15B implants ↓



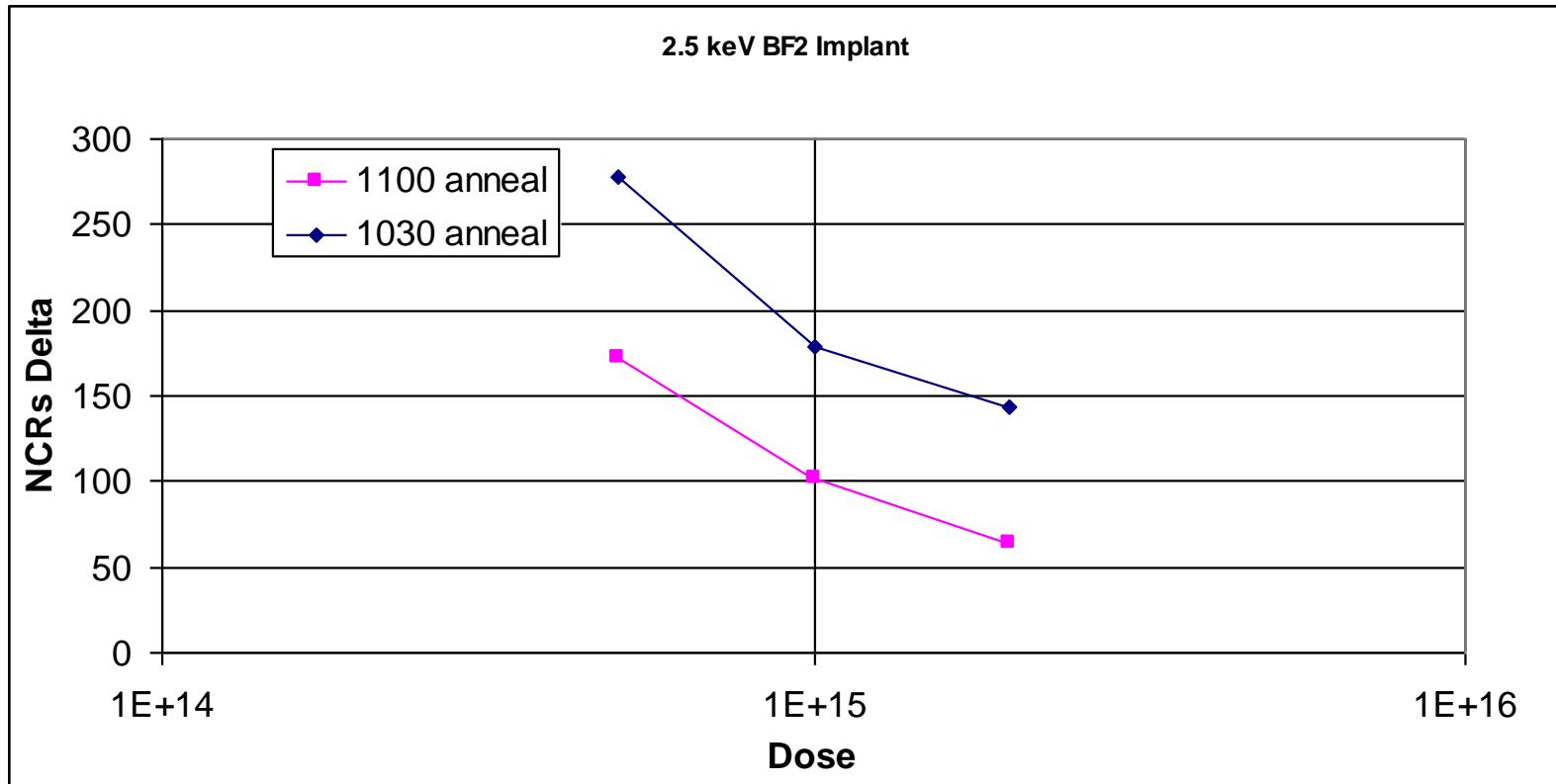
5E14
20k
Ge
PAI



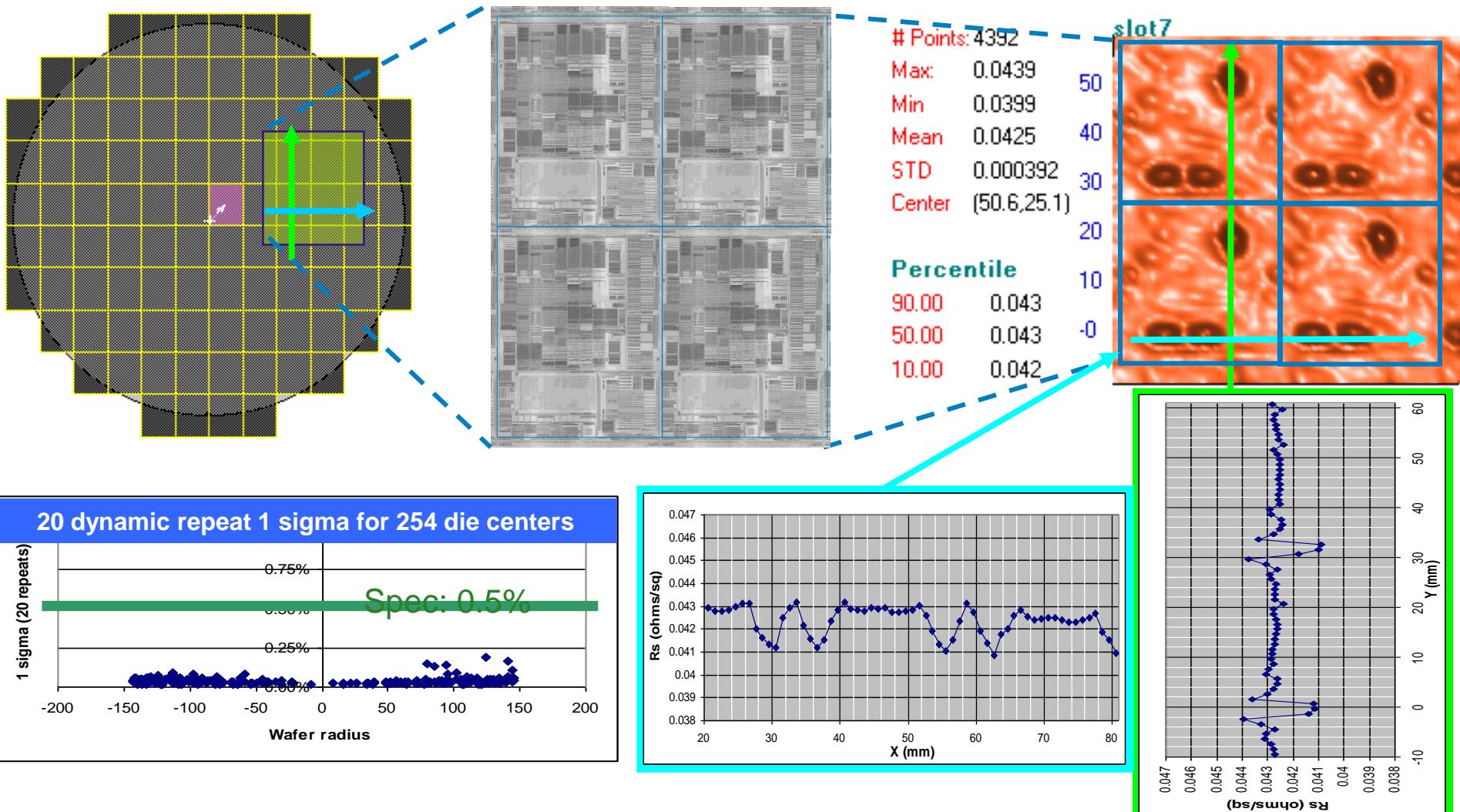
Spike Substantially Reduces Leakage



RS300 Eddy Current Test on P/P Implant

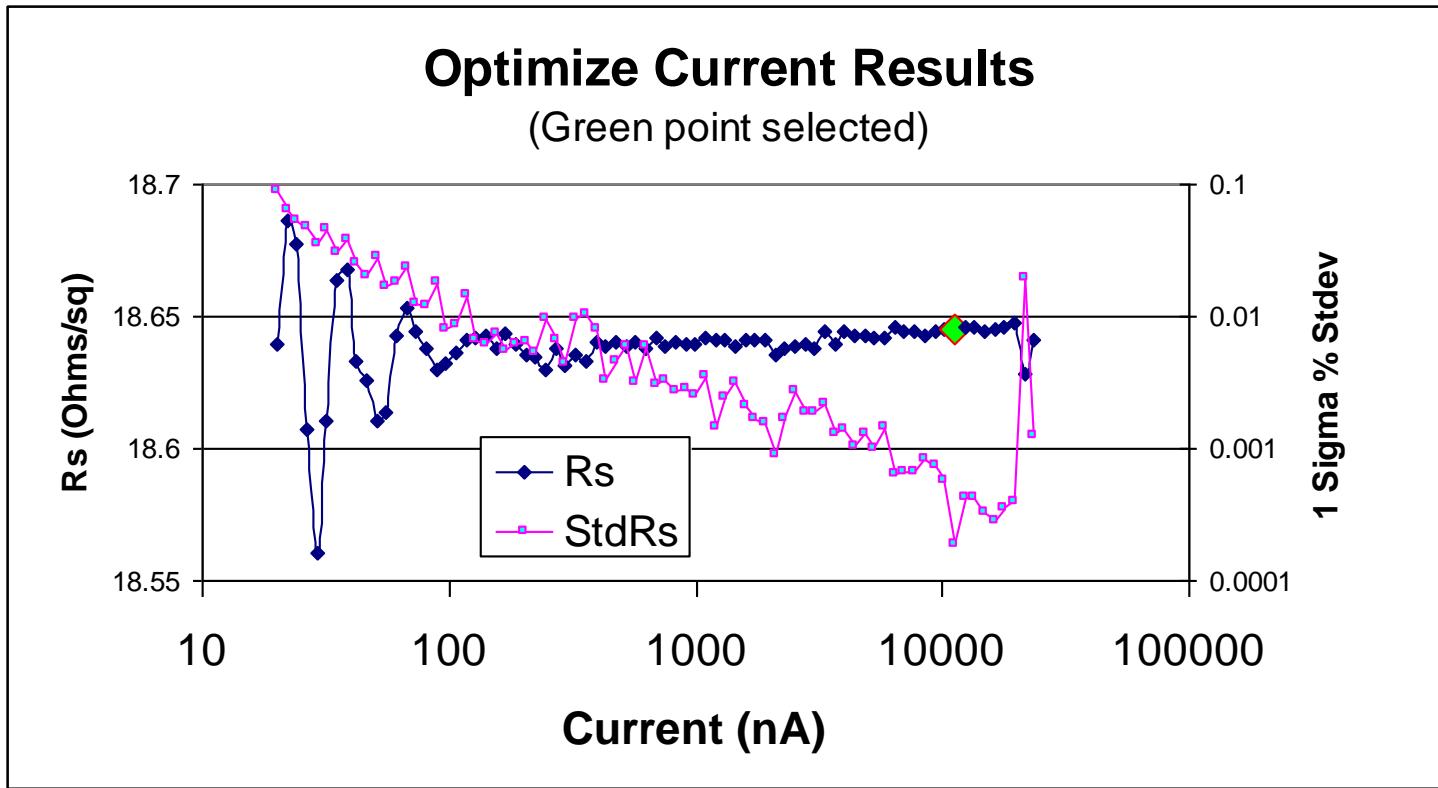


Product Wafer NCRs Mapping & Line Scan



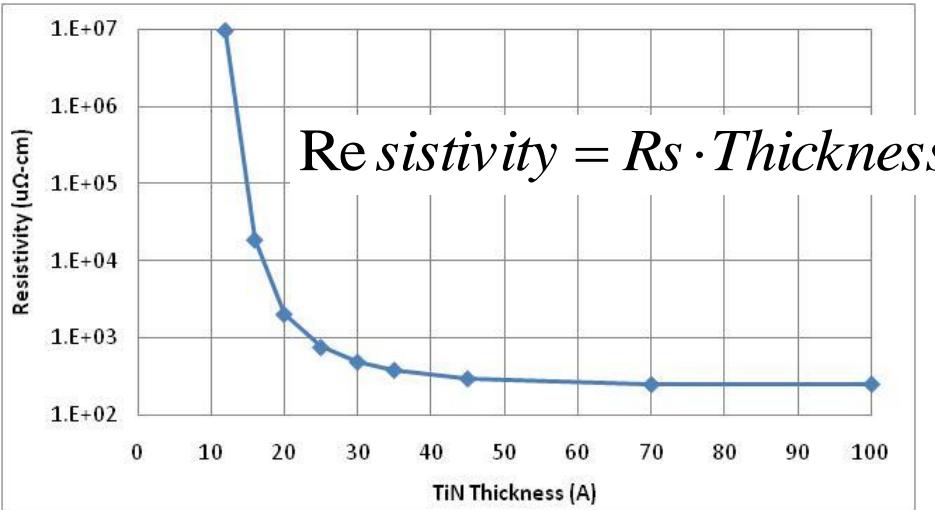
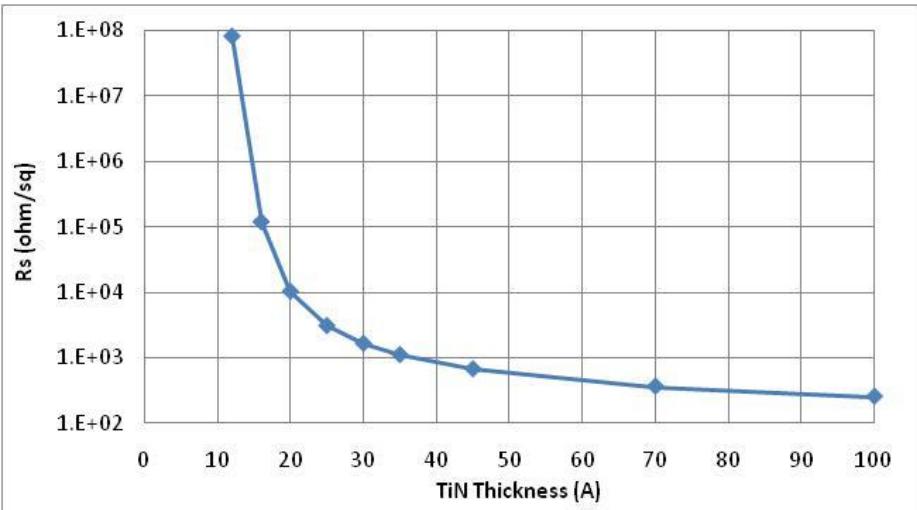
1 Sigma of dynamic 20 repeats <0.5%

Optimize Current Example 1



OC routine picked a good current.

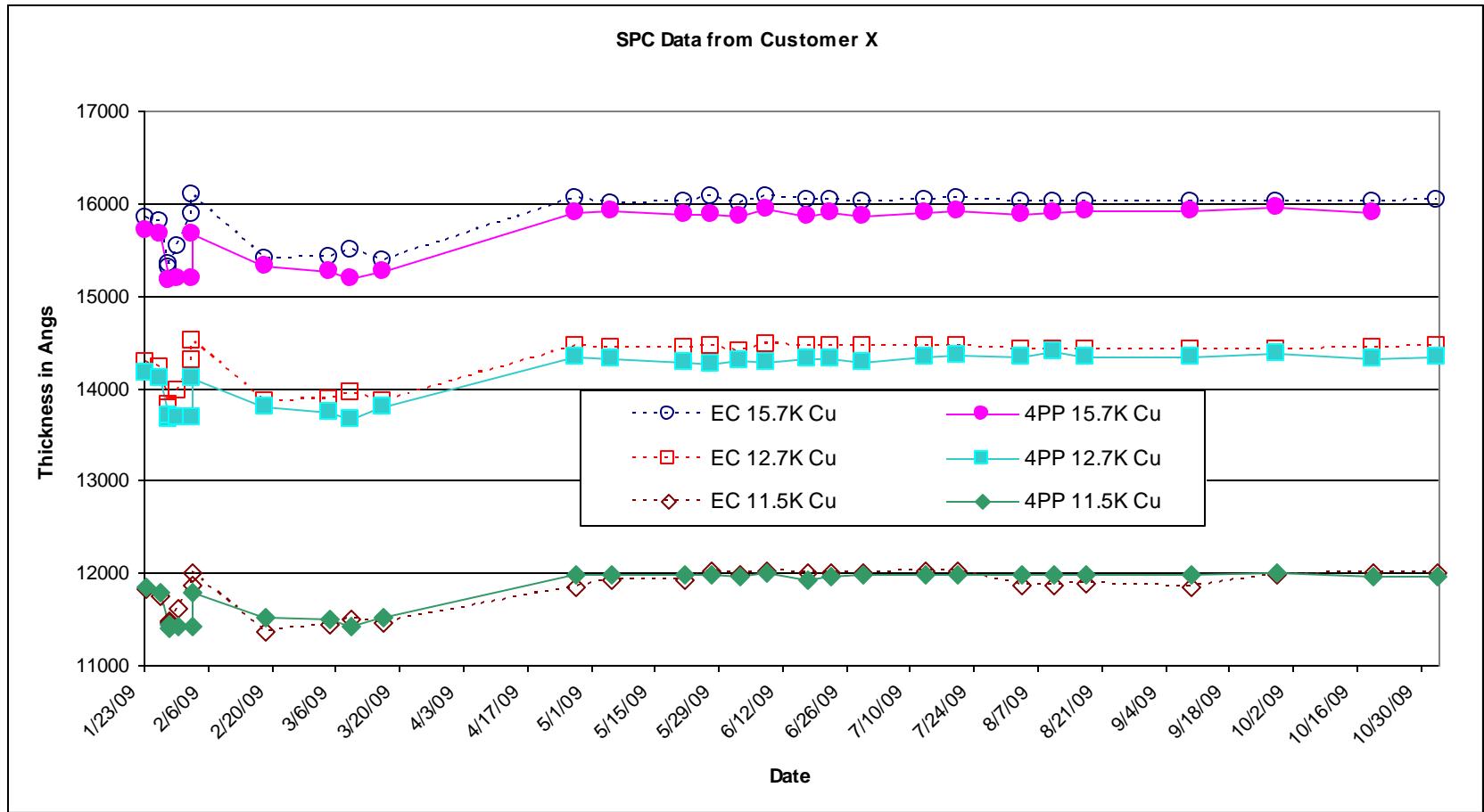
Sensitivity of TiN Film



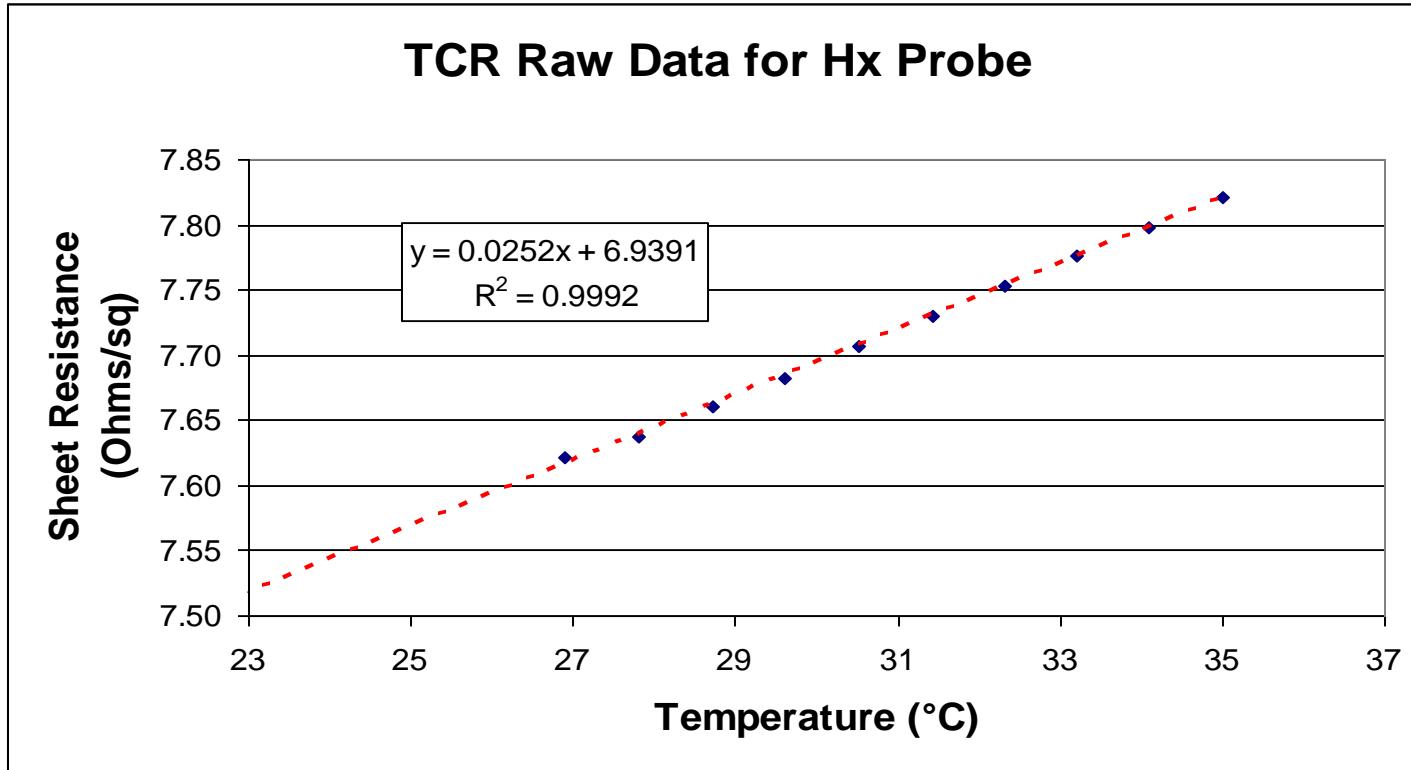
Wafer	slot10	slot12	slot14	slot16	slot18	slot20	slot22	slot24	slot8
TiN Thickness (A)	12	16	20	25	30	35	45	70	100
R_s (ohm/sq)	8.04E+07	1.18E+05	10201	3053	1637	1101	667	360	254
Resistivity ($\mu\Omega\text{-cm}$)	9.65E+06	1.88E+04	2040	763	491	385	300	252	254

- R_s trending up quickly with TiN film thickness decrease
 - Metal resistivity will become big with thickness decrease

Long Term SPC Data for Non Contact Rs

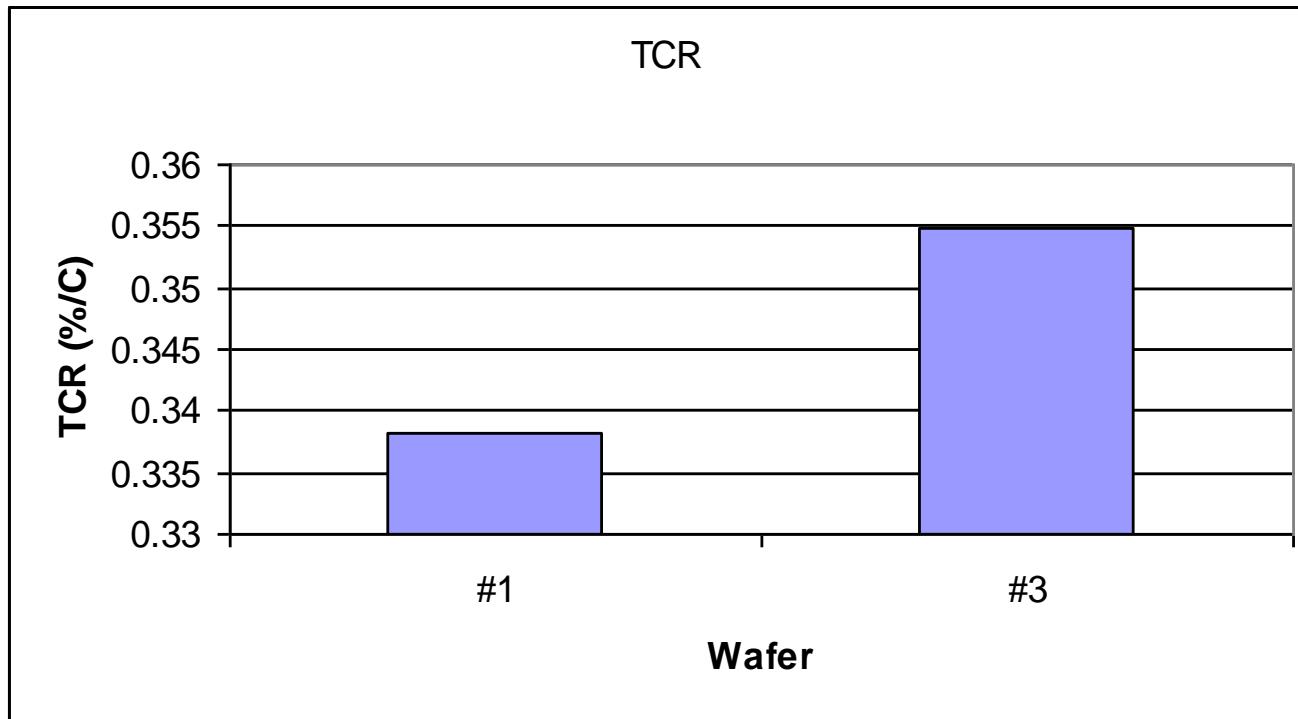


Example TCR Curve

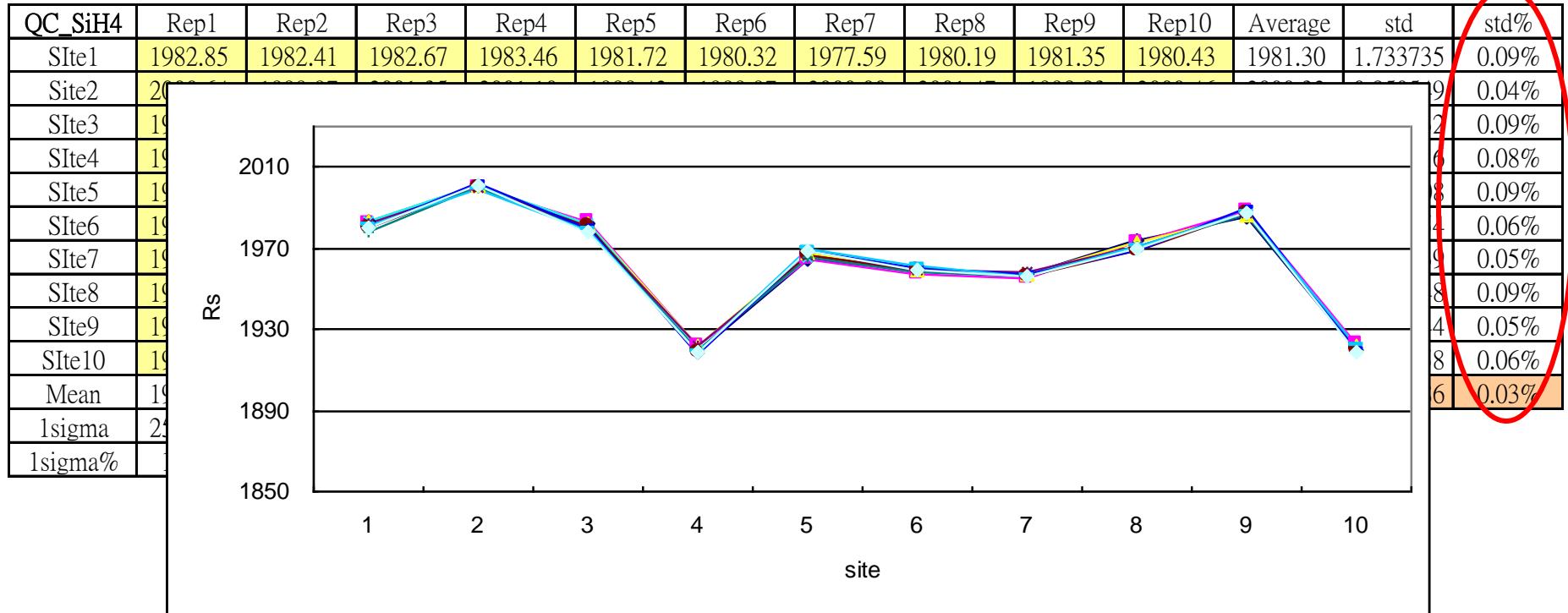


The maximum temperature which can be set in the system is 35 degrees C

RS200 TCR Values of NF19 and NF50 films

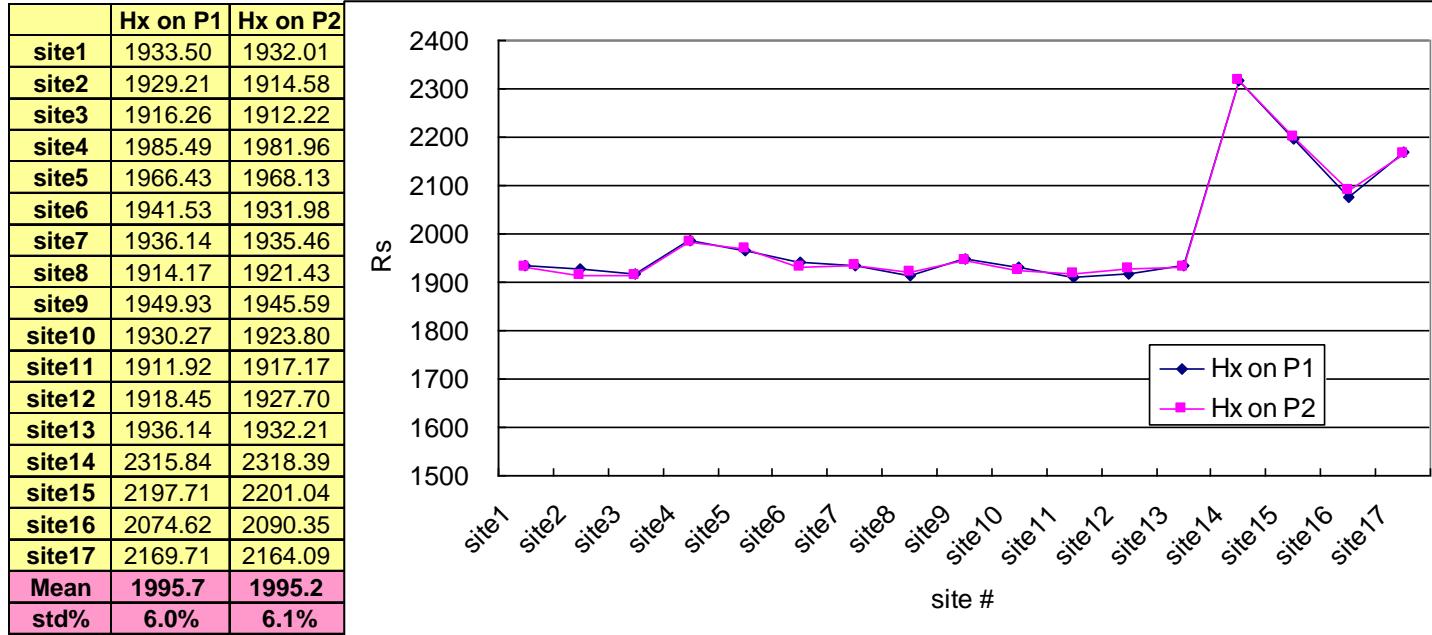


GRR Test Results



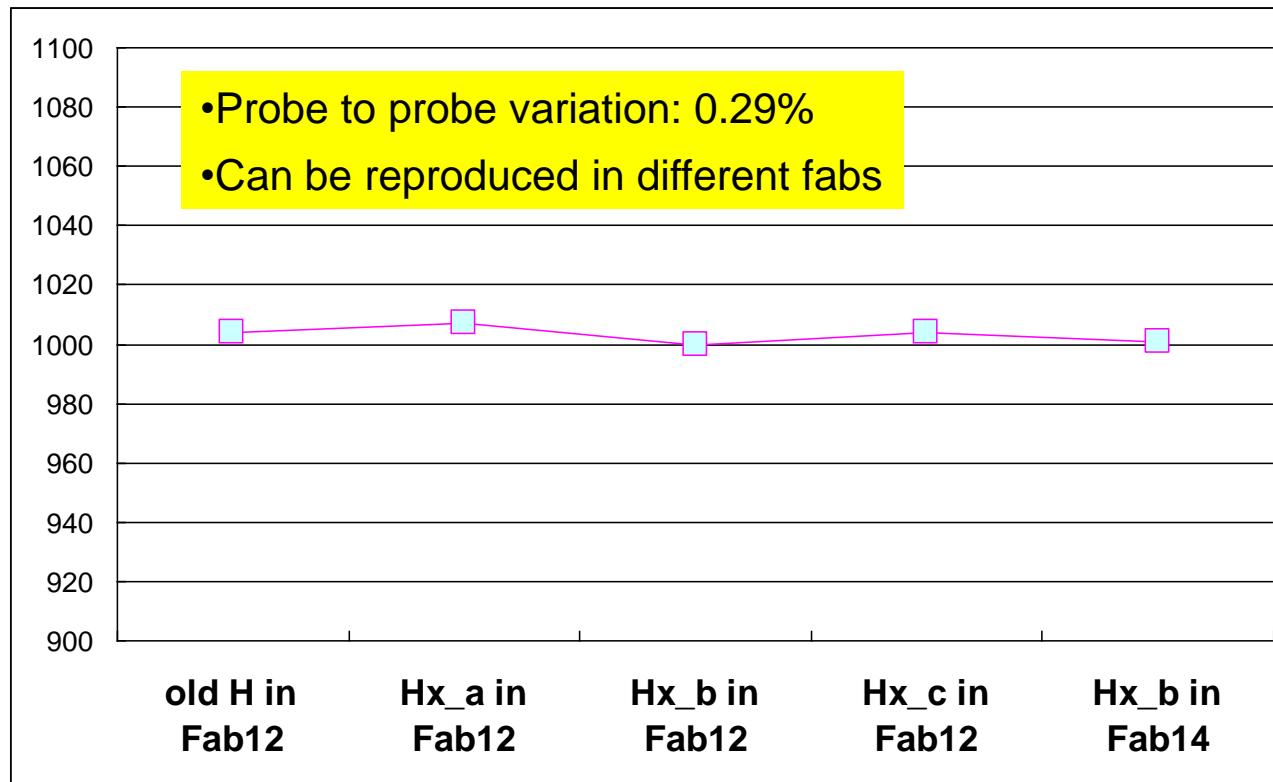
- 10 reps of 10 pts map recipe on QC_SiH4 wafer;
- The std% of 10 reps for all 10 sites is smaller than 0.1%;
- Hx stability test meet TSMC GRR requests

Dual Probe Arm Position Accuracy



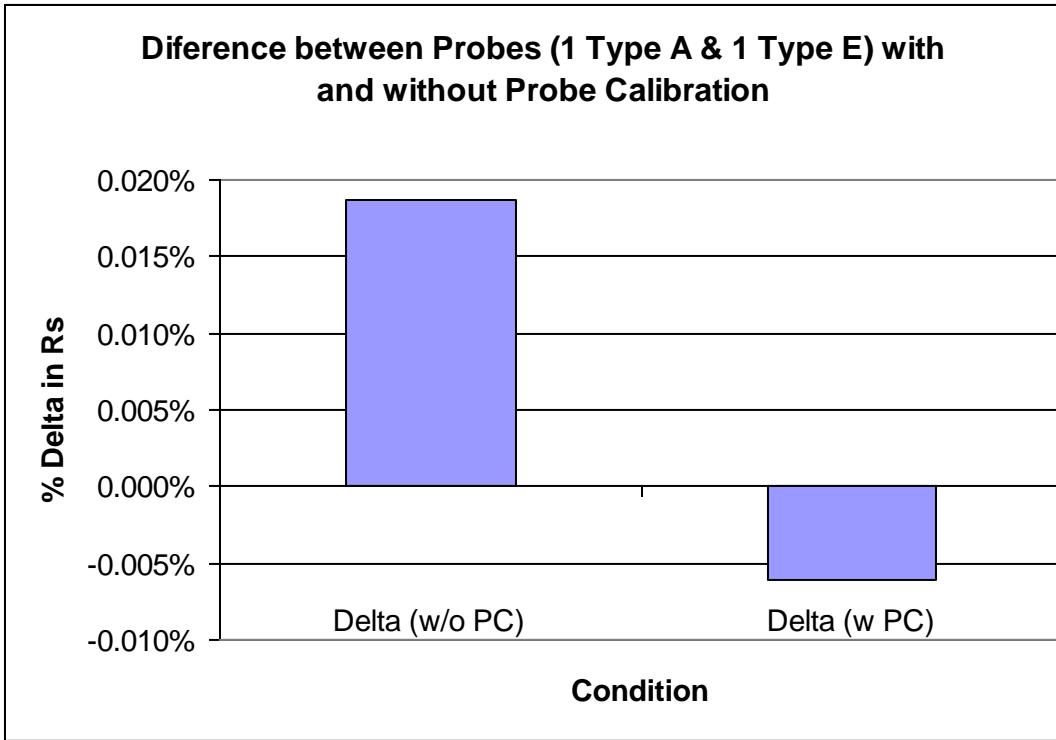
- Hx/H located on P1/P2, tested on same QC_SiH4 wafer with 17pts map recipe;
- No obvious deviation between two results;

Hx Matching Performance



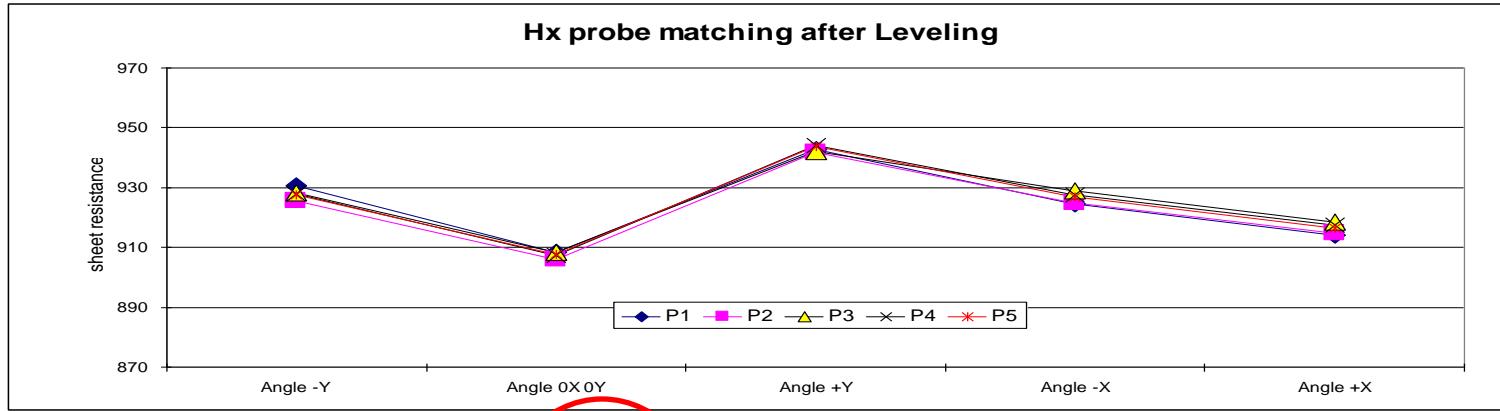
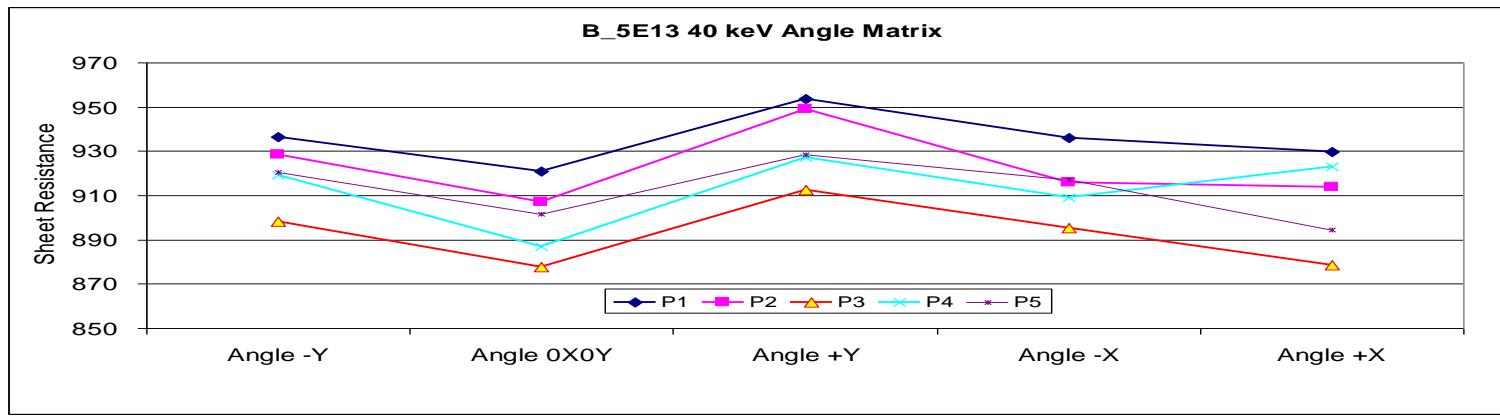
- Different Hx probes reported similar Rs values on QC wafers;
- One Hx probe reported similar Rs values on different tool site;
- Probe to probe variation is smaller than 0.3%.

Probe Calibration



Using probe Calibration reduced the delta between two probes by a factor of 3.

Matching Before & After Leveling of Tips



Map average	Mean	927.88	907.62	942.88	926.58	916.27
	std%	0.19%	0.11%	0.11%	0.20%	0.20%
Map 1sigma	Mean	13.38	13.22	16.21	12.98	14.56
	std%	0.04%	0.05%	0.01%	0.09%	0.06%

Summary

- Improved signal to noise on provides clearer pictures of the process through the Micromap feature.
- Larger signal makes possible post anneal measurements on the TP680.
- In many cases a good correlation to 4PP Rs can be obtained with the TP680.
- The Hx probe for the RS200 systems minimizes leakage effects allows measurements of implants down to 5nm Xj.