

Putting pieces together in the material space

Considerations for integrating Cobalt

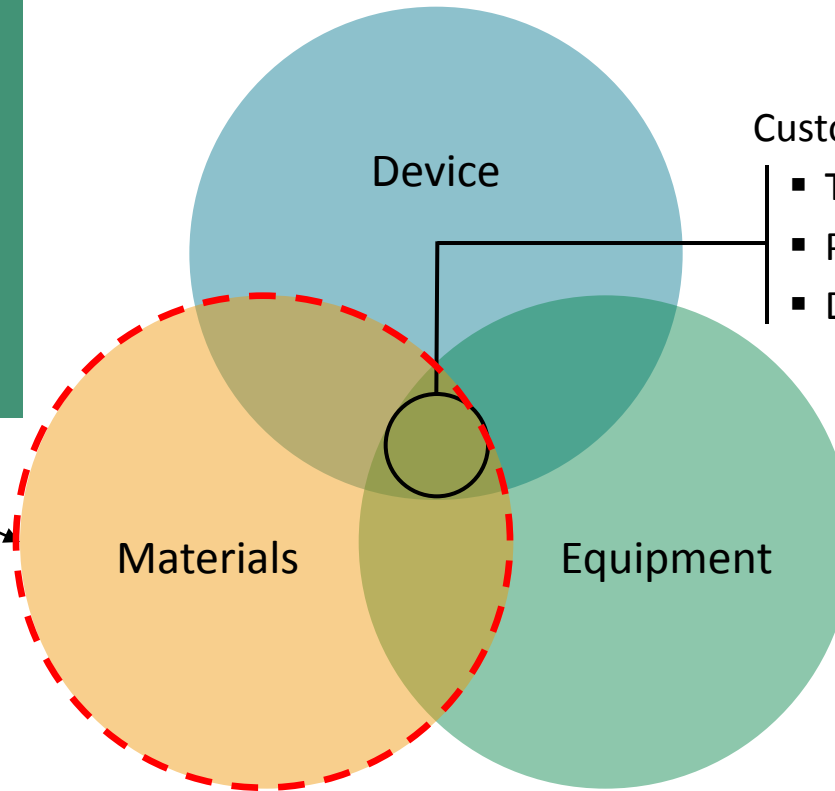
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Increasing Interdependency within Semiconductor Ecosystem

Opportunity to “put the pieces together” in the materials space

- Materials, handling, sensing, and delivery components
- Enabling new paradigms



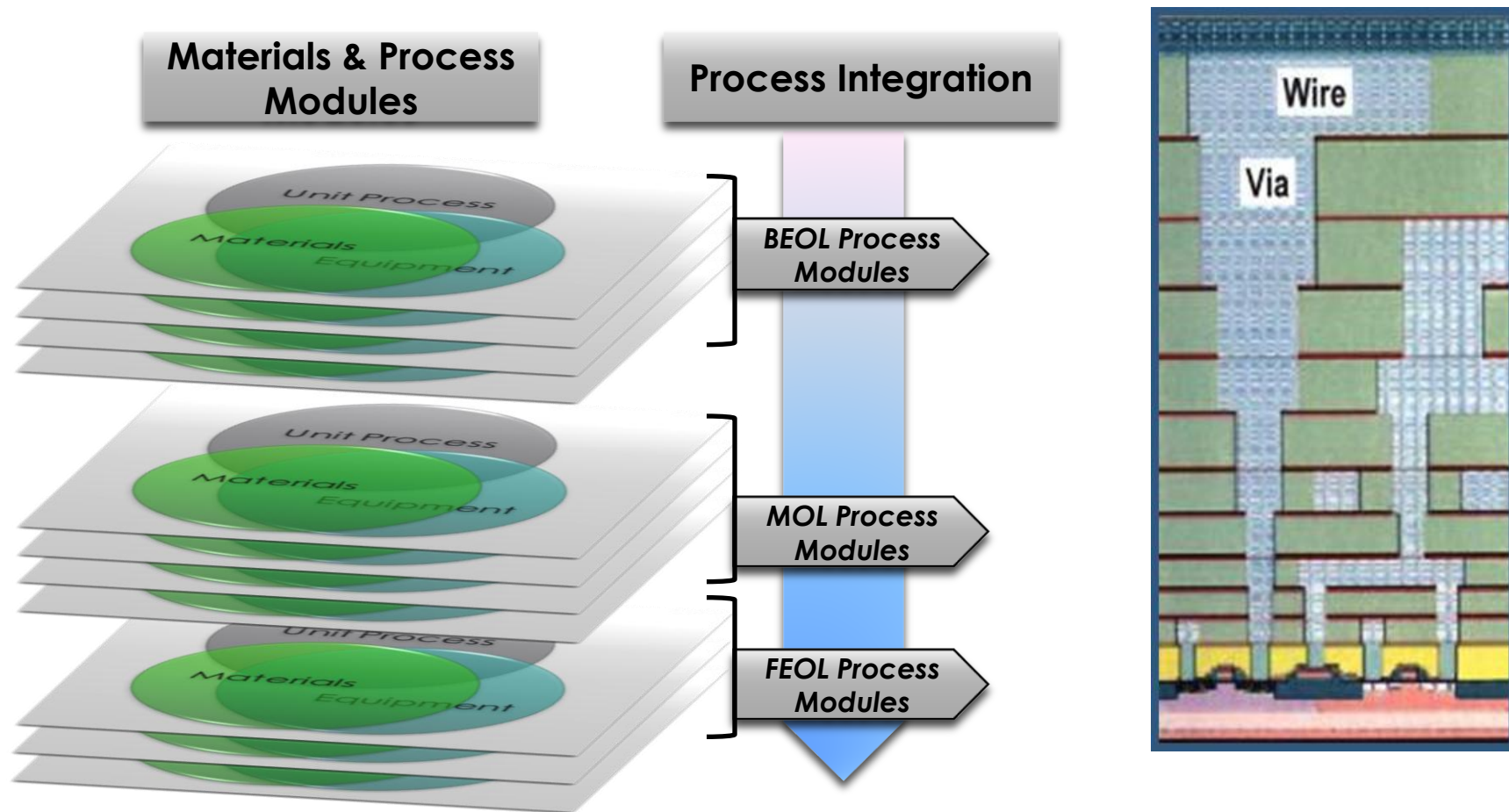
Customers Care About:

- Technology Performance
- Process Yield
- Device Cost

Significant overlap within the Semiconductor Ecosystem

- Increasing need to work together in this space
- Targeted collaborations are key to advanced node challenges

Integrated Stack: Putting Materials Together to Enable Performance



Understanding the integrated stack and material interactions enables tailored solutions that fit customer requirements

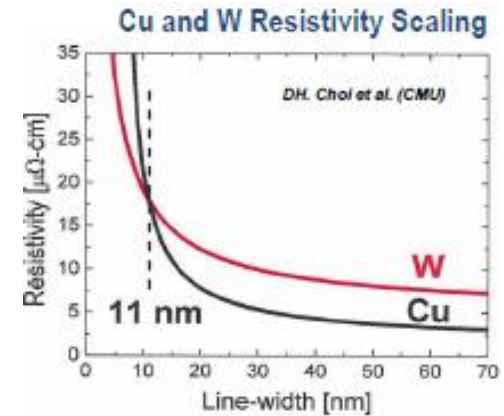
Changes in Metallization

Device Scaling causing interconnect challenges

- Line resistivity increasing due to Cu electron free mean path and film roughness
- Reliability issues due to Cu electro-migration below 20 nm
- High aspect ratio features causing yield issues

Alternative interconnect metals

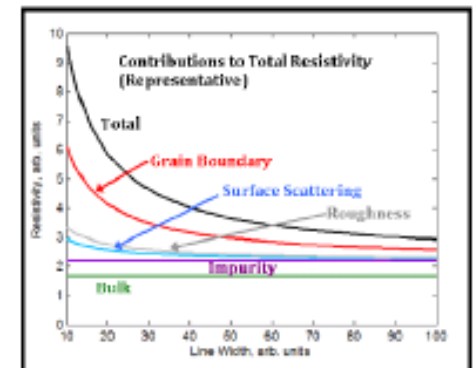
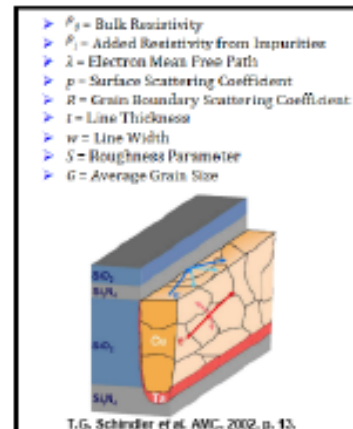
- Al, Co and W are alternatives
 - display better rho vs. Cu <20 nm
- Al displays poor EM performance
- Co displays better EM performance
 - can be deposited by ALD, CVD
 - can be integrated into device?



Mayadas-Shatzkes Model

$$\rho = (\underbrace{\rho_0}_{\text{Total}} + \underbrace{\rho_i}_{\text{Bulk Impurity}}) \left[1 + \underbrace{\frac{3}{8}(1-p)}_{\text{Surface}} \lambda \left(\frac{1}{t} + \frac{1}{w} \right) \cdot \underbrace{S}_{\text{Roughness}} + \underbrace{\frac{3}{2} \left(\frac{R}{1-R} \right) \frac{\lambda}{G}}_{\text{Grain Boundary}} \right]$$

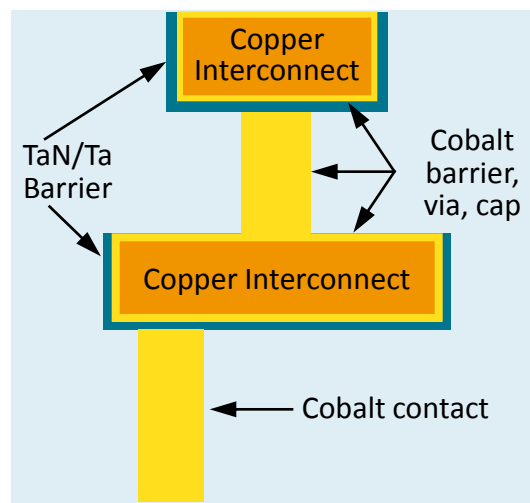
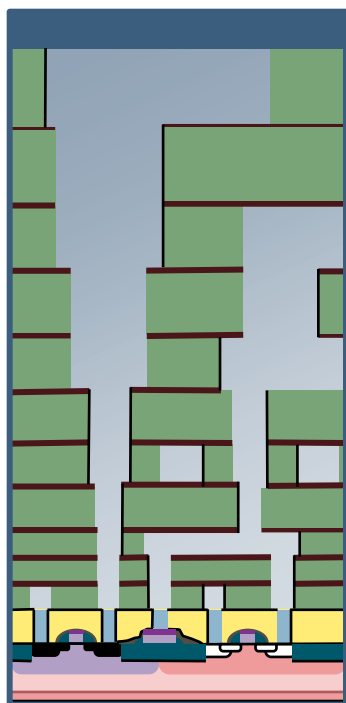
Rosenagel & Kwan, J. Vac. Sci. Tech. B, 22 (2004) 249.



Introduction of Cobalt Drives a New Product Cycle

Copper interconnect resistivity and reliability degrades as linewidth decreases – new metallurgy needed

IC Cross Section



Broad Process Implications

ALD/CVD precursors

Precursor delivery systems

Advanced gas filters

Post CMP clean formulations

Post Etch Clean formulations

Cu and Co plating formulations

Advanced filtration

Advanced fluid handling

Process Implications of Inflection Point Innovations

Cobalt

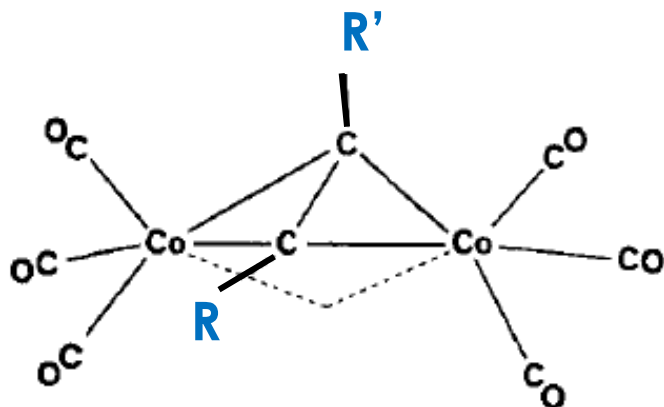
Lithography	Etch	Deposition	Clean	CMP	Implant	Fab Facility
Advanced Photoresist Packaging	Formulated Cleans	Copper Plating	Formulated Cleans	Formulated Cleans	Safe Gas Delivery Systems	Advanced FOUPs
Filtration and Dispense	Gas Filters and Diffusers	CVD and ALD Deposition Materials	Filtration	Filtration	Electrostatic Chucks	AMC Filtration
Gas/Liquid Purification	Gas Purification	Gas Filtration and Purification	Fluid Handling	CMP Cleaning Brushes	Gas Purification	Bulk Chemical Delivery
Reticle Pods	Specialty Materials	Specialty Materials	Containers	Pad Conditioners	Specialty Materials	Wafer Shippers

Entegris solutions enable technology inflection points in semiconductor processes

Cobalt Deposition Overview

- Cobalt CVD has been demonstrated with:
 - Cp_2Co
 - $\text{CpCo}(\text{CO})_2$
 - $\text{Co}(\text{CO})_3\text{NO}$
 - $\text{Co}_2(\text{CO})_8$
 - dicobalt hexacarbonyl tert-butylacetylene: “**CCTBA**” a Co(0) compound
- Very few examples of Cobalt ALD
 - Organometallic reagents and reducing agents (H, Hydrazine, Ammonia)
 - Unclear whether these are true ALD or just pulsed CVD
 - Customer experience has been that it is difficult to get C, O & N free films with published precursors

DiCobalt HexaCarbonyl- η^2 - μ^2 -acetylenes



Ability to enhance Co complexes

- Impact of terminal 'H' towards stability?
- Structure - stability relationship?
- Electronic effects towards stability?

Cobalt Precursor - Abbreviation	R'	R	synthesized	m.p.	b.p.	STA T ₅₀	STA % NVR	Film Growth	NMR	Conducting Films	Deposition Rate
CCTBA	H	t-Butyl	yes	13		138.4	6.8	yes	yes	yes	1
CCTMSA	H	TMSi	yes	31		138.3	5.8	yes	yes	yes	0.95
CCBTMSA	TMSi	TMSi	yes	81		175.6	1.8	yes	yes	yes	<0.75
CCTMSP	Me	TMSi	yes	-		149.6	1.98	yes	yes	yes	0.90
CCTBP	Me	t-Butyl	yes	-		151.4	1.52	yes	yes	yes	1.0

- Terminal 'H' on alkyne reduces thermal stability
- Di-substituted alkynes enhance stability

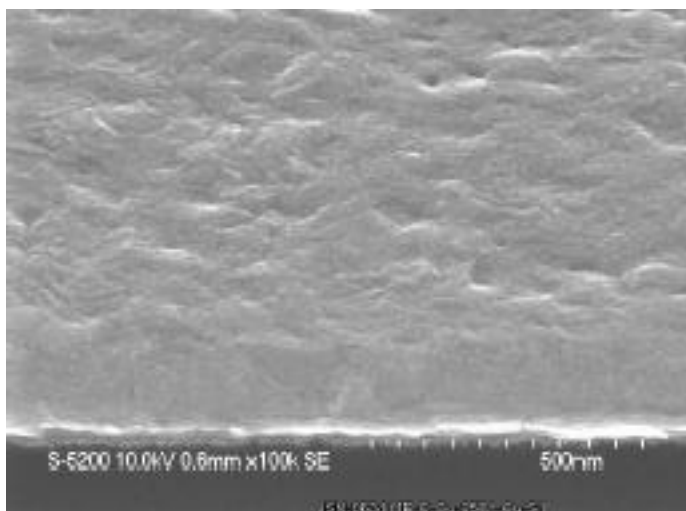
Selectivity Testing

30 Torr, 50 $\mu\text{mole/min}$, 0.5 lpm H_2

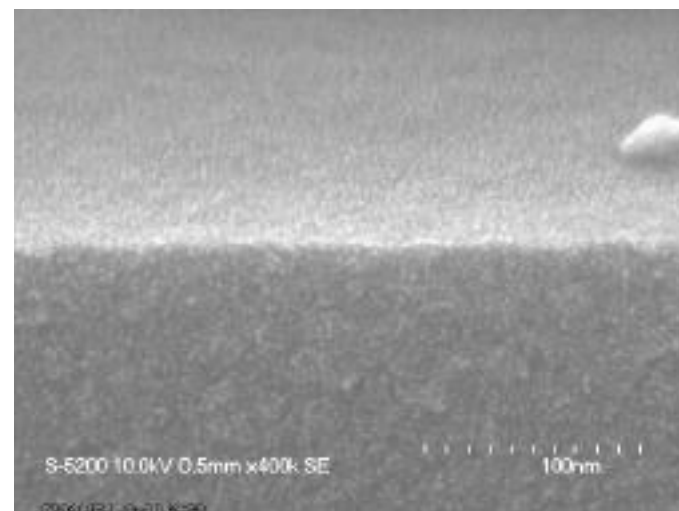
Temperature (°C)	Substrate	Position	Deposition Rate (A/min)
290	SiO_2	C	7.6
	Cu	BL	15.4
250	SiO_2	C	0.24
	Cu	CL	8.1
200	ULK	C	0
		L	0
	Cu	C	11.7
		L	11.3
150	ULK	CB	0
		L	0
	Cu	C	3.6
		B	4.9

Excellent selectivity at ≤ 200 °C

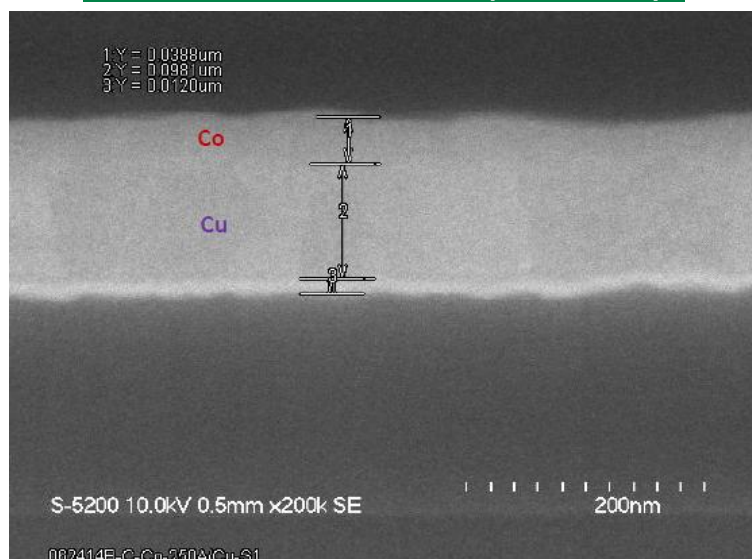
Excellent Selectivity: Cu vs ULK



350 Å Co on Cu (200 °C)



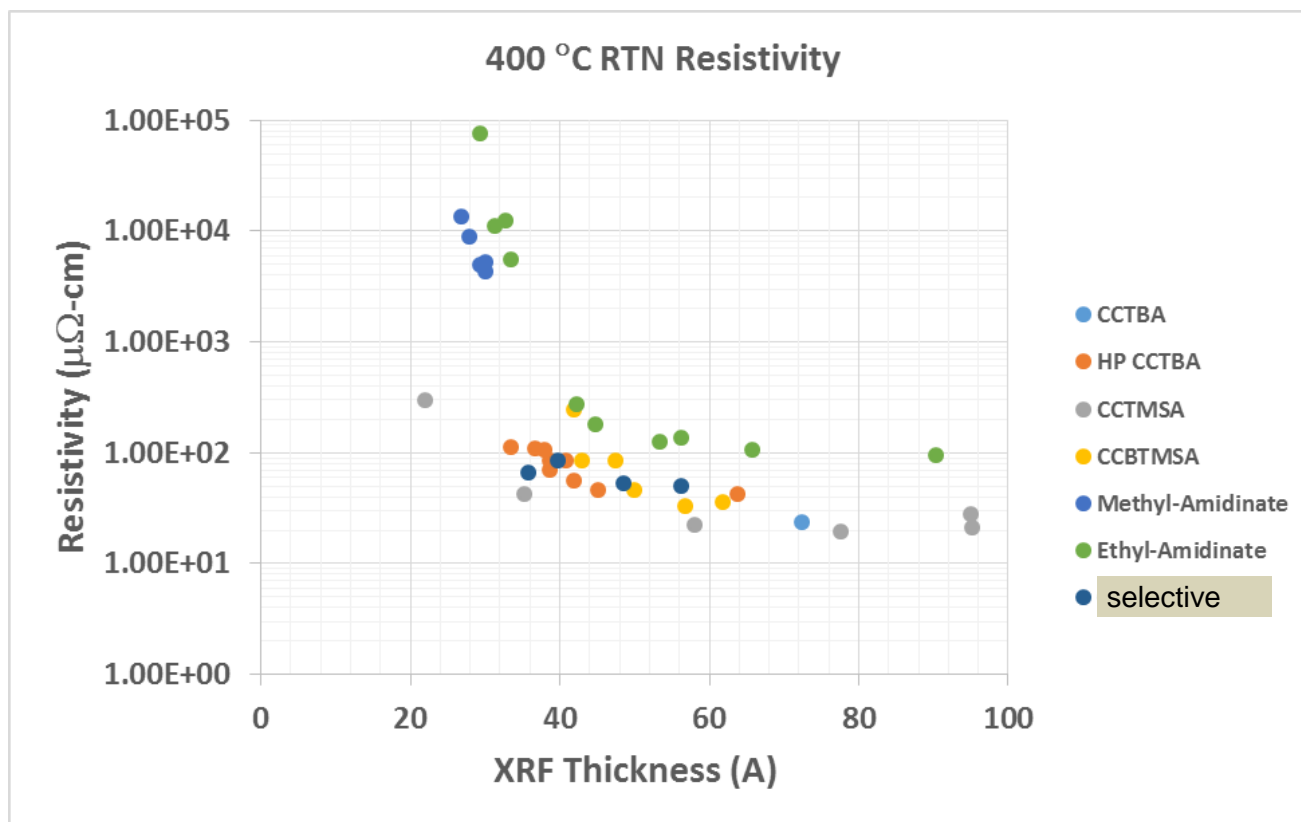
Negligible Co on ULK



	C	N	O	F	Si	Co
ULK, 061414B-L, OACo; TOA = 25	24.7	0.2	48.1	0.6	26.3	0.1
ULK, 061414B-L, OACo; TOA = 45	24.0	0.1	49.1	0.6	26.1	0.1
ULK, 061414B-L, OACo; TOA = 75	23.9	0.2	48.8	0.7	26.2	0.2

Confirmed by angle-resolved XPS

Film Resistivity – Rapid Thermal Anneal at 400°C



Selective Co Growth on Cu

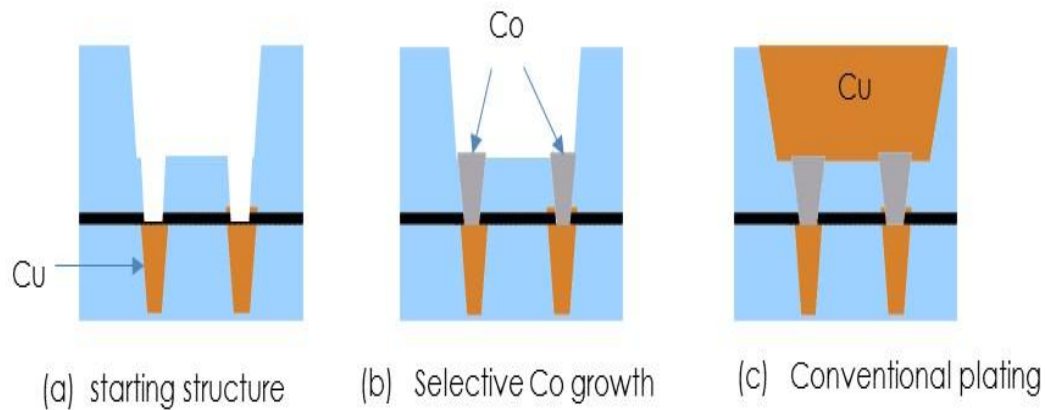
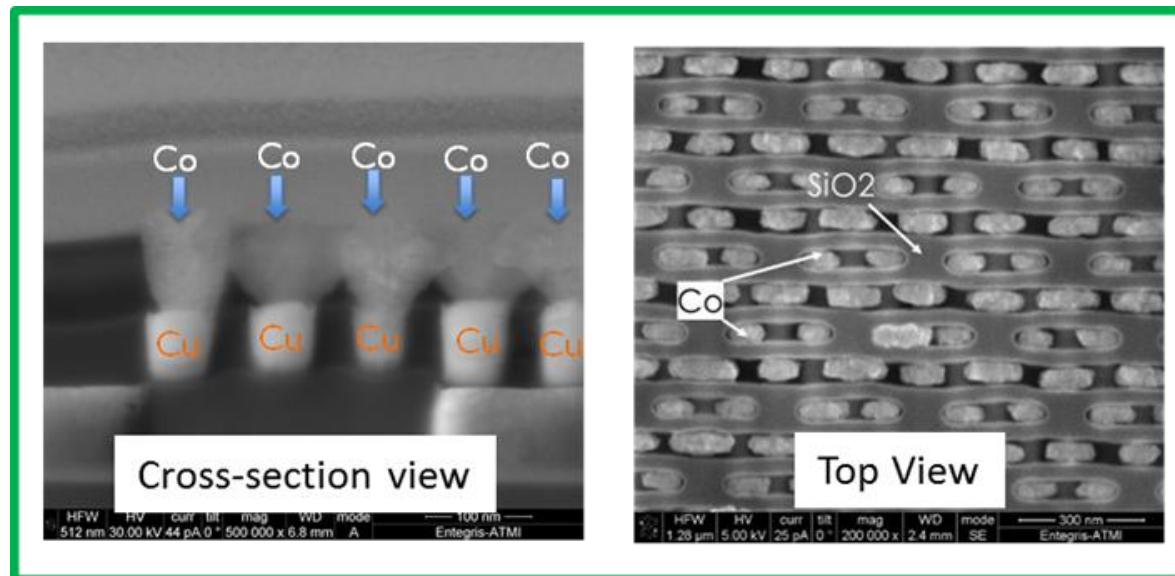
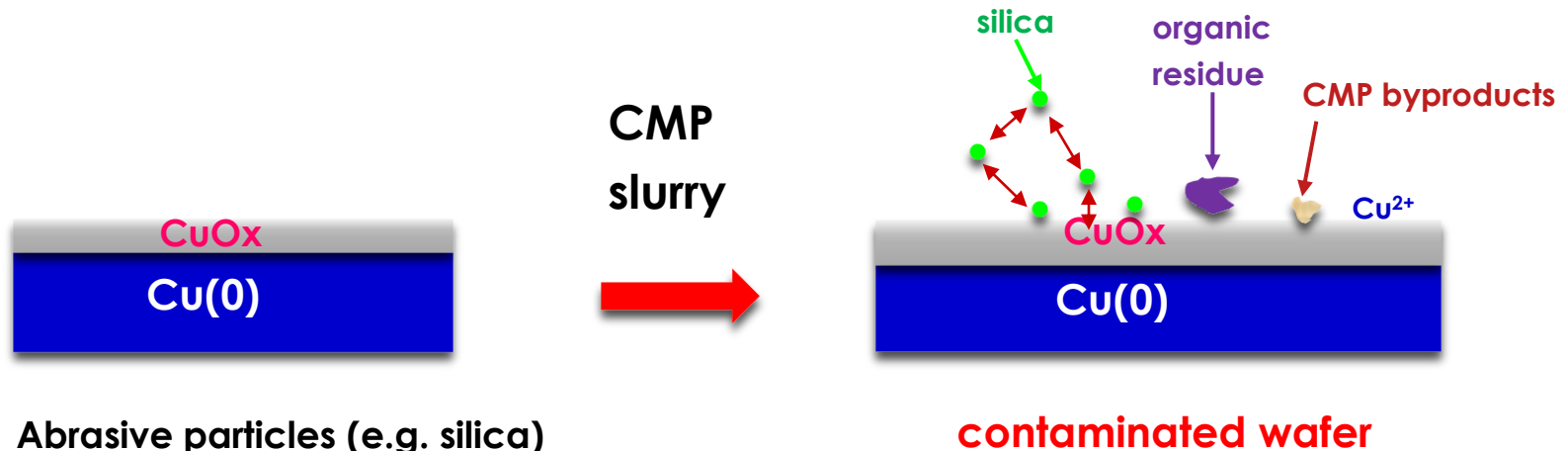


Fig. 2 Co selective growth integration



Traditional pCMP clean for copper



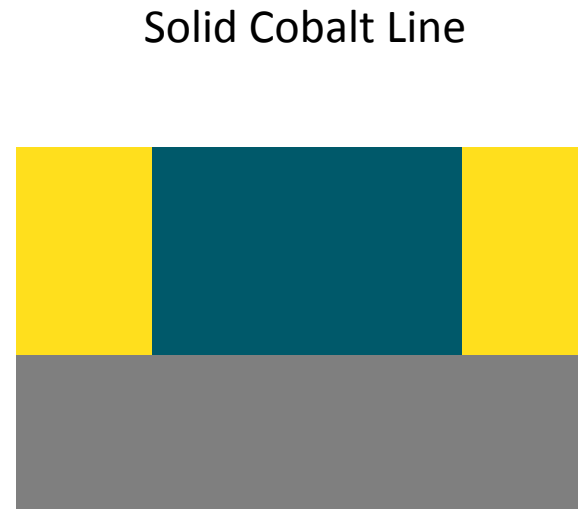
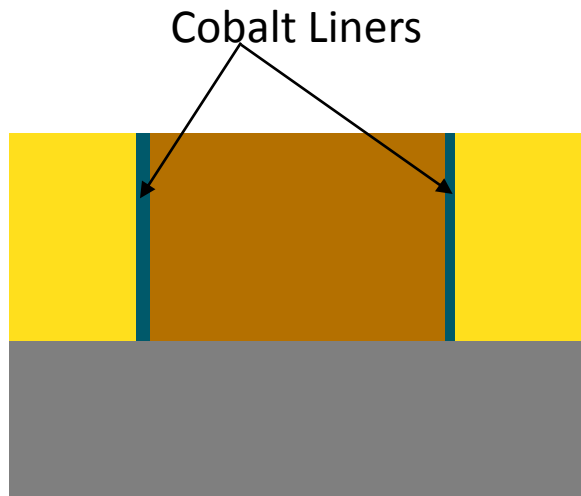
- Abrasive particles (e.g. silica)
- RR Promoters (aminoacids)
- Corrosion inhibitors (BTA, SHA)
- Oxidizers
- CMP pad and brush debris

Particles adhesion mechanisms

- **Physisorption** (van der Waals attraction – increases with PS)
- **Electrostatic** attraction or repulsion (zeta potential)
- **Chemisorption** (chemical reaction particle-surface, e.g ceria/TEOS)
- **Capillary condensation**

Changes due to Cobalt

■ Two Type of potential post polish Cobalt Structures



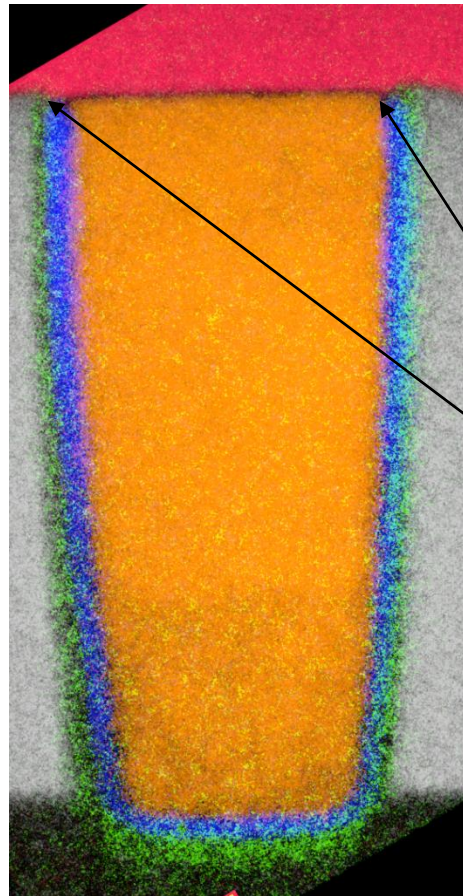
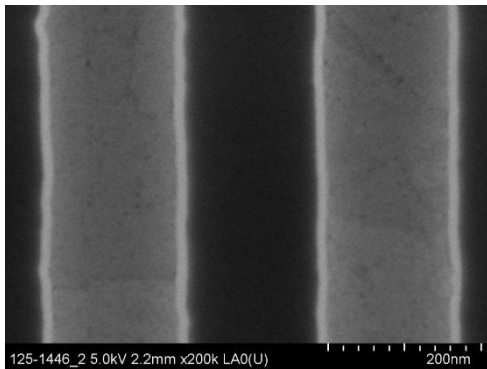
The Cleaning will be very different for these two structures

- Reduce the etch of Cobalt and the cobalt ions in the cleaning solution
- Lower Galvanic effects between the copper and cobalt
- Reduce the cobalt ions in the cleaning solution during cleaning
- Controlled etch to clean the surface.

NT 3300 cleaned copper lines with cobalt liner

TEM – EDX Element Map (overlay)

Orange – Cu
Blue – Co
Green – Ta
Red – C



Co liner touches Cu Barrier in this example with no gap between them.

This example needs great Cu cleaning and low Co Etching.

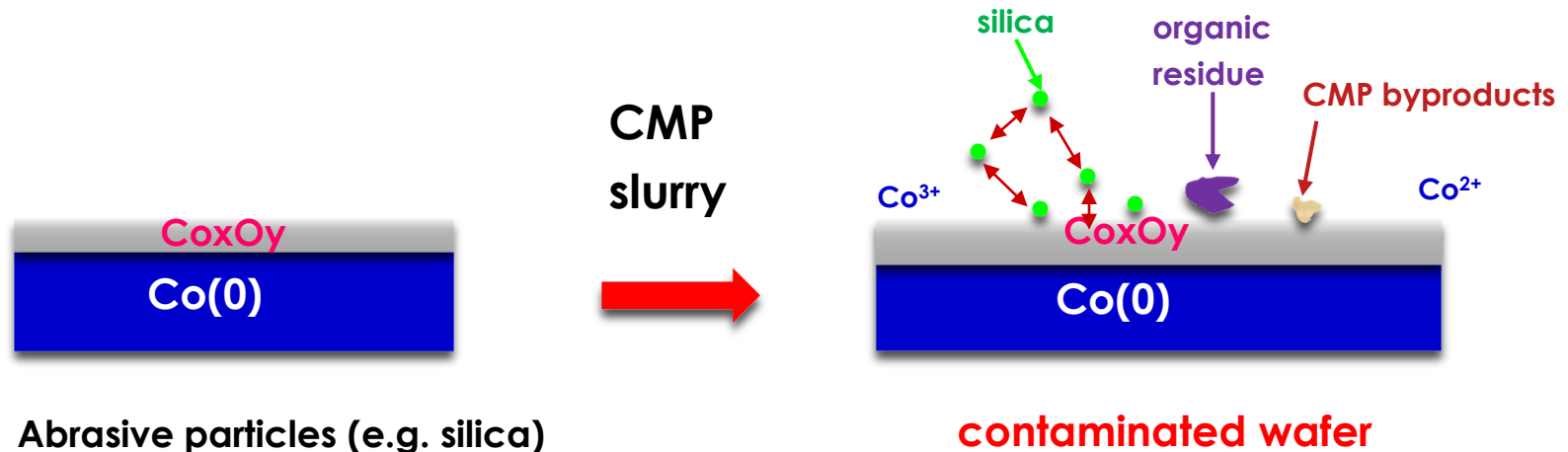
Key Requirements of the pCMP Cleaner

- No Etching of the Co Liner
- Good Cleaning of the Copper lines
- Low Cu/Co Galvanic etching

Cu Lines with Co/Ta Barriers

Top Down SEM

Advanced pCMP clean for Solid Co Lines



- Abrasive particles (e.g. silica)
- RR Promoters (aminoacids)
- Corrosion inhibitors (BTA, SHA)
- Oxidizers
- CMP pad and brush debris

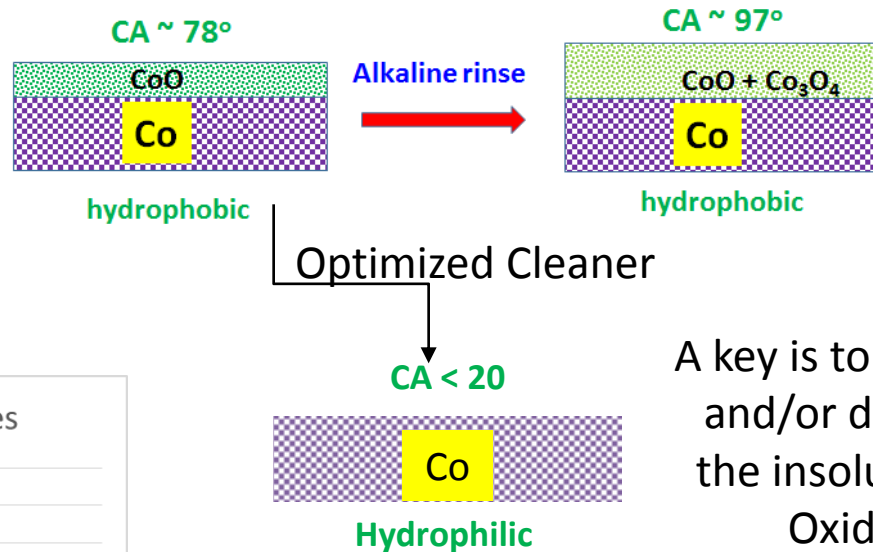
The ratio of the Co^{3+} to Co^{2+} ions is very important

Particles adhesion mechanisms

- **Physisorption** (van der Waals attraction – increases with PS)
- **Electrostatic** attraction or repulsion (zeta potential)
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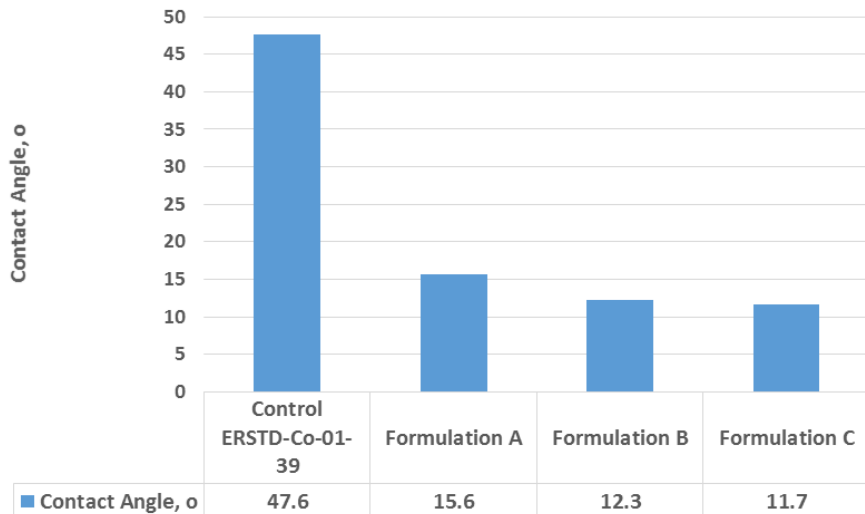
Cobalt pCMP Cleaner of Co solid lines

After pCMP cleaning films need to be clean with no to thin oxide layers and no Co_3O_4 ; plus, be hydrophilic.



A key is to prevent and/or dissolve the insoluble Co Oxides

Contact Angle Measurements on Co Substrates



Small Contact Angle (CA) indicates good clean surfaces and hydrophilic.

Formulation A, B & C are developmental cleaners that have been used to clean Cobalt films. This same technology can be applied to pCMP.

SUMMARY

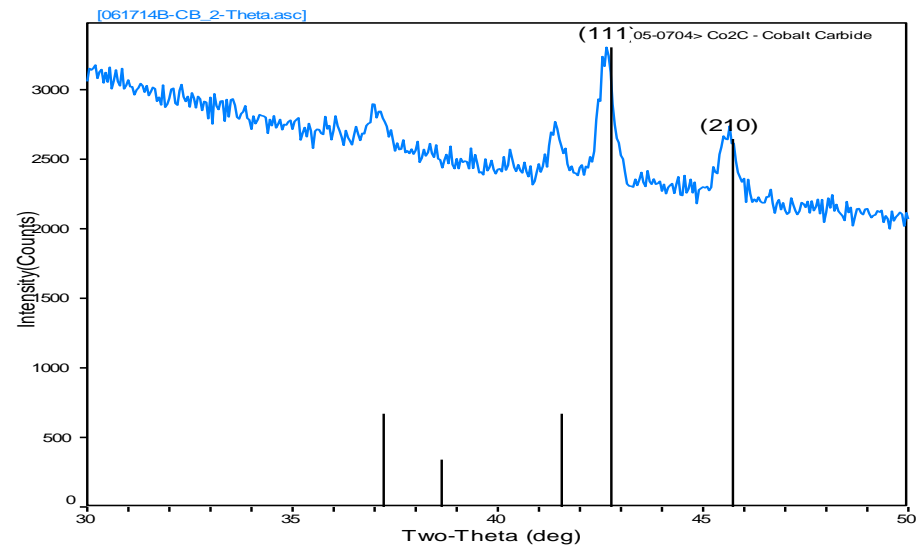
- Integration of Cobalt requires new understanding of
 - Stability /selectivity of Co precursor
 - Surface preparation prior to deposition
 - Co compatible post CMP & post etch cleans
- Mechanism of deposition & residual impurities in cobalt film influence effectiveness of subsequent cleaning steps



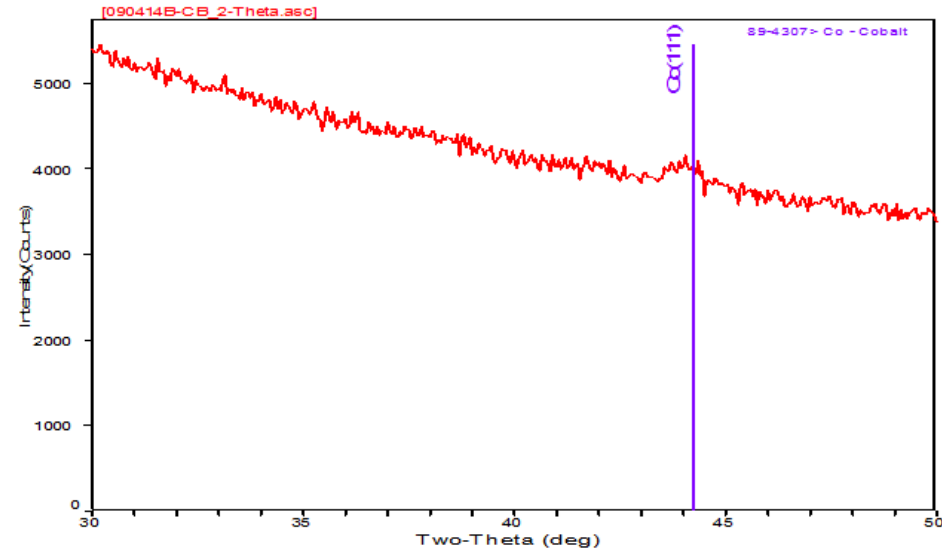
Deposition Conditions

- 150 - 290 °C deposition temperatures
- 50 -150 $\mu\text{mole/min}$ delivery
- 10 - 30 Torr pressures
- 110 - 130 °C vaporizer temperature
- 90 °C chamber temperature
- 0.5 – 3.0 lpm H_2 co-reactant
- 5 - 30 minutes
- Substrates: ULK, PVD Cu & TiN

Film Composition Varies with Substrate and Deposition Temperature



- Film deposited on SiO₂ at 250 °C
- XRD shows orthorhombic Co₂C peaks



- 120 Å Co deposited on Cu at 150 °C
- XRD now shows Co (111) peak